Total No	of Questions: 8	SEAT No. :				
P270	[6003]-348	[Total No. of Pages : 2				
T.E. (Computer Engineering)						
SYSTEM PROGRAMMING & OPERATING SYSTEM						
(2019 Pattern) (Semester-I) (310243)						
Time: 2	Housel	(Mars Marks - 70				
	ons to the candidates: 0	Max. Marks : 70				
1)	Attempt Q.L. Q.2. Q.3 Q.4, or Q.5 or Q.6, Q.7 or	0.8.				
2)	Figures to the right indicate full marks.	2.00				
3)	Neat diduram must/be drawn wherever necessary.	•				
4)	Assume spitable data if necessary.	, 6°				
	C 101	S				
Q1) a)	Explain "General loading scheme (using	suitable diagram)" with				
	advantages and disadvantages?	جي [9]				
	3	5				
b)	Cive complete design of Direct Linking Load	ler? [9]				
	S	3				
	Km)					
Q2/(a)	Give complete design of Absolute Loader wi	th suitable example? [9]				
What is the need of DLL? Differentiate between Dynamic and static link-						
	What is the need of DLL? Differentiate between ing?	191 6				
	Cr 10/2					
		200				
03) a	Explain the following types of Schedulers.	2101				
	Spes of senedulers.	×131				
	i) Short Term	- ~ ~ ~ ~ ~ ~ ~ ~ ~ ~ ~ ~ ~ ~ ~ ~ ~ ~ ~				
	5.	1,30				
	/ii) Long Term	VO)				
	Medium Torm	1, 70.				
	Explain the following types of Schedulers. i) Short Term Long Term Medium Term Explain seven state process model with diagrams between Five state process model. & Second	0,3				
b)/	Explain seven state process model with diagram	m? Atso explain difference				
/	between Five state process model & Seven	total and the total				
/	state process moder & Seten s	state process model? [8]				
	OR	~				
	between Five state process model & Seven s OR	•				
	,9,					
	ري. د کې د					
	~	B.T.O.				
	'O ₂	P.T.O.				

			~		
Q4) a)	Draw Gantt chart and calculate Avg Arrnaround time, Avg. waiting time				
	for the follow	wing process using	SUF non preem	ptive and round rob	in
	with time quantum 0.5 Unit				
į	Process	Burst Time	Arrival Time		
1	PI	2 07	10		
	P2	1,19	10		
	P3	V 2.	11		
Į	P4	1, 61	12		
b)	What is mon	n by Threads, Exp	lain Thread lifec	ycle with diagram	
	detail	3.		(1	81
	1000	9		103	
Q5) a)		note on following w		66	91
	_ ~	ore ii) Monitor iii) M		9'	
bt				e, deadlock detectio	
	deadlock recovery with example? [9]				
^		C	OB O		
0	, ,	^	9, 5		
Q6) p		icer Consumer prob	lem & Dining Phi	losopher problem wi	th
	solution?	,0,	3	[9	9]
1	What is deadl	ock? State and expl	amithe conditions	for deadlock, Expla	in
	them with exa	imple?	V	[9	
		(1)			1
Q7) a)	Consider pag	ge sequence 2, 3, 2	2, 1, 5, 2, 4, 5, 3	, 2, 5, 2 and discus	130
,	working of fo	ollowing page repla	cement policies A	Iso count page faint	S.
	(use no. of F	rames = 3)		× 18	1
	i) FIFO	20.		V 82, 18	
	ii) LRU	· 65.		0, 0.0	
b)	, , ,				
	OR O				
Q8) a)		note on following w	ith diagram	€. [8	1
	i) VM with Paging				
		h Segmentation	CA TO)	
b)	Explain Page	Table structure and	Invelted page Ta	ble? [9	1
			25		
		K K	Invelted page Ta		
		0 0	2000		
[6003]-3	348	2	S		
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