Total No. of Questions: 8]		of Questions : 8] SEAT No. :			
P-5	58	[Total No. of Pages : 2			
		[6004]-493			
B.E. (Computer Engineering)					
High Performance Computing					
(2019 Pattern) (Semester - VIII) (410250)					
		Hours] [Max. Marks: 70			
Instr	Instructions to the candidates:				
	<i>1</i> )	Answer Q.1 or Q.2, Q.3 or Q.4, Q.5 or Q.6, Q.7 or Q.8.			
	<i>2</i> )	Neat diagrams must be drawn wherever necessary.			
	<i>3</i> )	Figures to the right indicate full marks.			
<i>Q1</i> )	a)	Explain with diagram One-to-all broadcast on an eight-node ring with			
	0	recursive doubling technique. Node 0 is the source of the broadcast.			
	•	Also Explain all to one reduction with node 0 as destination. [7]			
	b)	Explain in detail Blocking and Non-Blocking Communication Using			
		MPI. [6]			
	c)	Write a short note on prefix-sum operation. [4]			
		OR C			
Q2)	a)	What is all to all broadcast communication operation? Explain all to			
		all broadcast on an eight node ring with step wise diagrams. (Show			
		first two steps and last communication step). [7]			
	b)	Explain scatter and gather communication operation with diagram. [6]			
	c)	Explain circular shift operation? [4]			

- Q3) a) Explain parallel Matrix —Matrix multiplication algorithm with example? [7]
  - b) Explain different performance Metrics for Parallel Systems. [6]
  - c) Explain Minimum Execution Time and Minimum Cost Optimal Execution Time. [4]

OR

a)	What is granularity? What are effects of granularity on performance parallel systems?	e of [ <b>7</b> ]
b)	Explain various sources of overhead in parallel systems?	[6]
c)	Explain "Scaling Down (downsizing)" a parallel system with example 1.	ple. [4]
a)	What is CUDA? Explain different programming languages suppor CUDA. Discuss any three applications of CUDA.	t in [8]
b)	Describe processing flow of CUDA-C program with diagram.	[6]
c)	Explan the following terms in CUDA: device, host, device co Kernel.  OR	ode, [4]
a)	Explain CUDA memory model. Discuss thread hierarchy.	[8]
b)	kernel for addition of two vectors and explain how it will calculate addit	
c)	What is a Kernel in CUDA? What is kernel launch? Explain argume that can be specified in a Kernel launch.	ents [4]
a)		
b)	Explain Parallel Depth First Search algorithm in detail?	[6]
c)	What is Kubernets? Explain its features and applications.	[4]
a)	Write short notes on :	[8]
	i) Parallel Merge sort	
	ii) GPU applications	
b)	What are the issues in sorting on parallel computers? Explain was appropriate example?	vith [ <b>6</b> ]
c)	Explain parallel BFS algorithm in brief.	[4]
	b) c) a) b) c) a) b) c) a) b) b)	parallel systems?  b) Explain various sources of overhead in parallel systems?  c) Explain "Scaling Down (downsizing)" a parallel system with example system of CUDA. Discuss any three applications of CUDA.  b) Describe processing flow of CUDA-C program with diagram.  c) Explain the following terms in CUDA: device, host, device concentration.  b) What is block dimension and grid dimension in CUDA? Write a CU kernel for addition of two vectors and explain how it will calculate additionable using threads.  c) What is a Kernel in CUDA? What is kernel launch? Explain argumenthat can be specified in a Kernel launch.  a) Explain odd-even transportation in bubble sort using paraformulation. Give one stepwise example solution using odd-extransportation.  b) Explain Parallel Depth First Search algorithm in detail?  c) What is Kubernets? Explain its features and applications.  OR  a) Write short notes on:  i) Parallel Merge sort  ii) GPU applications  b) What are the issues in sorting on parallel computers? Explain wappropriate example?

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