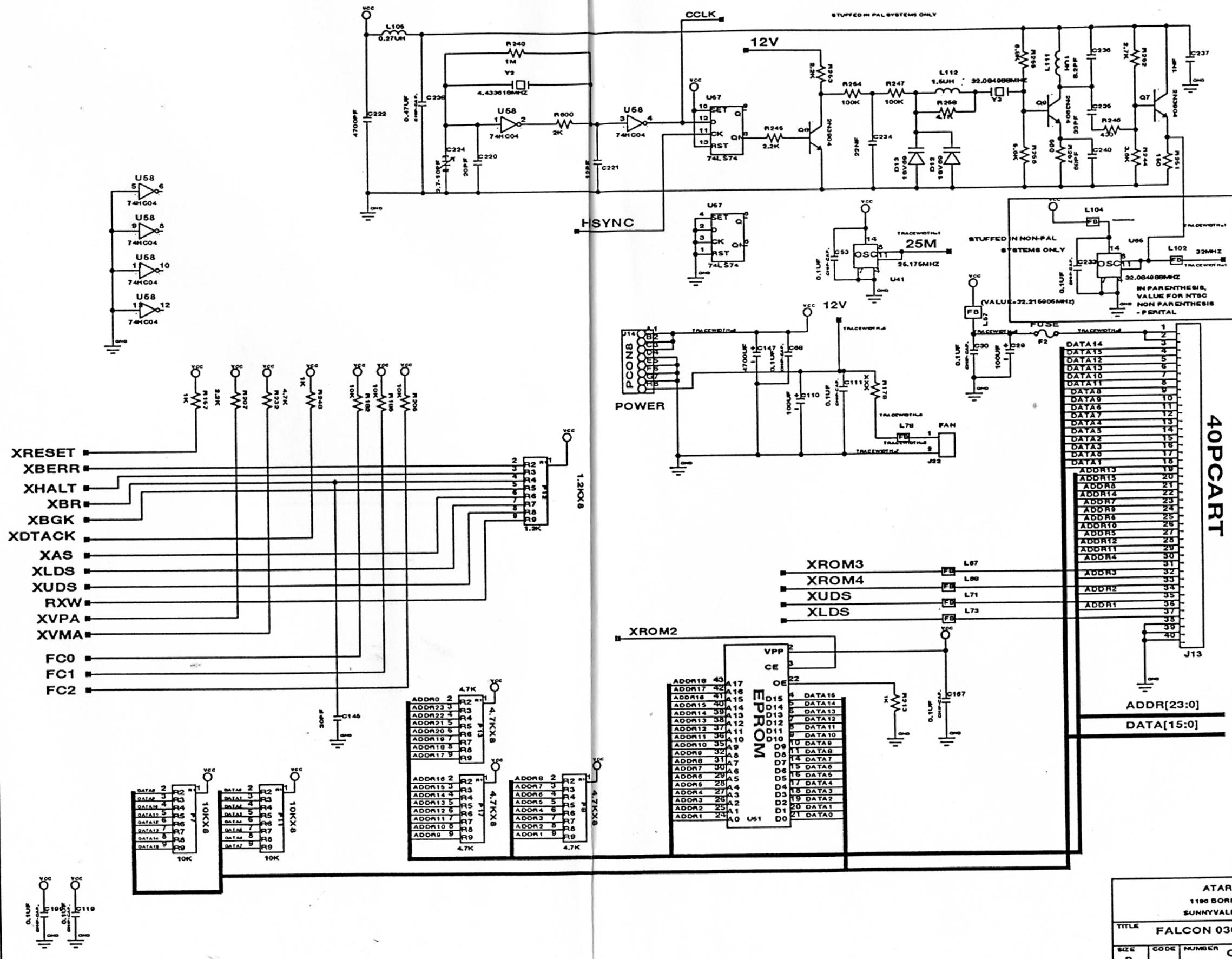
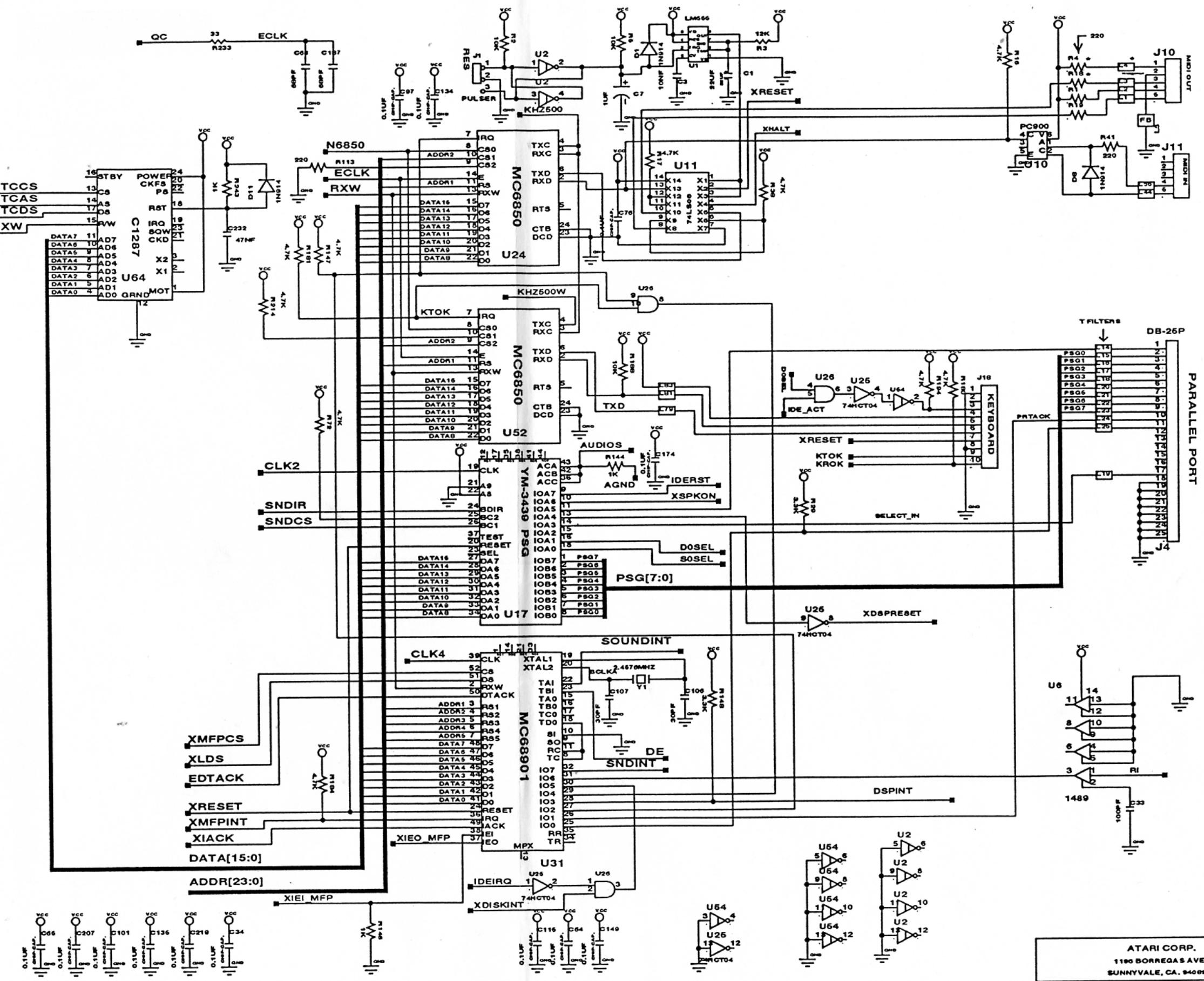


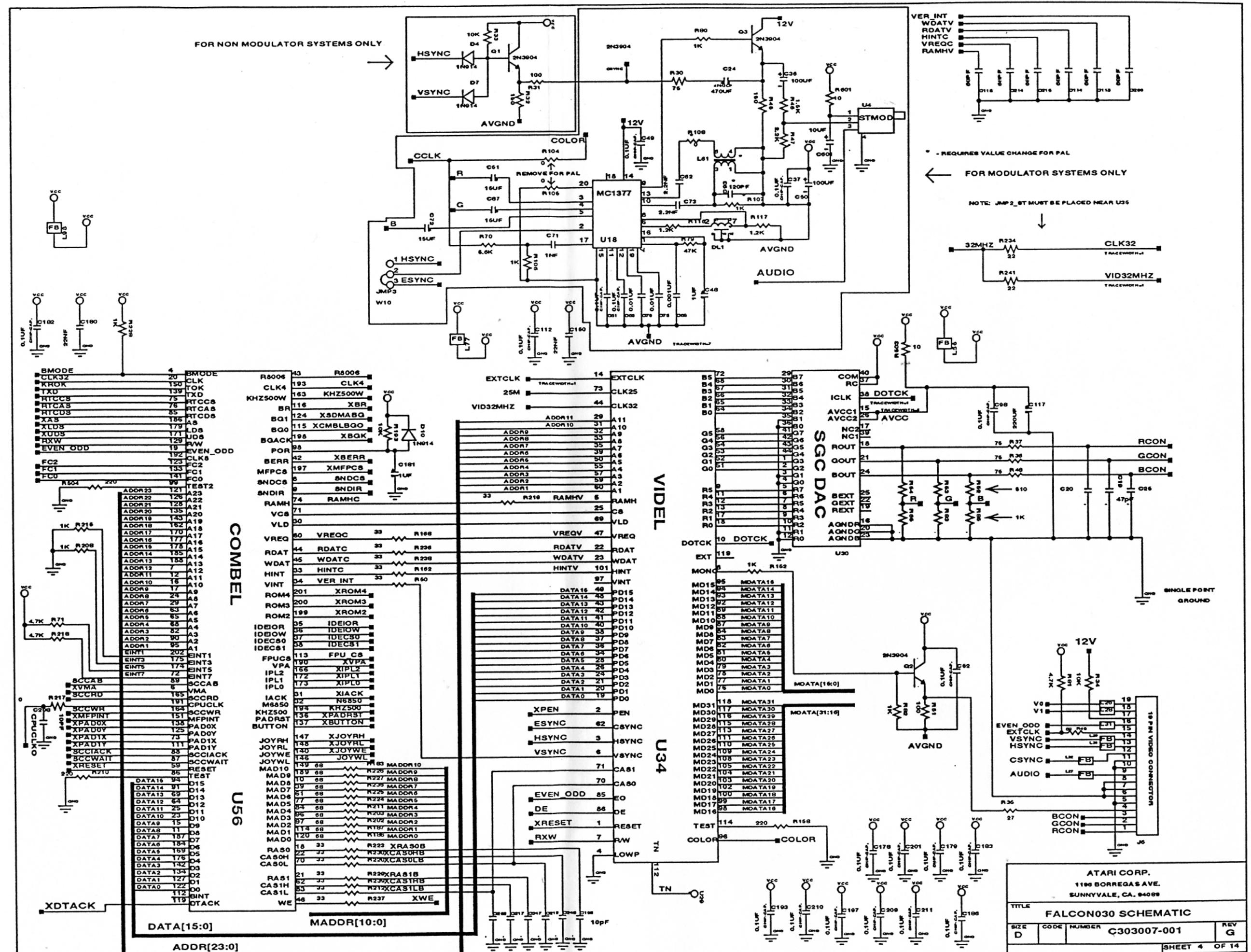
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A	INITIAL RELEASE	7/23/92	
B	REVISED PER ECO #1488	8/25/92	
C	REVISED PER ECO #1492	8/25/92	
D	REVISED PER ECO #1497	8/25/92	
E	REVISED PER ECO #1498		
F	REVISED PER ECO #1505	9/10/92	
G	REVISED PER ECO #1600	1/6/93	<i>[Signature]</i>

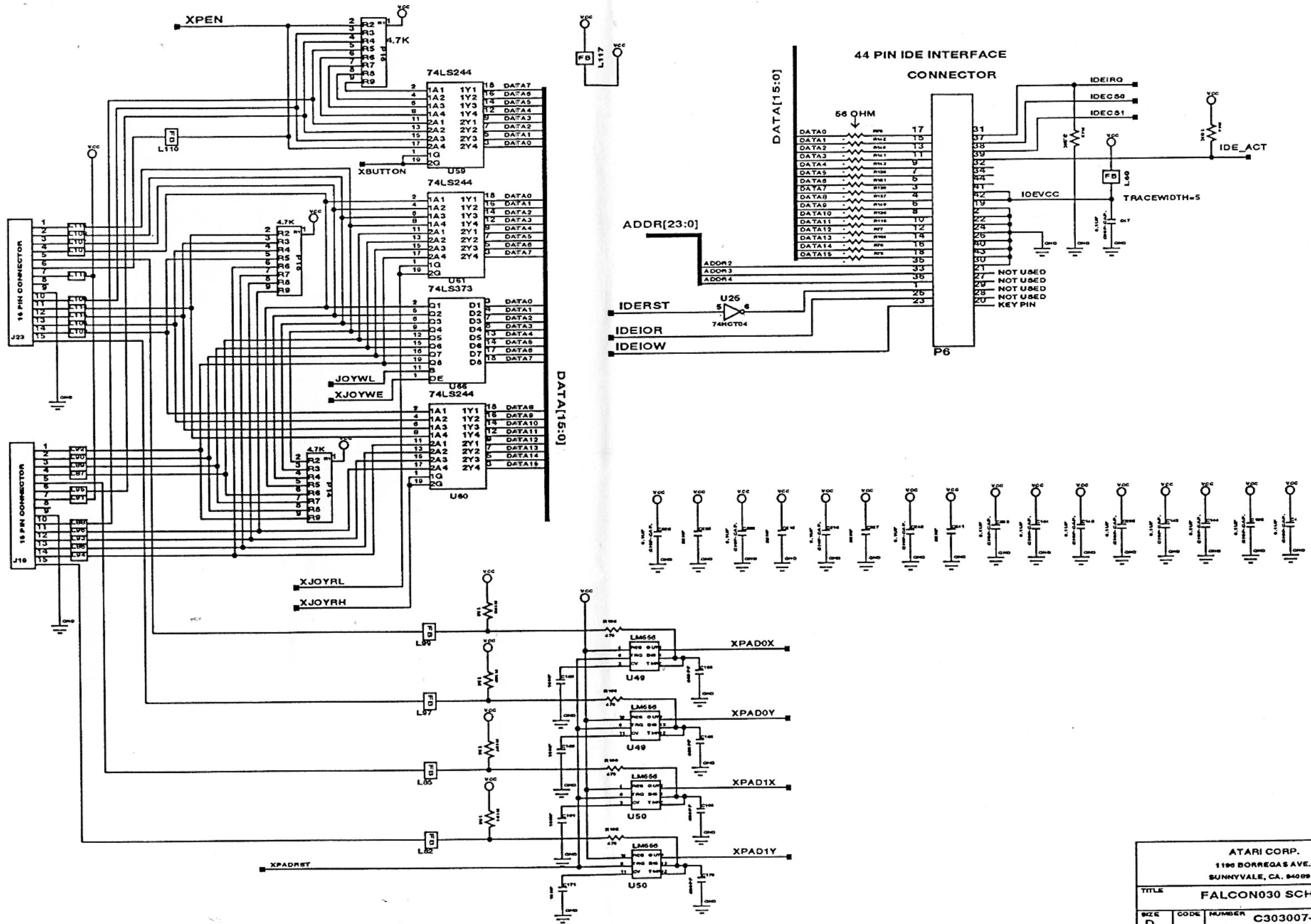
		DRAWN BY TRH	DATE	ATARI CORP.		
		CHECKED <i>JH 1-6-93</i>	DATE	1196 BORREGAS AVE. SUNNYVALE, CA. 94089		
CA401335-001		ENGINEER <i>JH 1-6-93</i>	DATE	TITLE FALCON 030 SCHEMATIC		
NEXT ASSEMBLY	USED ON ORIGINAL MODEL	APPROVED	DATE	SIZE D	CODE	NUMBER C303007-001
APPLICATION		APPROVED	DATE			REV G
						SHEET 1 OF 14

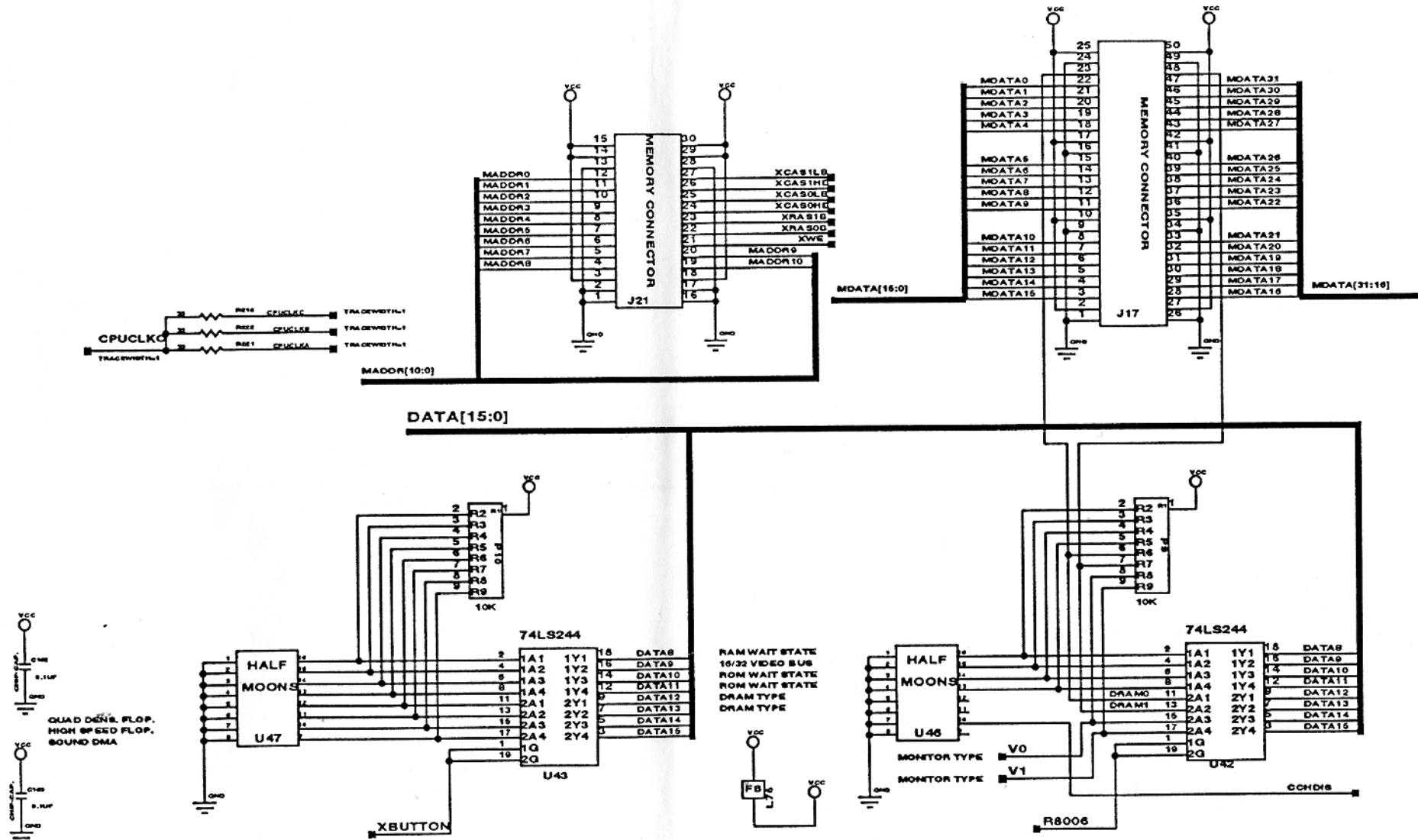
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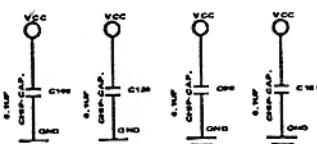
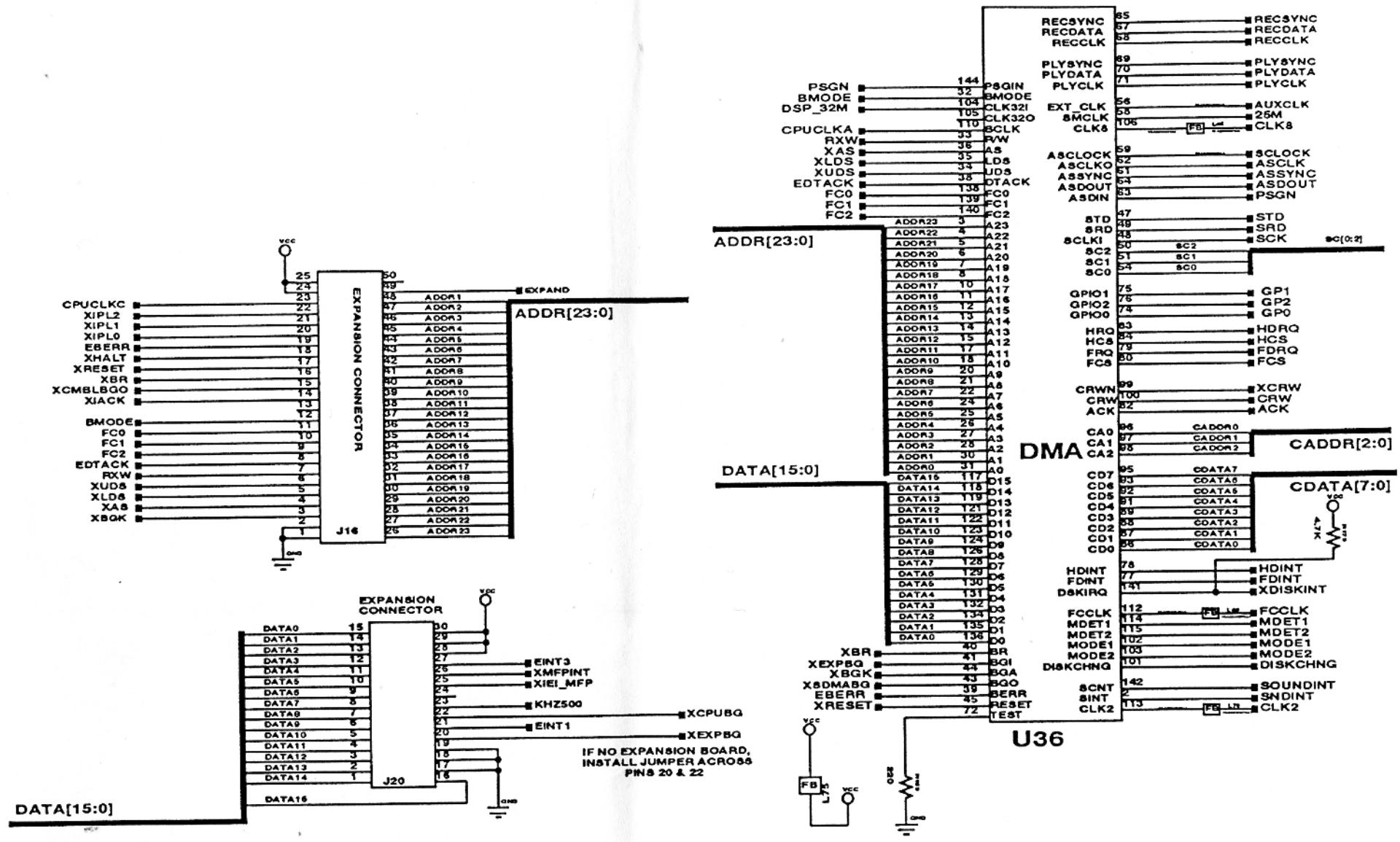








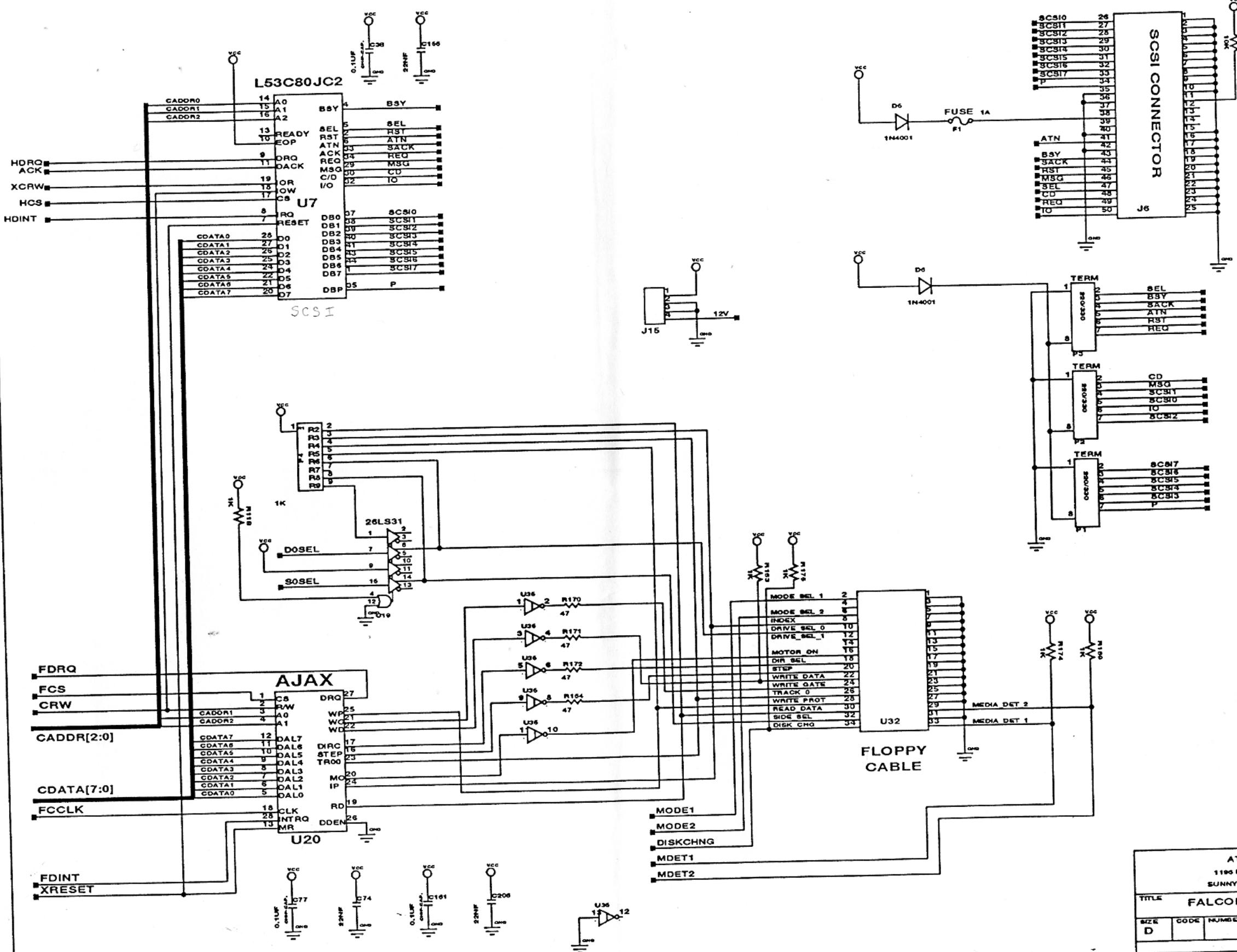




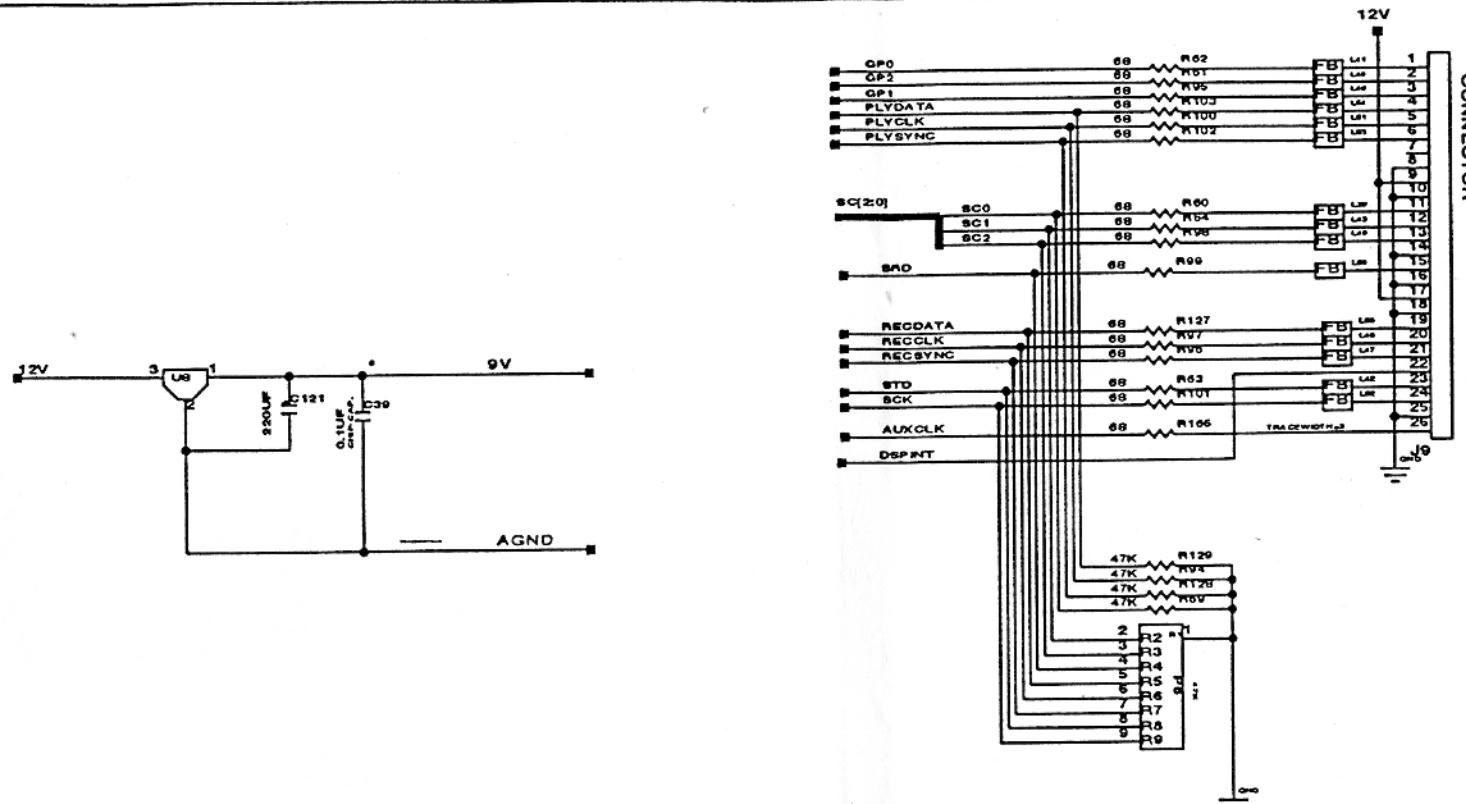
ATARI CORP.
1196 BORREGAS AVE.
SUNNYVALE, CA. 94089

TITLE FALCON030 SCHEMATIC

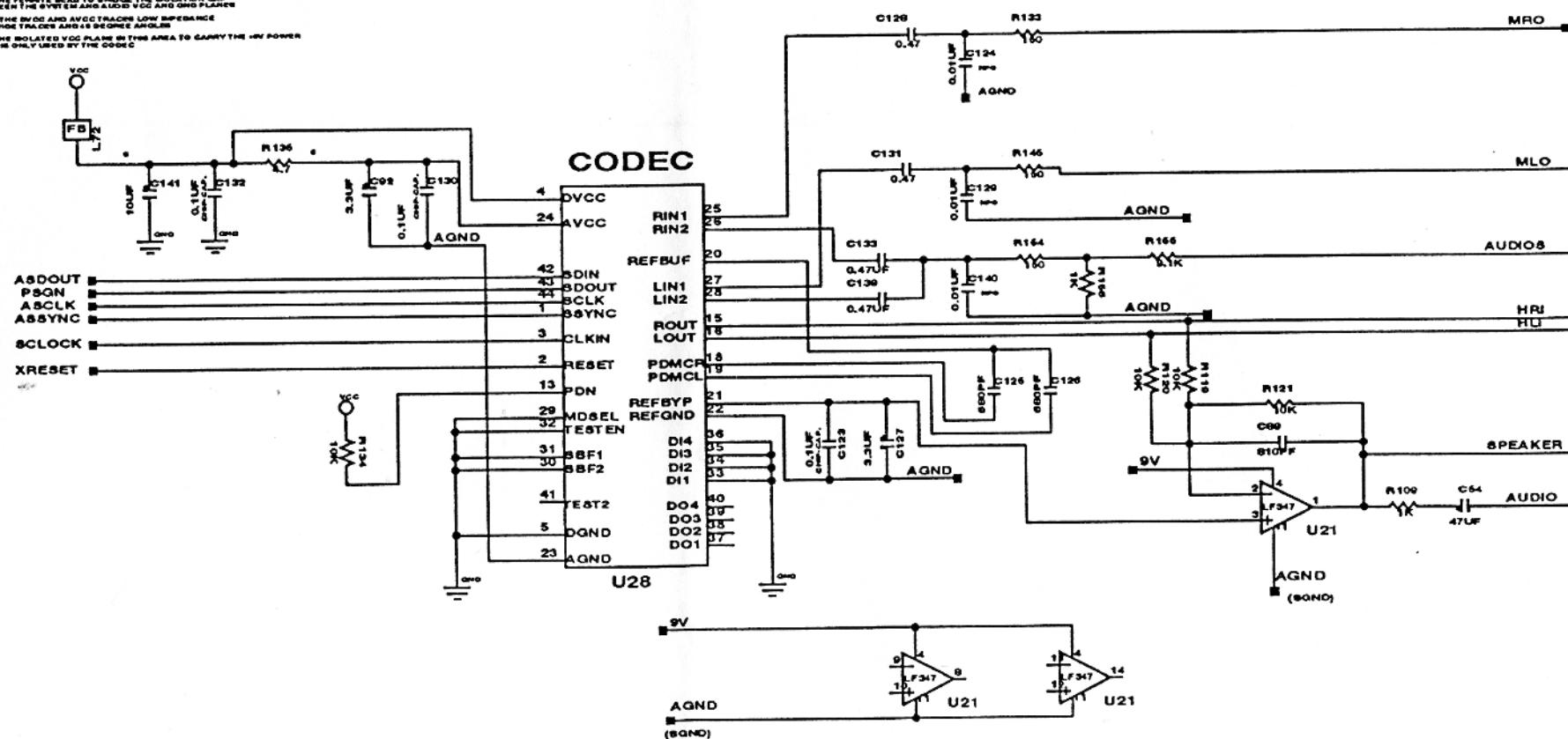
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SHEET 7 OF 14



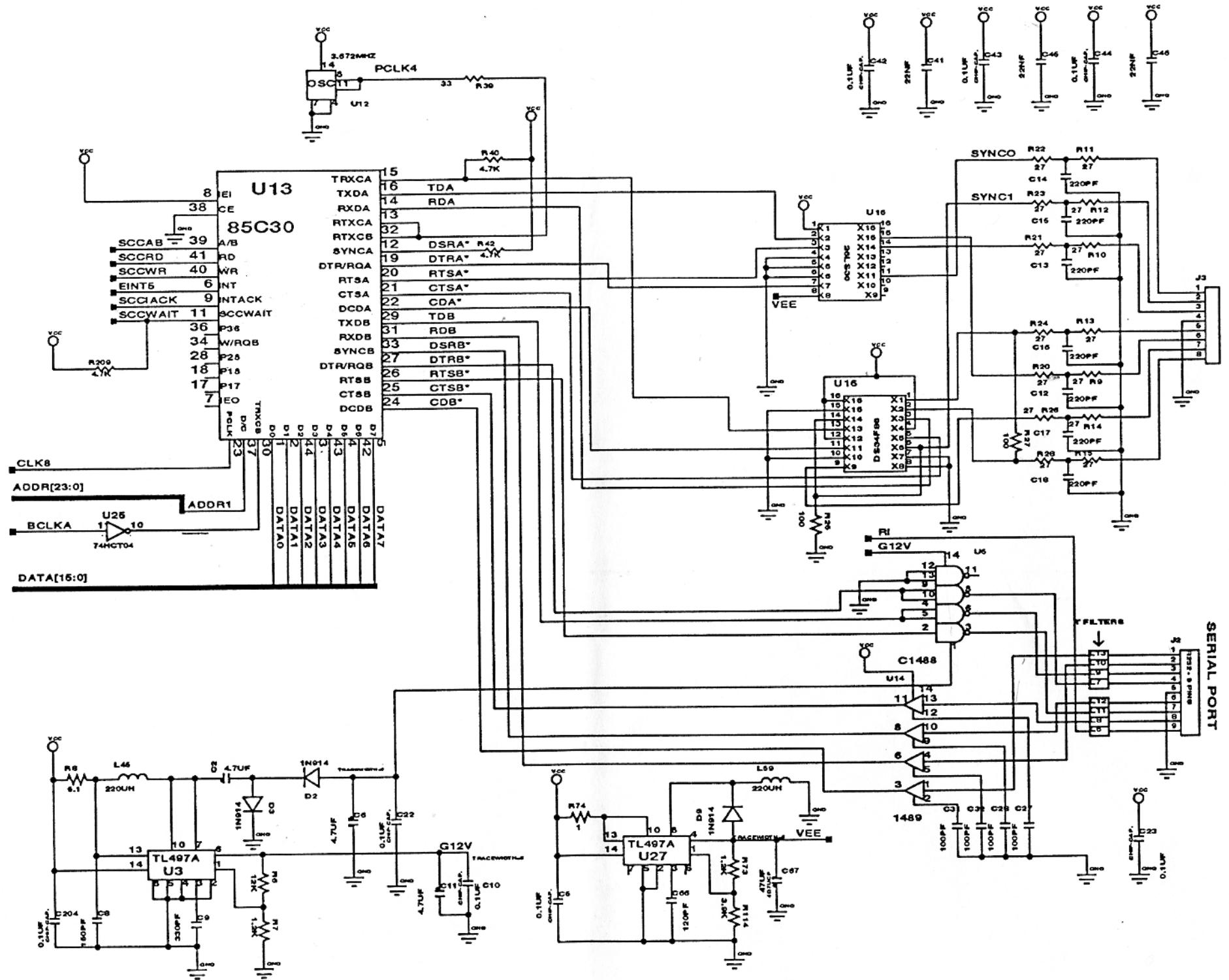
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TITLE FALCON030 SCHEMATIC		
SIZE D	CODE	NUMBER C303007-001
REV G		
SHEET 8 OF 14		



CLOCK TRACES, PULLDOWN, SLEW OR, SMOKE, AUXCLK MUX
KEEP THE VCC PLANE AS FIRM AS POSSIBLE
USE CURVED TRACES IF POSSIBLE
KEEP TRACE WIDTHS CONSTANT, IF NOT, THEN TAPER SIGNALS
TO AVOID SUDDEN IMPEDANCE CHANGES
KEEP DC IMPEDANCES CLOSE TO THE IC'S AND AT VTHMOS U
SO THAT THE SHOTGUN EFFECT IS AS SMALL AS POSSIBLE
KEEP THE VCC PLANE AS FIRM AS POSSIBLE
USE WIDE TRACES AND 45 DEGREE ANGLES
TO AVOID GET THE ISOLATION GAP
BETWEEN THE SYSTEM AND VCC AND GND PLANES
KEEP THE DVCC AND AVCC TRACES LOW IMPEDANCE
USE WIDE TRACES AND 45 DEGREE ANGLES
USE THE ISOLATED VCC PLANE IN THIS AREA TO CARRY THE HV POWER
AVCC IS ONLY USED BY THE CODEC

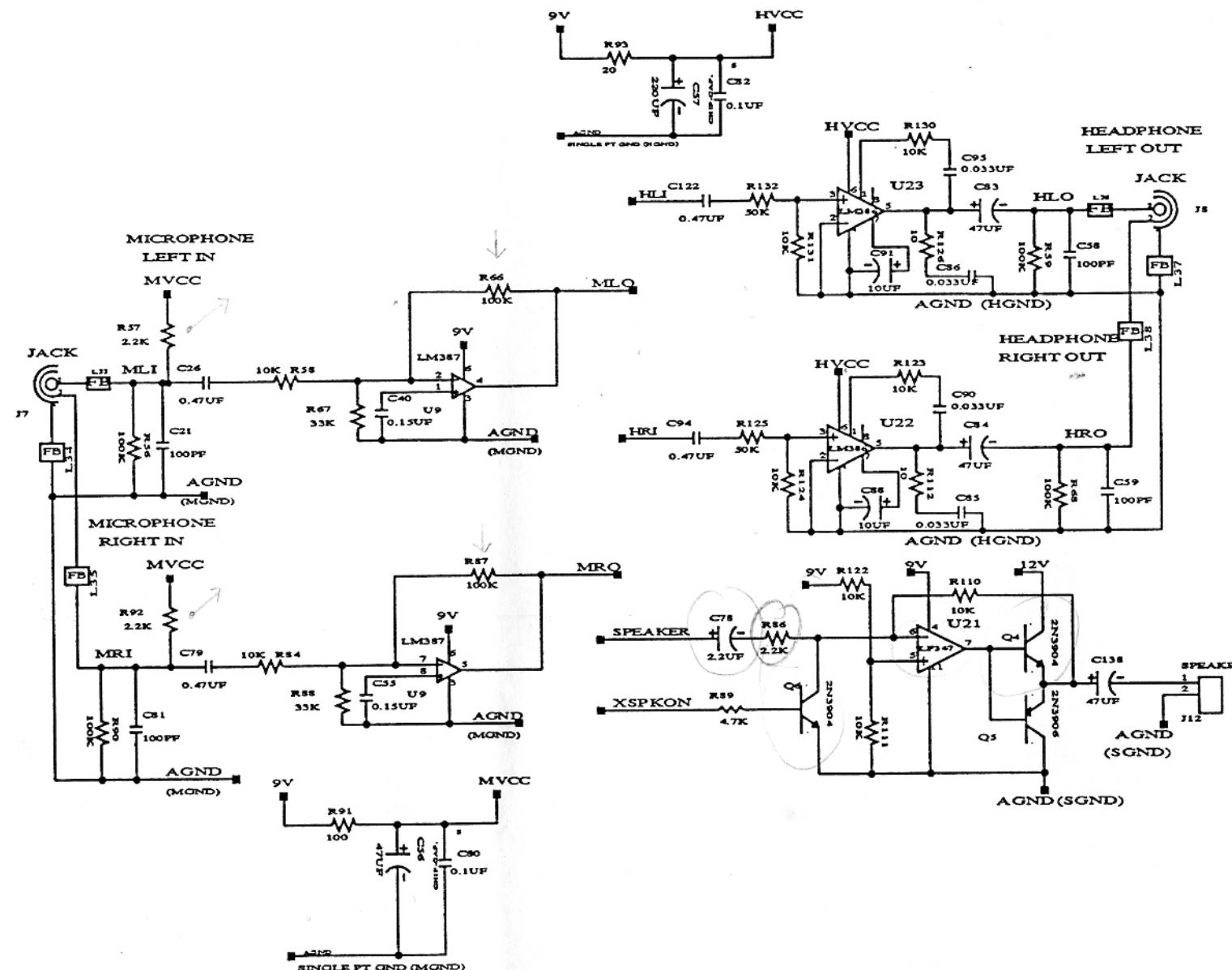


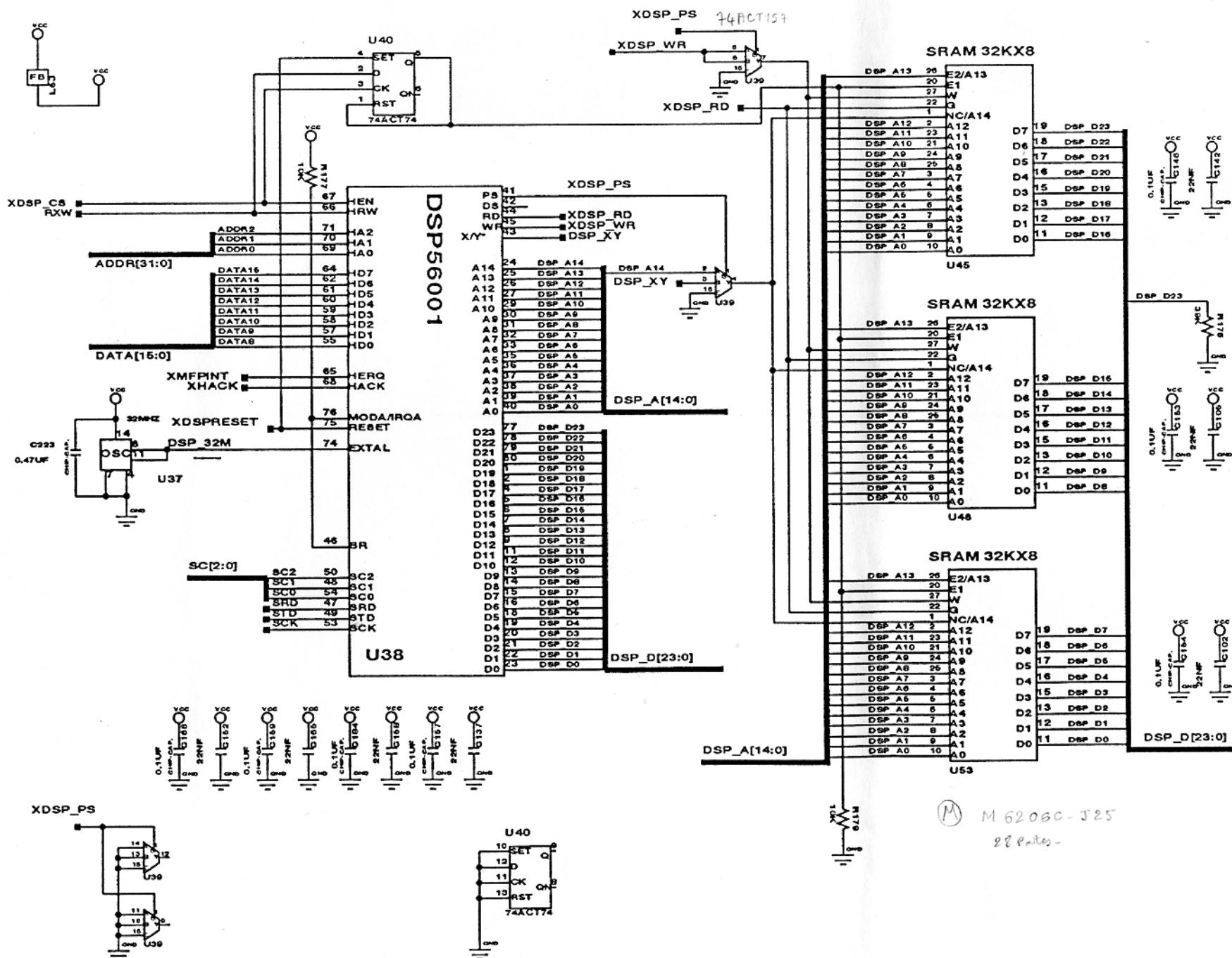
ATARI CORP.		
1160 BORREGAS AVE.		
SUNNYVALE, CA. 94089		
TITLE FALCON030 SCHEMATIC		
SIZE D	CODE NUMBER C303007-001	REV G
SHEET 9 OF 14		



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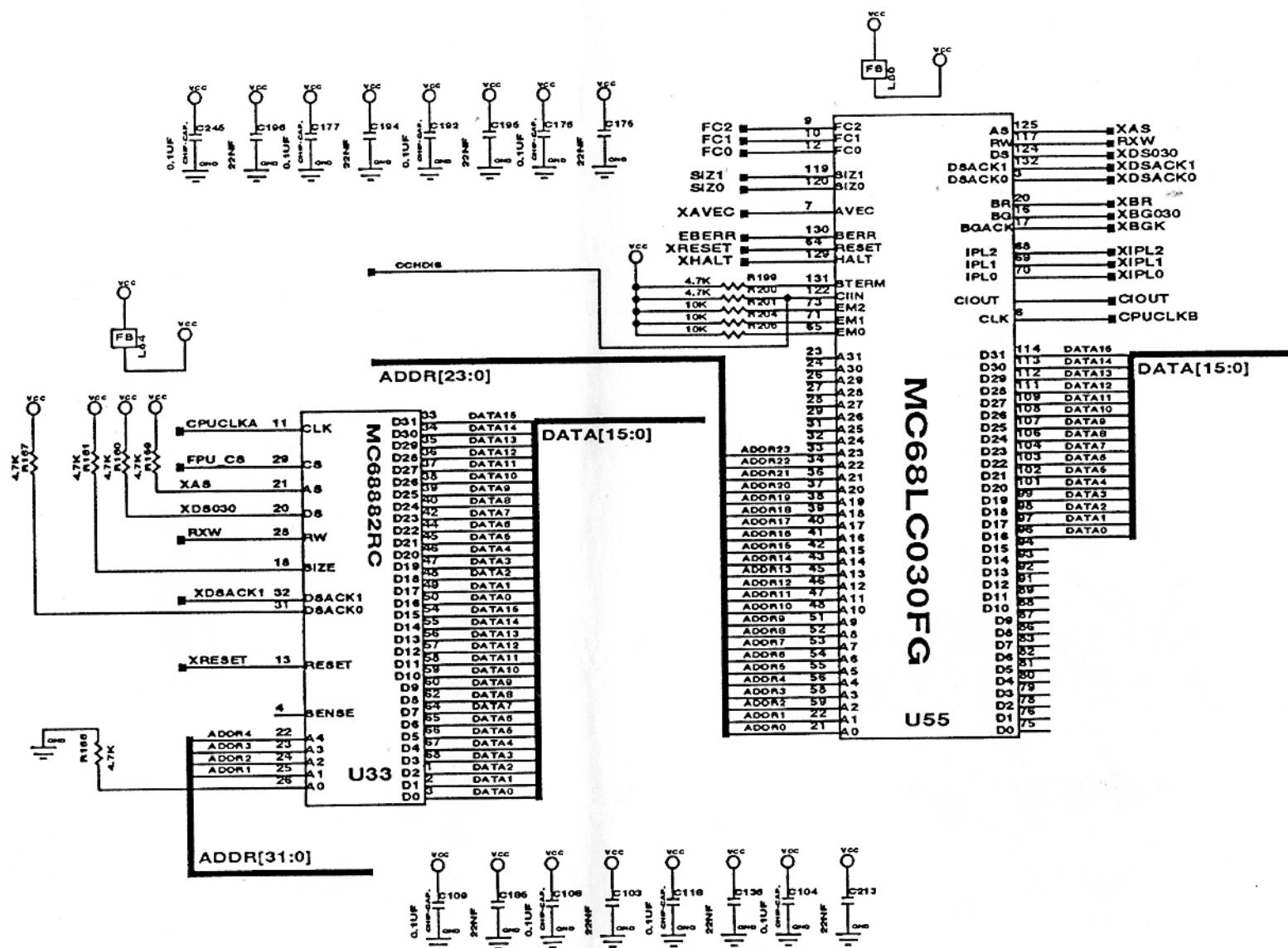
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SIZE D CODE NUMBER C303007-001 REV G
SHEET 10 OF 14





(M) M 6206C-J25
28 parts -

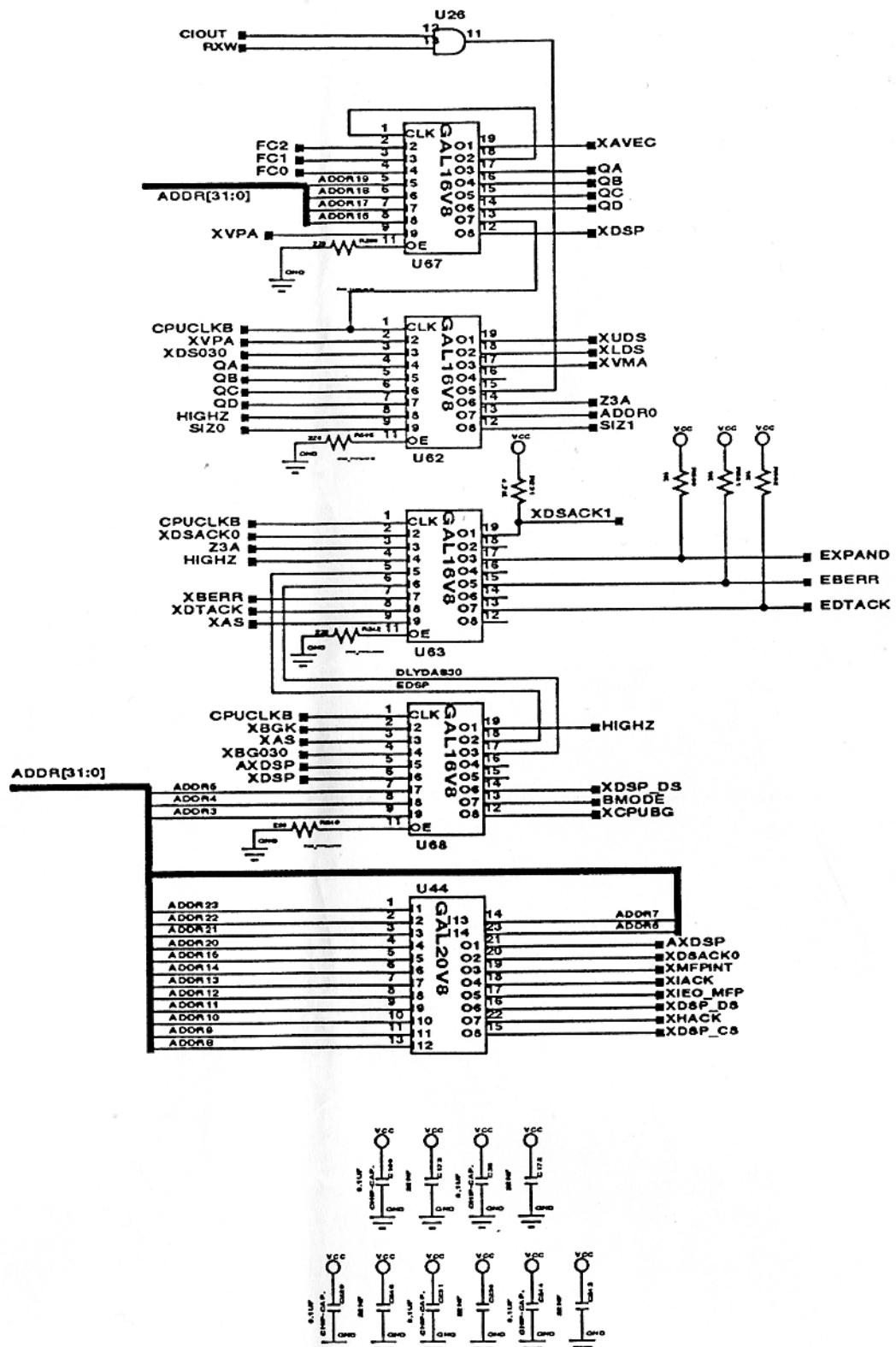
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TITLE		
FALCON030 SCHEMATIC		
SIZE	CODE	NUMBER
D		C303007-001
		REV
		G
		SHEET 14 OF 14



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SUNNYVALE, CA. 94089

TITLE FALCON030 SCHEMATIC

SHEET 12 OF 14



Interface IDE

