

# Salah Eldin Hassen

☎ (+20) 1127709232 | 📍 Giza, Egypt | ✉ [salah0eldin.work@gmail.com](mailto:salah0eldin.work@gmail.com) | 🔗 [LinkedIn](#) | 🐙 [GitHub](#)

## EDUCATION

**Bachelor of Electronics and Communications Engineering** | Cumulative Grade: Very Good 2021 - 2026  
*Cairo University Faculty of Engineering (CUFE)* Cairo, Egypt

## SKILLS

**Software** : C/C++ - Python - Vibe Coding - OOP - Automation - Scripting - AI Tools - Qt - Git & GitHub - LaTeX.

**Embedded** : ESP32 - STM32 - AVR - PIC - FreeRTOS - I2C - USART - SPI - CAN.

**Digital** : HDL (VHDL - Verilog - System Verilog) - TCL - FPGA Xilinx - Linting.

**Web** : HTML5 - CSS3 - JavaScript - Bootstrap - Servers - APIs - SQL - JSON.

**Operating Systems** : Linux (Fedora (main) - Ubuntu) - Windows.

## INTERNSHIPS & WORK EXPERIENCE

**Embedded & IoT Intern** | Internship Jul 2025 - Current  
*Lotus Power* Hybrid | Maadi, Egypt

- Developed IoT-based farm management devices, collaborating on embedded systems.

**Fourth-Year EECE Department Representative** | Volunteering Jul 2025 - Current  
*Cairo University Faculty of Engineering (CUFE)* Giza, Egypt

- Collaborated with teaching staff and coordinated timetables to support department operations.

**Autonomous Embedded Member** | Volunteering Apr 2025 - Jul 2025  
*Cairo University Eco Racing Team* Giza, Egypt

- Created IoT application for autonomous car, worked with CAN protocol, GPS module, and motor driver.

**Robotics & Embedded Instructor** | Seasonal Jan 2023 - Jan 2025  
*Beta Academy* Dokki, Egypt

- Taught programming and Arduino to over 600 students with excellent feedback.

**Coding AI Trainer** | Freelancing Sep 2024 - Dec 2024  
*Outlier* Remote

- Trained and optimized AI models for C++ and Python coding tasks.

## PROJECTS

**FreeRTOS-Based Dual Microcontroller Door Security System** | [Link](#) Summer 2024

- Implemented password authentication, EEPROM storage via I2C, and automated door control.
- Technologies: ATmega32 - I2C - USART - EEPROM - FreeRTOS.

**Advanced Digital Multimeter on PCB** | [Link](#) Spring 2024

- Designed a multimeter for voltage (-200V to 200V), current (0.5mA to 2A), and resistance (0 to 5M ohm).
- Implemented the circuit on a custom PCB from scratch.
- Technologies: ATmega32 - PCB - GPIO - LCD - Keypad - ADC - Relays - MUX - DEMUX.

**I2C-Integrated Control Unit** | [Link](#) Summer 2024

- Developed a control unit for temperature monitoring and motor control using I2C devices.
- Technologies: PIC18F46K20 - MCC - I2C - USART - RTC - EEPROM.

**Simulation & Linting Scripts** | [Link](#) Winter 2024

- Developed Python and Shell scripts for creating do files and running ModelSim.
- Created Python script for TCL files to lint designs using Qverify.
- Technologies: Python - Shell - TCL - GTKWave - Qverify.

**Advanced Tic Tac Toe Game** | [Link](#) Spring 2024

- Developed a Tic Tac Toe game with AI using minimax and Qt-based GUI.
- Implemented user authentication and automated testing with Qt Test.
- Technologies: C++ - Minimax Algorithm - Secure Hashing - Qt - Qt Test - SQLite - Git - GitHub Actions.

<b>Multi-CV Generator Script</b>   <a href="#">Link</a>	Spring 2025
<ul style="list-style-type: none"> <li>Developed a Python script to generate ATS-friendly LaTeX CVs from JSON data.</li> <li>Technologies: Python - LaTeX - JSON.</li> </ul>	
<b>SPI Slave Interface</b>   <a href="#">Link</a>	Spring 2025
<ul style="list-style-type: none"> <li>Designed a modular SPI Slave Interface with RAM modules.</li> <li>Validated using self-checking testbenches in Vivado and QuestaSim.</li> <li>Technologies: Vivado - QuestaSim - Verilog - SPI - Single-Port RAM - Constraints - Linting.</li> </ul>	
<b>Spartan-6 DSP48A1</b>   <a href="#">Link</a>	Spring 2025
<ul style="list-style-type: none"> <li>Designed and tested a DSP48A1 block in Verilog with C++ golden model.</li> <li>Technologies: Verilog - DSP48A1 - C++ - Test Benches - Linting.</li> </ul>	
<b>Reverse Tic Tac Toe Game</b>   <a href="#">Source</a> - <a href="#">Game</a>	Spring 2024
<ul style="list-style-type: none"> <li>Developed a 3x3 and 4x4 Reverse Tic Tac Toe game with web interface.</li> <li>Technologies: HTML5 - CSS3 - JavaScript.</li> </ul>	

## COURSES

<b>Embedded Linux Diploma</b>   Edges Academy	Jun 2025 - Current
<ul style="list-style-type: none"> <li>Mastered Linux administration, including system configuration and Bash scripting for automation.</li> <li>Developed Linux device drivers and device trees, tested on QEMU and Raspberry Pi.</li> <li>Learned Yocto Project for building custom embedded Linux systems with layers and recipes.</li> <li>Gained expertise in C++ OOP principles and Qt framework for GUI application development.</li> </ul>	
<b>Advanced Embedded Diploma</b>   Eng: Ahmed Abdel-Gafar	Dec 2024 - Current
<ul style="list-style-type: none"> <li>Studied ARM Cortex-M4 architecture, debugging, memory systems, and compilation process.</li> <li>Developed linker scripts, startup code, and drivers for GPIO, RCC, SysTick Timer, and NVIC.</li> <li>Designed and tested bootloader with Flash Memory Interface.</li> <li>Learned LIN, CAN protocols, AUTOSAR architecture, and MISRA C compliance.</li> </ul>	
<b>Embedded PIC Diploma</b>   Eng: Ahmed Abdel-Gafar	Jul 2024 - Sep 2024
<ul style="list-style-type: none"> <li>Learned embedded systems fundamentals, C programming, and PIC microcontroller driver development.</li> <li>Implemented USART, SPI, and I2C communication protocols using Embedded C.</li> </ul>	
<b>Embedded AVR Diploma</b>   Eng: Mohammed Tarek	Jun 2024 - Oct 2024
<ul style="list-style-type: none"> <li>Studied embedded systems fundamentals, C programming, RTOS, and data structures.</li> <li>Implemented AVR microcontroller interfacing and drivers with hands-on hardware labs.</li> </ul>	
<b>Linux Fundamentals</b>   IEEE ASU	Aug 2024 - Sep 2024
<ul style="list-style-type: none"> <li>Learned Linux file management, shell scripting, and user permissions.</li> <li>Gained skills in process control, package handling, and SSH.</li> </ul>	
<b>Digital Design Diploma</b>   Eng: Kareem Waseem	Jan 2025 - Mar 2025
<ul style="list-style-type: none"> <li>Studied RTL design, FPGA flow, Verilog, synthesis, and timing analysis.</li> <li>Explored Vivado, IP catalog, clock domain crossing, and Questa Lint for verification.</li> </ul>	

## EXTRACURRICULAR ACTIVITY

- First place in Robotics Competition (2022).
- ECPC Contestant (2022, 2023).
- NASA Hackathon participant.