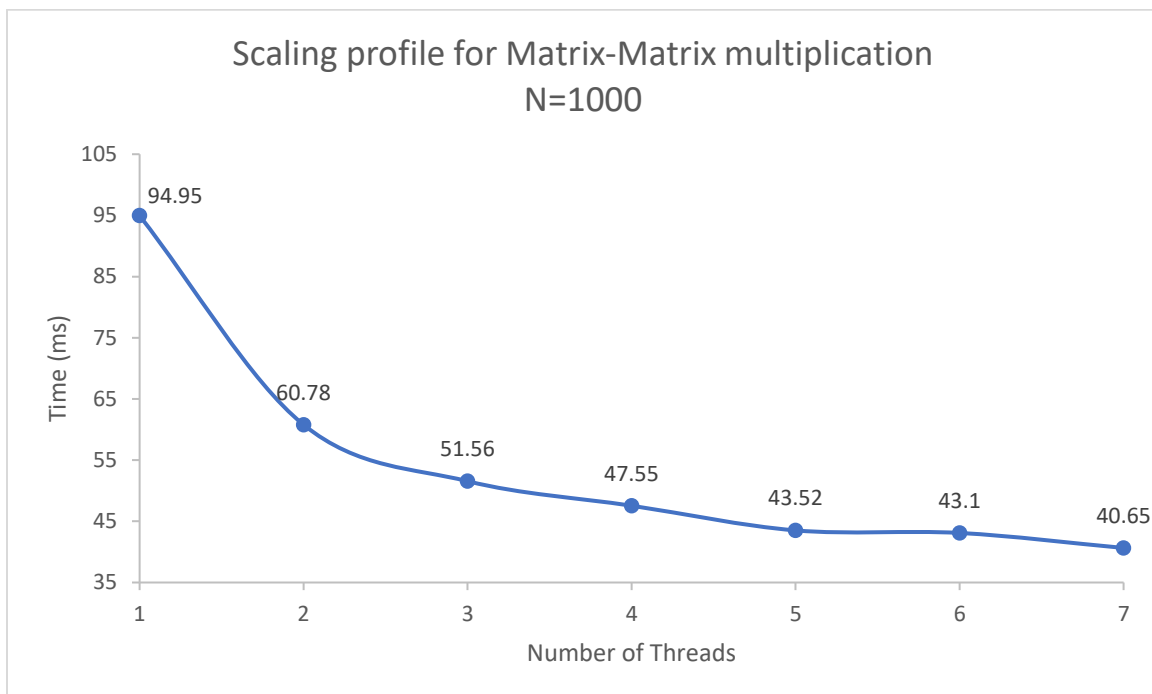


The entries in both matrices are random numbers between 1 and 9 with the following restrictions which provide a symmetric, positive definite matrix.

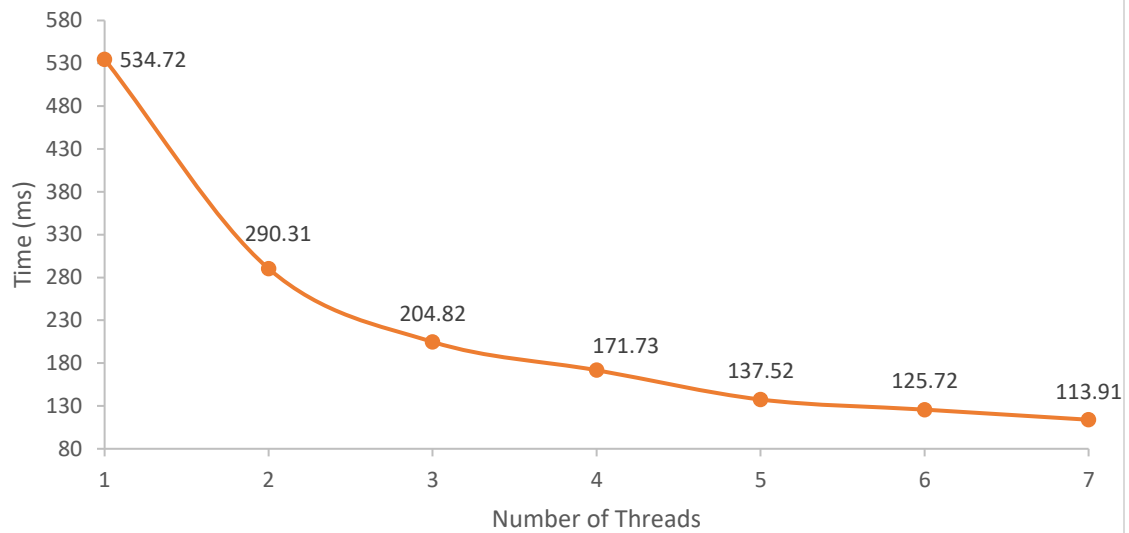
1. The off-diagonal entries are negative and symmetric.
2. The diagonal entries are the absolute value of the row sums.

The Intel® Math Kernel Library for Matrix Multiplication [1] is applied to speed up calculations, then, timing function would record the calculation time at parallel zone. To compile the code, you need ICC compiler.

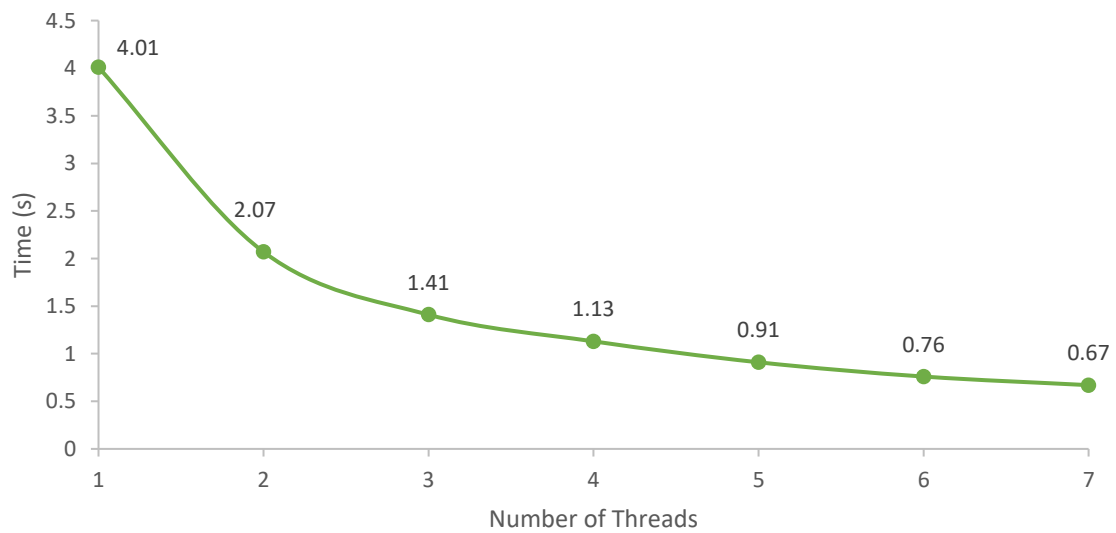
Scaling profile using different thread numbers and matrix sizes are represented in Figures below:

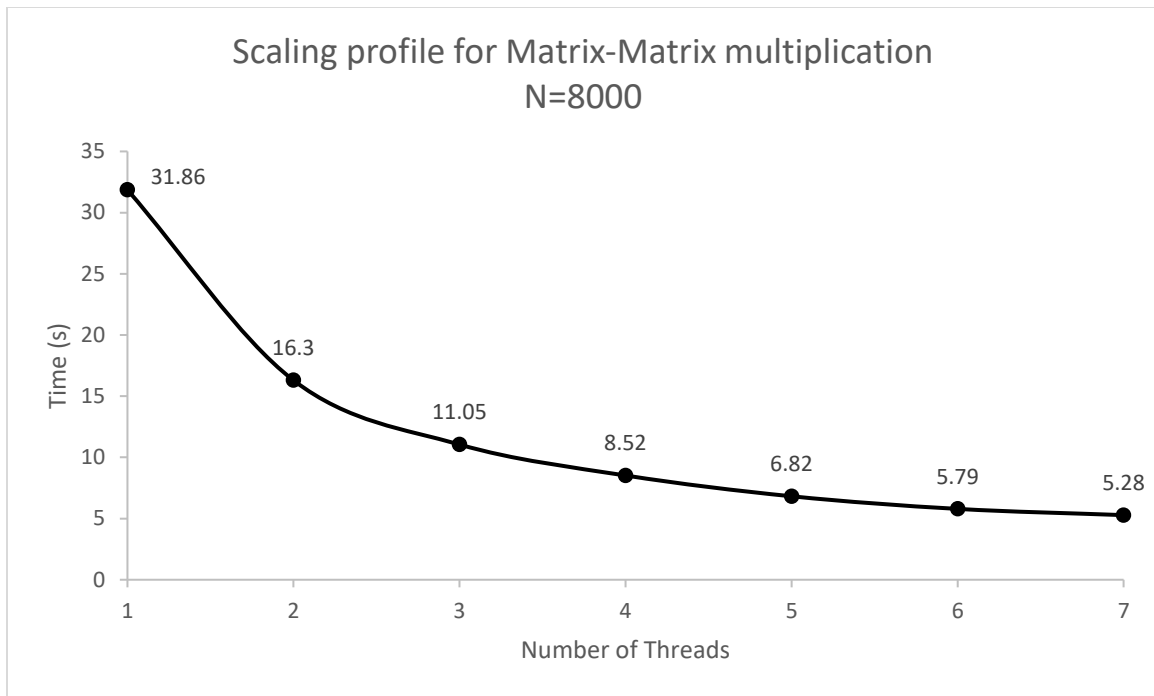


Scaling profile for Matrix-Matrix multiplication
N=2000



Scaling profile for Matrix-Matrix multiplication
N=4000





[1] <https://www.intel.com/content/www/us/en/develop/documentation/mkl-tutorial-c/top/multiplying-matrices-using-dgemv.html>