(4.5/6)

Course Title:	Computer Organization and Assembly Language
Task:	Quiz 1
Section:	BCS-3A
Date:	14ᵗʰ Sep, 2022

21L-S160

Q1. For each of the following words, identify the byte that is stored at lower memory address and the byte that is stored at higher memory address in big and little endian format:

(3)

0x5688

Big Endian

Lower Memory 56

Higher Memory 88

Little Endia

Lower Memory : 88

Higher Memory : 56

Q2. Calculate the physical address that is generated by the following segment offset pairs:

(3)

1DEF:0001

$$\begin{array}{r} 1DEF0 \\ 0\,00\,0\,1 \\ \hline 1DEF1 \\ F \end{array}$$

1.5