Bandgap Reference Circuit

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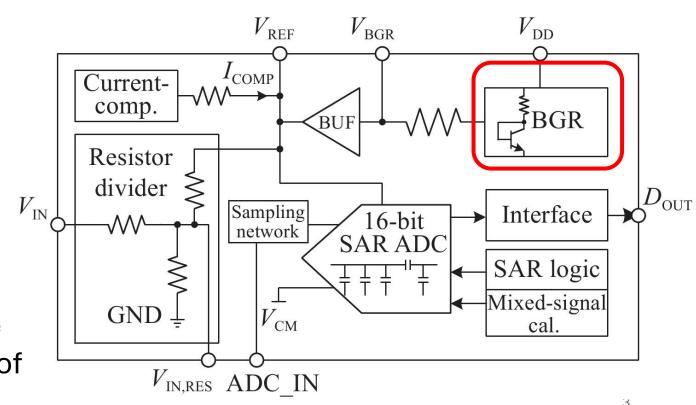
What you can learn

• Note: For optimal viewing experience, please use slideshow mode.

Bandgap Reference (BGR)

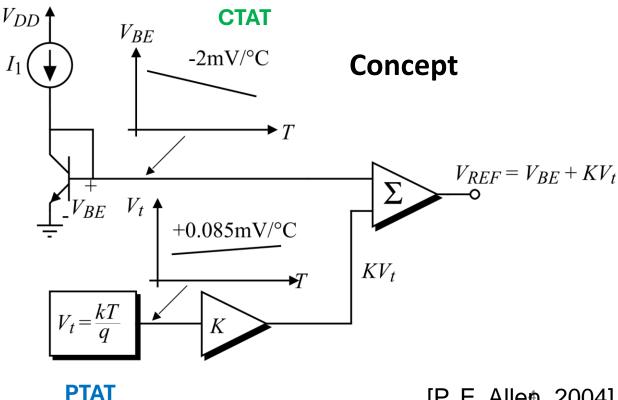
- BGR generates a fixed DC reference voltage that does not change with the variation of PVT (Process, Supply Voltage, and Temperature) variation
- Application:
 - Almost everywhere!
 - ADC, DAC,
 - LDO, Sensors,
 - Oscillators (VCO),
 - Precision Analog Circuits and many more!!

Target: Generate a fixed DC voltage proportional to the Bandgap voltage of silicon.



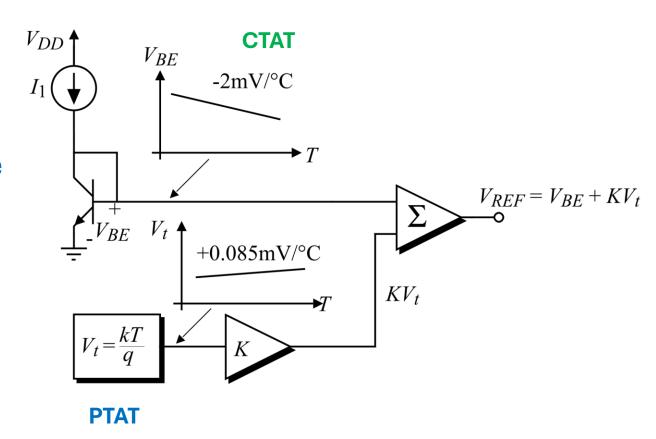
Bandgap Reference Circuit

- The principle of the bandgap voltage reference
 - Balance the negative temperature coefficient of a pn junction with the positive temperature coefficient of the thermal voltage, Vt = kT/q.
 - In short \rightarrow Balance V_{BF} with V_{T}
 - Result
 - Provides "almost" constant voltage across different PVT: $V_{BG} \approx 1.2 \text{ V}$
 - PVT
 - Process, Voltage and Temperature
 - SS_1P62_M40C
 - TT 1P80 25C
 - FF 1P98 125C



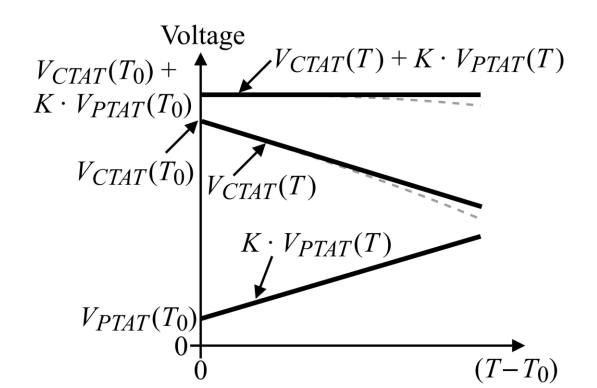
Effect of Temperature

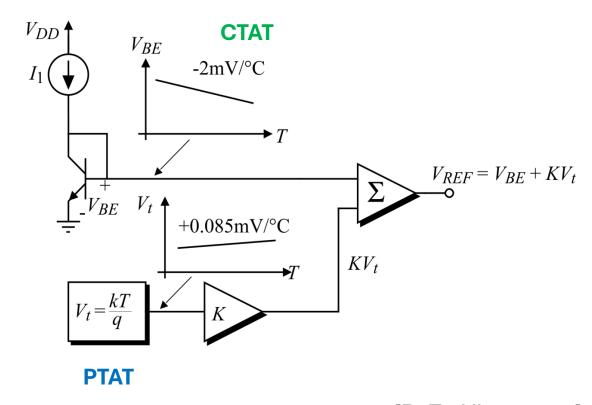
- Two voltages,
 - One that increases proportionately with temperature and is called proportional to absolute temperature or PTAT.
 - One that decreases proportionately with temperature and is called complementary to absolute temperature or CTAT.



Effect of Temperature

- Two voltages,
 - PTAT
 - CTAT
- ZTC (T) = K. PTAT (T) + CTAT (T)





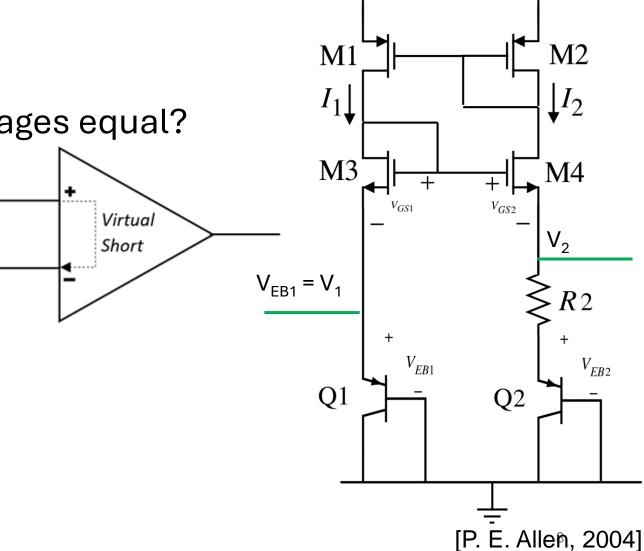
[P. E. Allen, 2004]

But HOW!!

- Let's see the basic Bandgap Circuit Structure
- Let's brush up some concepts
 - "Floating" Current Mirror and "Floater"
 - "Virtual Short" of an OpAmp
 - BJT base-emitter voltage
 - KVL and KCL

Basic Bandgap Circuit

- For the BGR to work
 - $V_1 = V_2$
- How can we make two node voltages equal?
 - Using OpAmp
 - Virtual Short
 - Virtual Ground
 - Asymptotic Equality Principle
 - Using Floating Current Mirror



 V_{DD}

 V_{DD}

Floating Current Mirror and Floater

Let's Discuss Current Mirror

MOSFET Drain Current Equation:

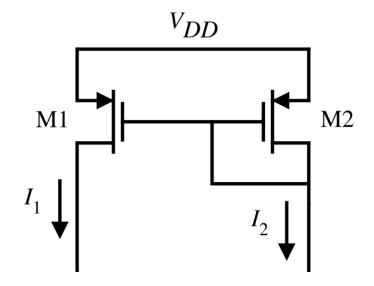
$$I_D = \frac{\mu_n C_{ox}}{2} \frac{w}{L} (V_{GS} - V_{Th})^2$$

VGS of M1 and M2 are the same.

$$\nabla_{GS1} = V_{GS2}$$

Both M1 and M2 are Identical

$$| |_2 = |_1$$



[P. E. Allen, 2004]

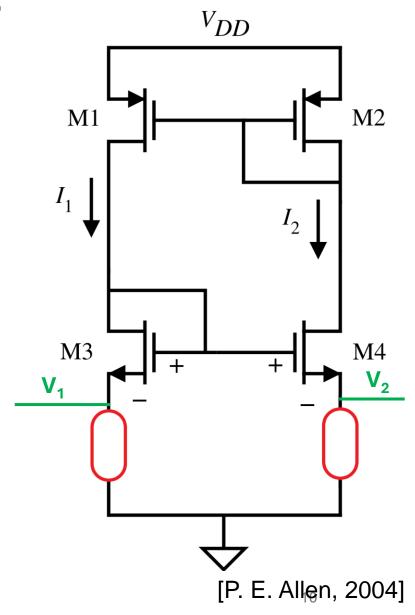
Floating Current Mirror and Floater

Are the voltages of VGS3 and VGS4 equal irrespective of the load? In other words, can we force V1 = V2?

MOSFET Drain Current Equation:

$$I_D = \frac{\mu_n C_{ox}}{2} \frac{w}{L} (V_{GS} - V_{Th})^2$$

- Since M1, M2, M3 and M4 are Identical
 - |1 = |2
 - VGS1 = VGS2



Floating Current Mirror and Floater

Are the voltages of VGS3 and VGS4 equal irrespective of the load?

• Since M1, M2, M3 and M4 are *Identical*

$$I_{1} = \frac{\mu_{n}C_{ox}}{2} \frac{w}{L} (V_{GS3} - V_{Th})^{2}$$

$$I_{2} = \frac{\mu_{n}C_{ox}}{2} \frac{w}{L} (V_{GS4} - V_{Th})^{2}$$

$$Since I_{1} = I_{2}$$

$$\frac{\mu_{n}C_{ox}}{2} \frac{w}{L} (V_{GS3} - V_{Th})^{2} = \frac{\mu_{n}C_{ox}}{2} \frac{w}{L} (V_{GS4} - V_{Th})^{2}$$

$$(V_{GS3} - V_{Th})^{2} = (V_{GS4} - V_{Th})^{2}$$

$$V_{GS3} = VGS_{4}$$

$$V_{S3} = VS_{4} [\because VG_{3} = VG_{4}]$$

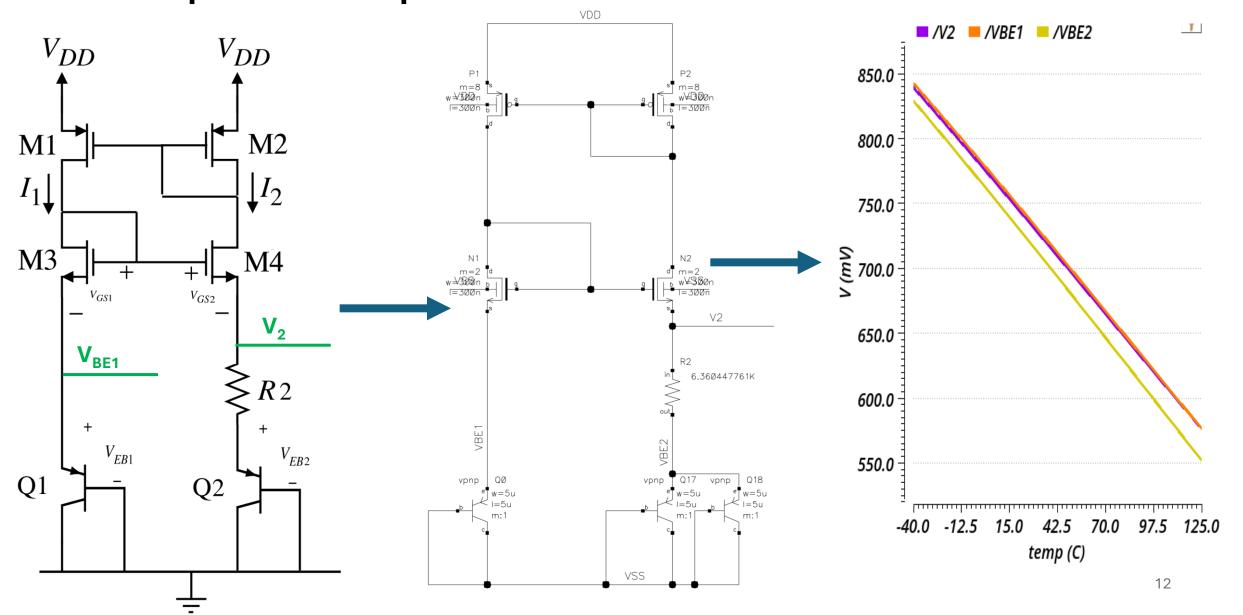
$$V_{1} = V_{2}$$

 V_{DD} M2 M1M3[P. E. Allen, 2004]

Floater

Concept Checkpoint

Note: VDD = 1.8V, VSS = 0V



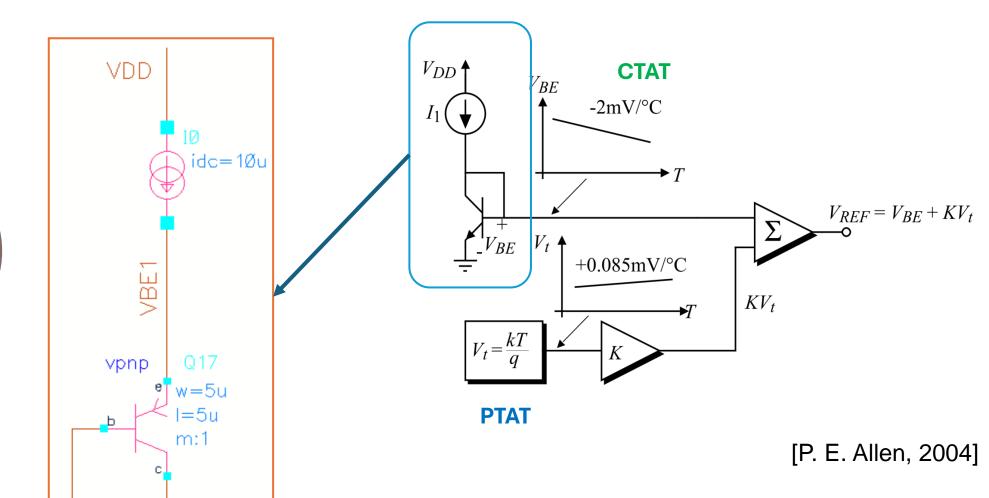
VBE of BJT

$$V_{BE} = V_T \ln \left(\frac{I_C}{I_S}\right)$$

$$V_{BE} = \frac{kT}{q} \ln \left(\frac{I_C}{I_S}\right)$$

Isn't $V_{BE} \propto T$??

VSS

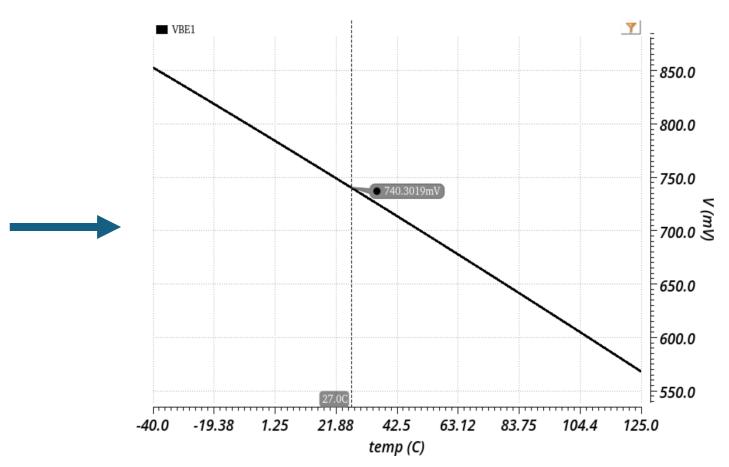


Let's Do a simulation!



VDD idc=1Øu vpnp w=5um:1 VSS

Sweep the temp from -40°C to 125°C



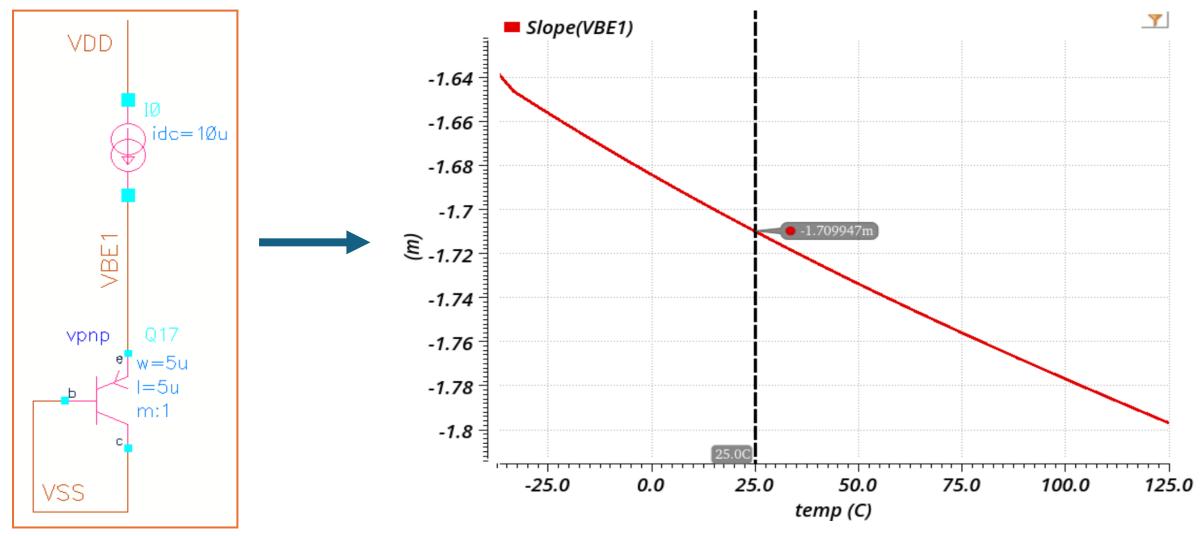
$$V_{BE} = \frac{kT}{q} \ln \left(\frac{I_C}{I_S} \right)$$

Why is the voltage decreasing?

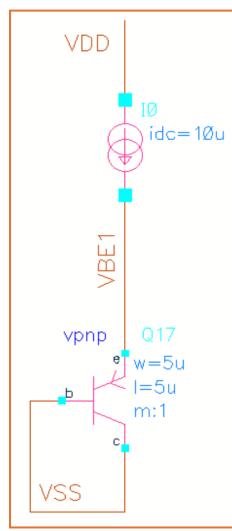
$$V_{BE} = \frac{kT}{q} \ln \left(\frac{I_C}{I_S} \right)$$

Sweep the temp from -40°C to 125°C

CTAT Current



CTAT Current



$$V_{BE} = \frac{kT}{q} \ln \left(\frac{I_C}{I_S} \right)$$

- I_s has such an incredibly strong temp dependence that even through the log it overwhelms the proportional Temp dependence!
- Let's see HOW!

$$V_{BE} = \frac{kT}{q} \ln \left(\frac{I_C}{I_S} \right)$$

I_S = Saturation Current of BJT

$$I_{S} = \frac{qAn_{i}^{2}D}{WN_{A}}$$

Where,

q = Electron Charge (Constant)

A = Area of Emitter (Constant at 1st order)

W = Width of Base(Constant at 1st order)

 N_A = Doping Concentration (Constant)

D = Diffusion Constant (NOT CONSTANT)

 n_i^2 = Number of intrinsic carrier concentration

$$D = \frac{kT}{q} \mu$$

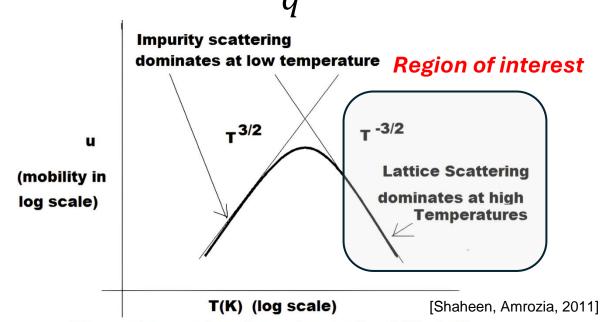


Figure. Temperature dependence of mobility in Si.

$$D = \frac{kT}{q} \mu = \frac{kT}{q} T^{-3/2}$$

$$n_i^2 = D'T^3 e^{-\frac{V_{BG}}{V_T}}$$

$$V_{BE} = \frac{kT}{q} \ln \left(\frac{I_C}{I_S} \right)$$

$$q A n^{\frac{2}{5}} D$$

$$I_S = \frac{qAn_i^2D}{WN_A}$$

$$n_i^2 = D'T^3 e^{-\frac{V_{BG}}{V_T}}$$

$$D = \frac{kT}{q} \mu$$

$$= \frac{kT}{q} T^{-3/2}$$

$$I_S = \frac{qA \left(D'T^3 e^{-\frac{V_{BG}}{V_T}}\right) \frac{kT}{q} T^{-3/2}}{WN_A}$$

$$I_S = E \cdot T^3 \cdot T \cdot T^{-\frac{3}{2}} e^{-\frac{V_{BG}}{V_T}}$$

$$I_S = E \cdot T^{\frac{5}{2}} e^{-\frac{V_{BG}}{V_T}}$$

$$I_S = E \cdot T^{\frac{5}{2}} e^{-\frac{V_{BG}}{V_T}}$$

Can we use MOSFET to achieve CTAT?

No. Since the Electrical Behavior of MOSFET is heavily process

Dependent!

$$V_{BE} = \frac{kT}{q} \ln \left(\frac{I_C}{I_S} \right)$$
 Overall CTAT

$$V_{BE} = \frac{kT}{q} \ln(I_c) - \frac{kT}{q} \ln(I_s)$$

$$V_{BE} = \frac{kT}{q} \ln(I_c) - \frac{kT}{q} \ln(E) - \frac{5}{2} \frac{kT}{q} \ln(T) + \frac{kT}{/q} \cdot \frac{V_{BG}}{V_{T}}$$

$$V_{BE} = \frac{kT}{q} \ln(I_C) - \frac{kT}{q} \ln(E) - \frac{5}{2} \frac{kT}{q} \ln(T) + V_{BG}$$
Weak PTAT
Strong CTAT
Constant BG voltage

Base-Emitter Voltage of BJT

$$V_{BE} = \frac{kT}{q} \ln(I_c) - \frac{kT}{q} \ln(E) - \frac{5}{2} \frac{kT}{q} \ln(T) + V_{BG}$$

$$\text{Weak PTAT} \qquad \text{Strong CTAT} \qquad \text{Constant BG voltage}$$

- Overall Negative Temp coefficient
- Directly related to the Bandgap voltage (V_{BG})
 - Bandgap voltage is an intrinsic property of a semiconductor
 - It doesn't depend on PVT variation, and this is what we want!
 - Our target is to get this voltage and use that as a way of making a voltage reference!

Idea of BGR

$$V_{BE} = \frac{kT}{q} \ln(I_c) - \frac{kT}{q} \ln(E) - \frac{5}{2} \frac{kT}{q} \ln(T) + V_{BG}$$
Weak PTAT Strong CTAT Constant BG voltage

- Increase "Weak PTAT" to be equal to "Strong CTAT"
 - They will cancel each other!
 - Mathematically: ZTC (T) = K. PTAT (T) + CTAT (T)
- We'll be left with a CONSTANT Bandgap Voltage
 - For Silicon it is approximately 1.2 V

Idea of BGR

$$V_{BE} = \frac{kT}{q} \ln(I_C) - \frac{kT}{q} \ln(E) - \frac{5}{2} \frac{kT}{q} \ln(T) + V_{BG}$$

$$V_{BE} = \frac{kT}{q} \ln\left(\frac{I_C}{I_S}\right)$$
Weak PTAT

Strong CTAT

Constant BG voltage

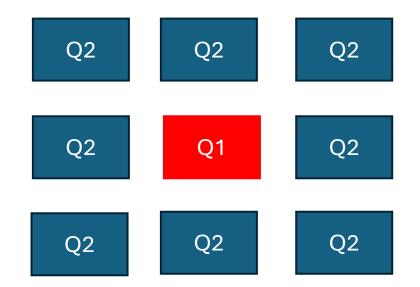
Weak PTAT

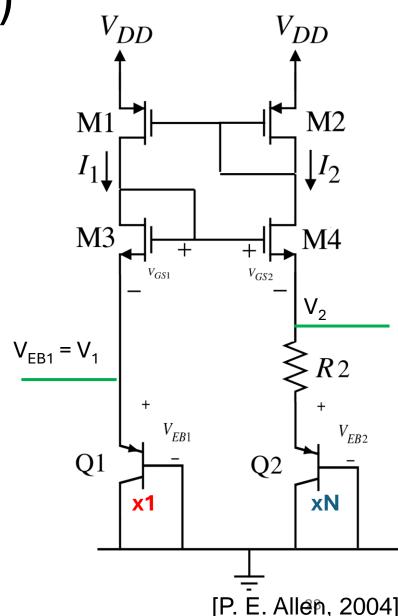
Strong CTAT

- But first of all, We need to extract "Weak PTAT" from V_{BE}!
 - How can we do that?

Choice of N (Making PTAT Stronger)

- Usually the value of "N" is chosen such a way to make N = integer² – 1
- For example, $N = 8 (3^2 1)$
- Why??
- Because of matching in layout. E.g. Commoncentroid matching



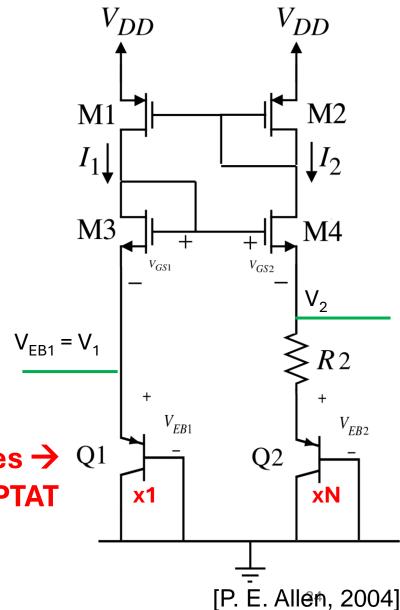


Extracting PTAT

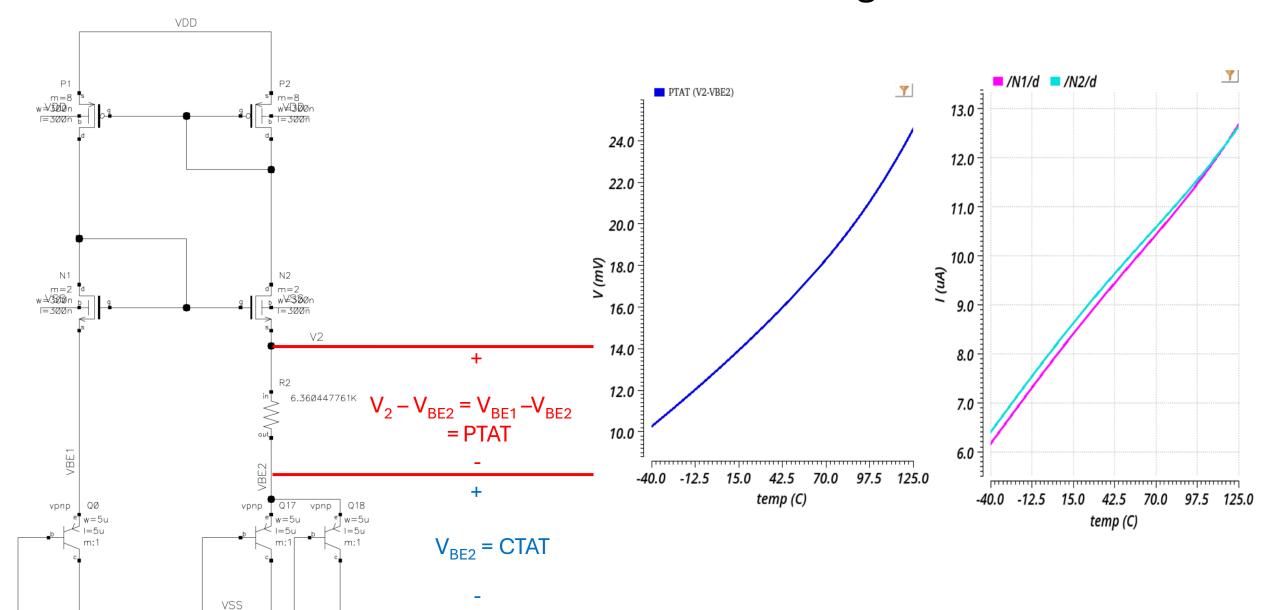
- Make I1 = I2 = I and V1 = V2 using current mirror or OpAmp
- $V_{BE1} = RI + V_{BE2}$
- V_{BE1} V_{BE2} = RI
- $\frac{kT}{q} \ln \left(\frac{I_C}{I_S} \right) \frac{kT}{q} \ln \left(\frac{I_C}{NI_S} \right) = RI$ [Assuming identical BJT]
- $\frac{kT}{q}\ln(N)$ RI
- $I = \frac{kT}{qR} \ln(N)$

PTAP Current

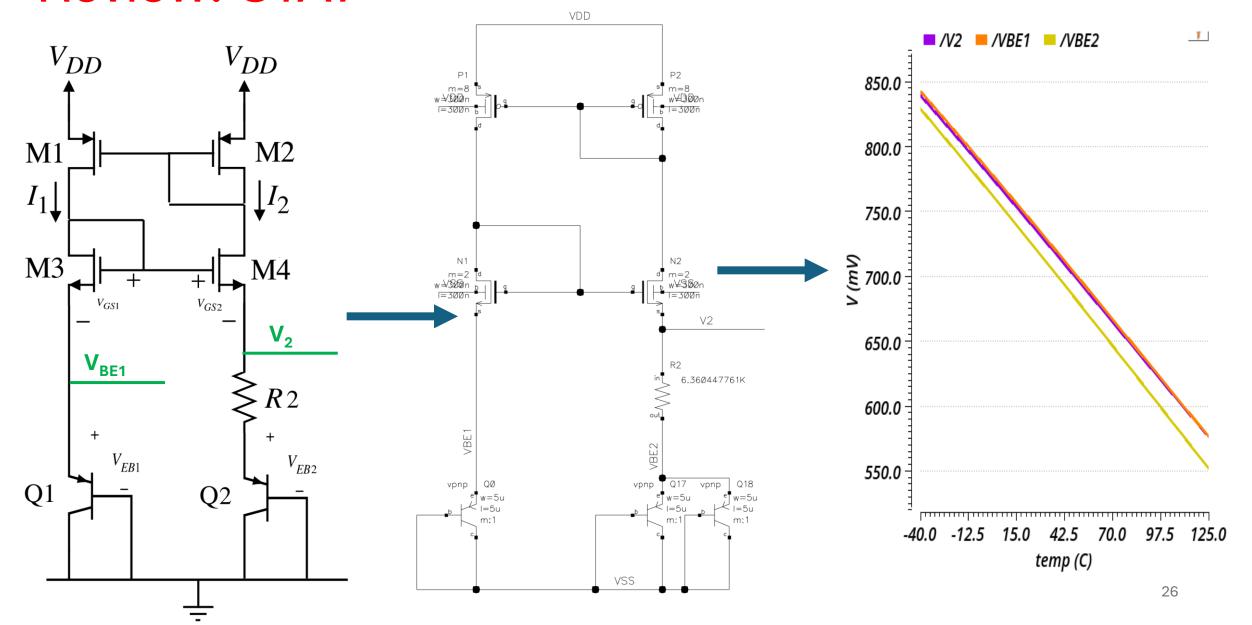
→ Use CM to generate multiple copies → Q1 Drop it across a resistor to generate PTAT Voltage



Simulation Verification of PTAT Current and Voltage



Review: CTAT

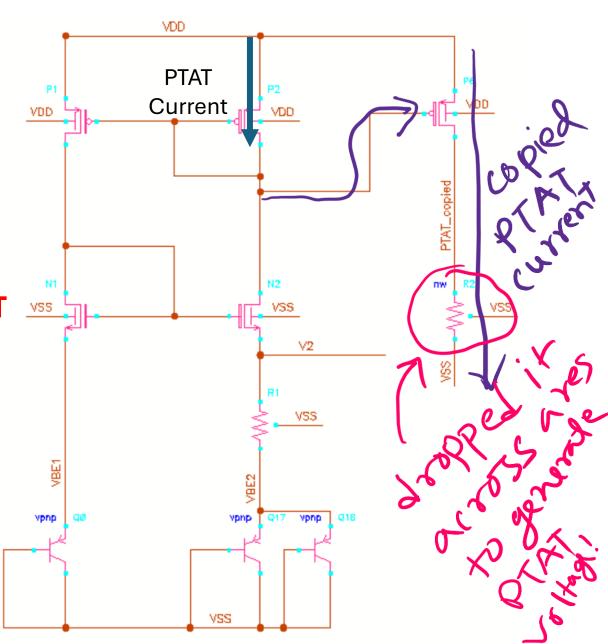


Extracting PTAT Voltage Separately

$$I_{\mathsf{PTAT}} = \frac{kT}{qR} \ln(N)$$

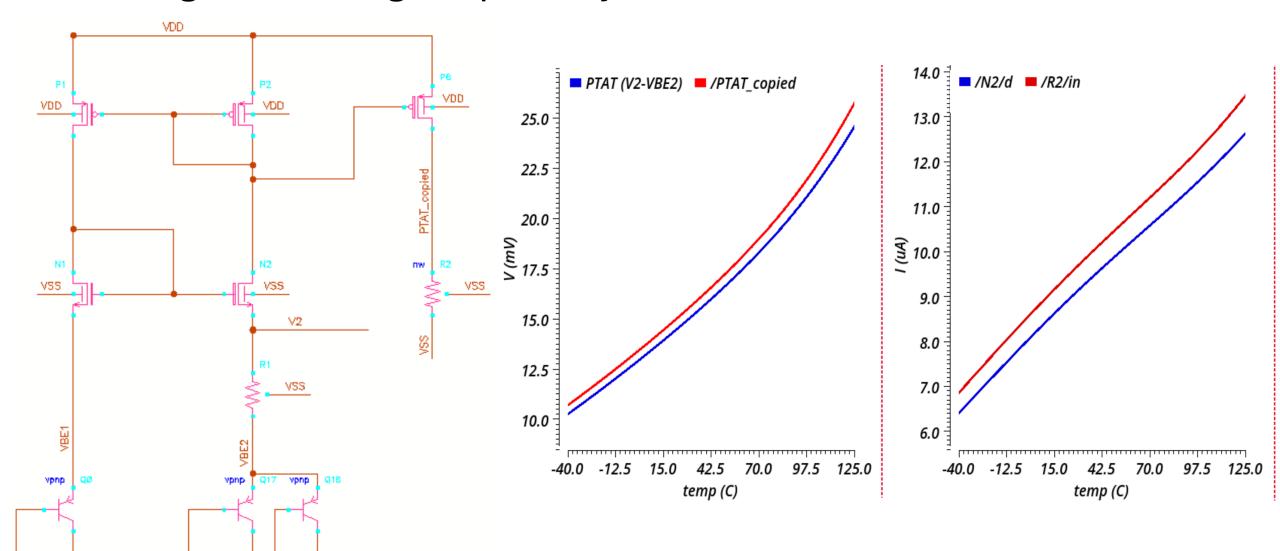
PTAP Current

- → Use CM to copy PTAT current
- → Drop it across a resistor to generate PTAT Voltage



Extracting PTAT Voltage Separately

VSS



Review: Idea of BGR

$$V_{BE} = \frac{kT}{q} \ln(I_c) - \frac{kT}{q} \ln(E) - \frac{5}{2} \frac{kT}{q} \ln(T) + V_{BG}$$

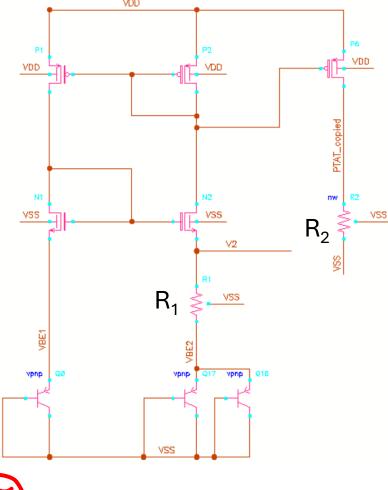
$$\text{Weak PTAT} \qquad \text{Strong CTAT} \qquad \text{Constant BG voltage}$$

- Increase "Weak PTAT" to be equal to "Strong CTAT"
 - They will cancel each other!
 - Mathematically

• ZTC (T) = K. PTAT (T) + CTAT (T)
$$= \frac{R_2}{R_1}. PTAT (T) + CTAT (T)$$

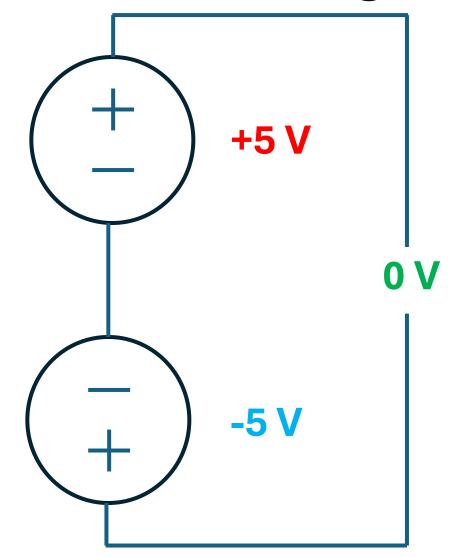


Increase R2/R1 Ratio to make PTAT Stronger



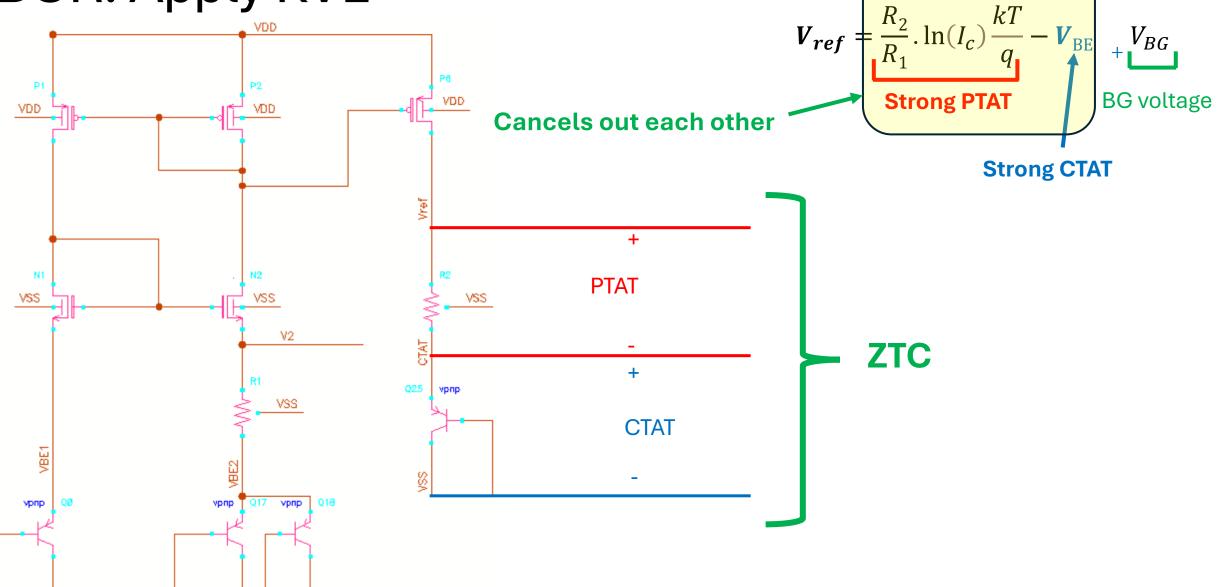


Review: Kirchhoff's Voltage Law (KVL)

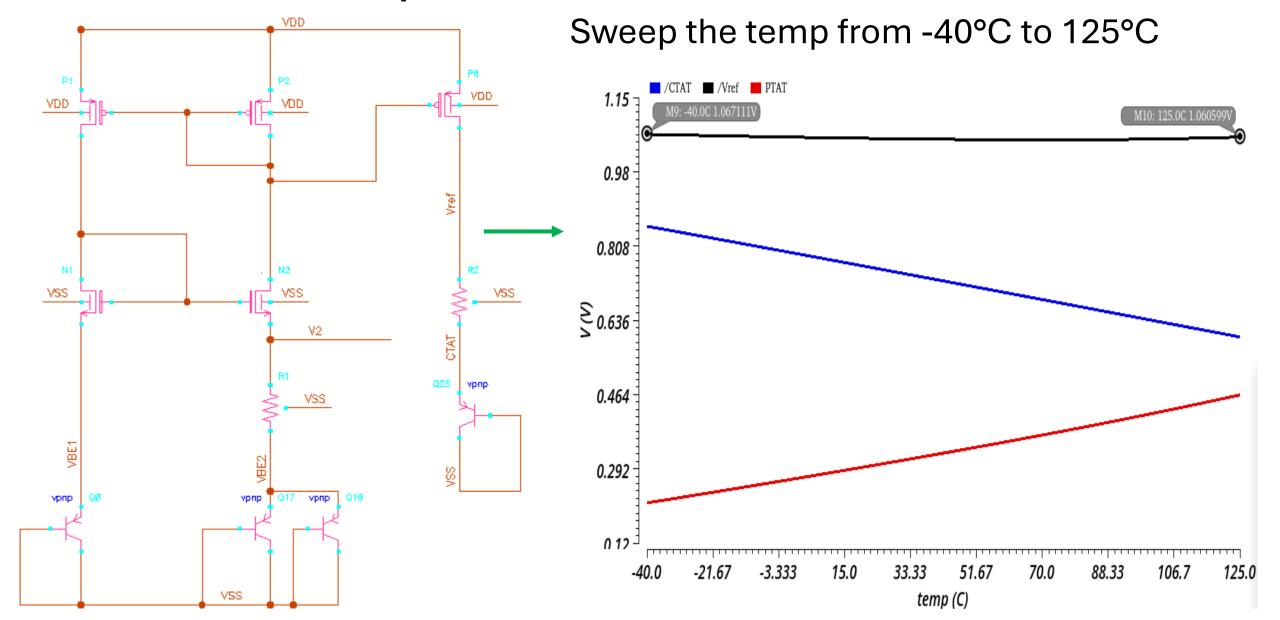


BGR: Apply KVL

VSS



Simulation output of BGR



Temperature Coefficient

Vref_h = 1.061 V
Vref_l = 1.067 V

$$T_h = 125^{\circ}C$$

 $T_l = -40^{\circ}C$
Vref@25C = 1.060 V
Temp Coefficient = $\frac{|Vref_h - Vref_l|}{T_h - T_l} \propto \frac{10^6}{Vref@25^{\circ}C} = \frac{ppm}{r}$

$$\frac{|1.061 - 1.067|}{125 - (-40)} \times \frac{10^6}{1.060} = 34.3 \ ppm/_{\circ C}$$

The plotted derivative function is

$$\frac{\partial(voltage)}{\partial(temperature)}$$

in units of

$$\frac{V}{^{\circ}C}$$

. Divide this function by your nominal Vref - e.g. $1.22V @ 25^{\circ}C$ - and multiply it by 1.000.000 (ppm = part per million), so you get

$$rac{V}{{}^{\circ}C} imes rac{10^6}{Vref[V])} = rac{ppm}{{}^{\circ}C}$$

.

kokykokykoky said: ①

So which point should I take to be the TC?

The relative temperature dependence of your ref. voltage (in

$$\frac{ppm}{{}^{\circ}C}$$

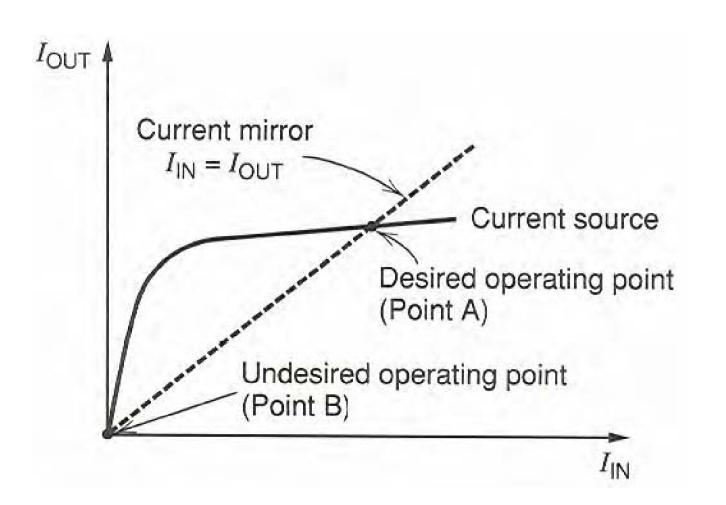
) will change over the temperature range. Which value you should take depends on your specification needs: you could specify several

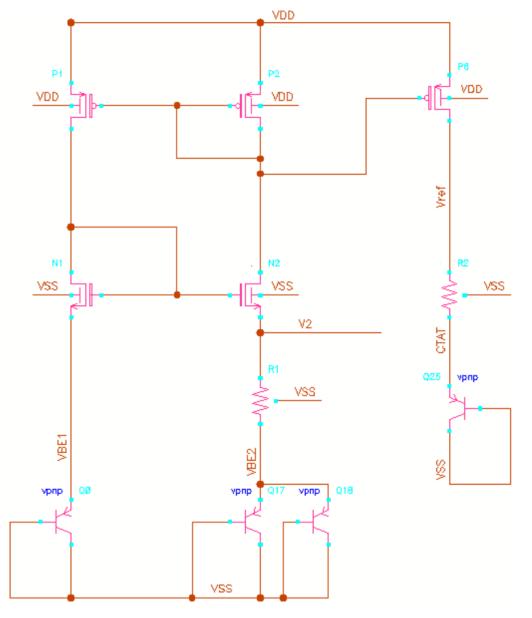
$$\frac{ppm}{{}^{\circ}C}$$

values for their corresponding temperature points, or you could specify $\underline{\text{min. \& max values}}$ for a specified temperature range, or a $\underline{\text{mean value}}$ for such a range \pm its max. deviations, e.g. for the application temperature range.

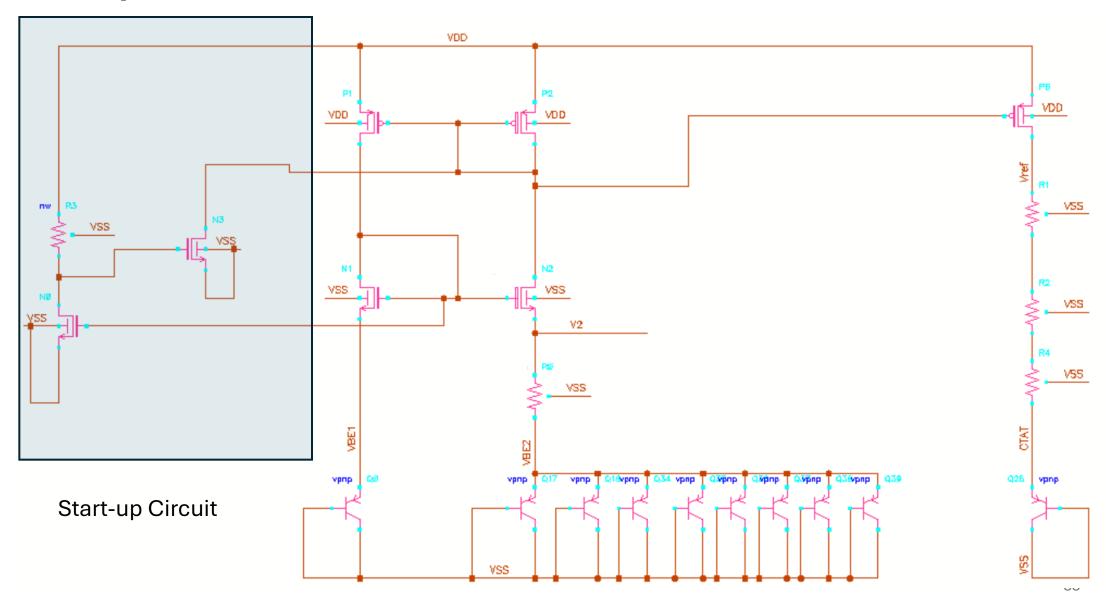
See also company bandGap dataSheets how they specify their values.

Stable Biasing Point of a Reference Source





Start-up Circuit



Some good sources for learning more!

- New Analog Circuit Design (By Prof. Ali Hajimiri, Caltech) Lectures
 131N to 134N
- Design of Bandgap Reference

References

- [1] P. E. Allen and D. R. Holberg, "CMOS Analog Circuit Design," 2nd Edition, Oxford University Press, New York, 2004
- [2] Shaheen, Amrozia, Wasif Zia, and Muhammad Sabieh Anwar. "Band structure and electrical conductivity in semiconductors." LUMS School of Science and Engineering, Lahore, Pakistan (2011).
- [3] J. Baker, "Circuit Design, Layout and Simulation", 4th Edition, John Wiley & Sons, July 2019. ISBN 9781119481515

Thank you!