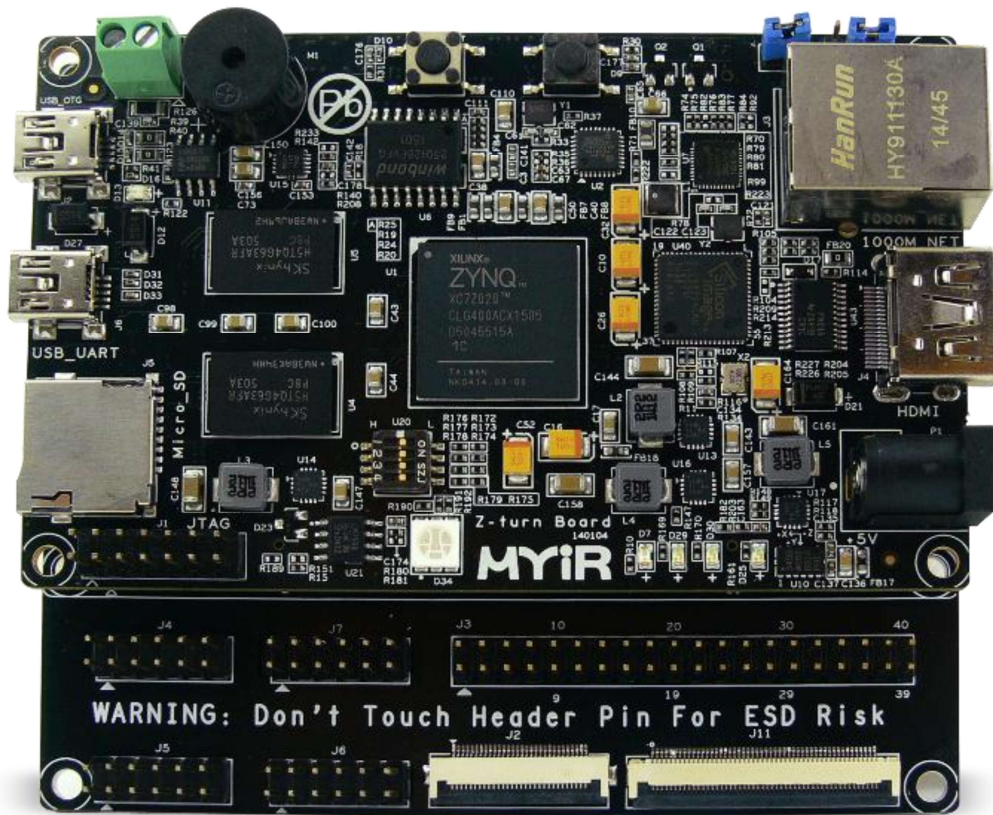


## 6 Setting up the Hardware



Please perform the following steps to setup the MYS\_C7z010/20 Starter Kit.

- Close the JP2 jumper and disconnect the JP1.
- Connect the Xilinx USB Platform Cable DLC9 to the J7 JTAG port and the USB port of the PC. This will provide JTAG connection to the board.
- Connect the MY-UART012U USB to COM cable to the J6 UART port and the USB port of the PC. This will provide USB-UART connection to the board.
- Connect 5V power supply to P1 on the MYS\_C7z010/20.
- Connect an Ethernet cable to the J8 RJ45 connector on the MYS\_C7z010/20 and the Gigabit Ethernet port of the PC.
- Start a Putty session and set the serial port parameters to 115200 baud rate, 8 bits, 1 stop bit, no parity and no flow control (please refer to the **Setting up the Host PC** section at the end of this document for installing the software driver for the USB-UART port and setting up the UART).
- Set the IP address of your PC to **192.168.1.1** with subnet mask of **255.255.255.0**.

## 6.1 MIO Pin introduction

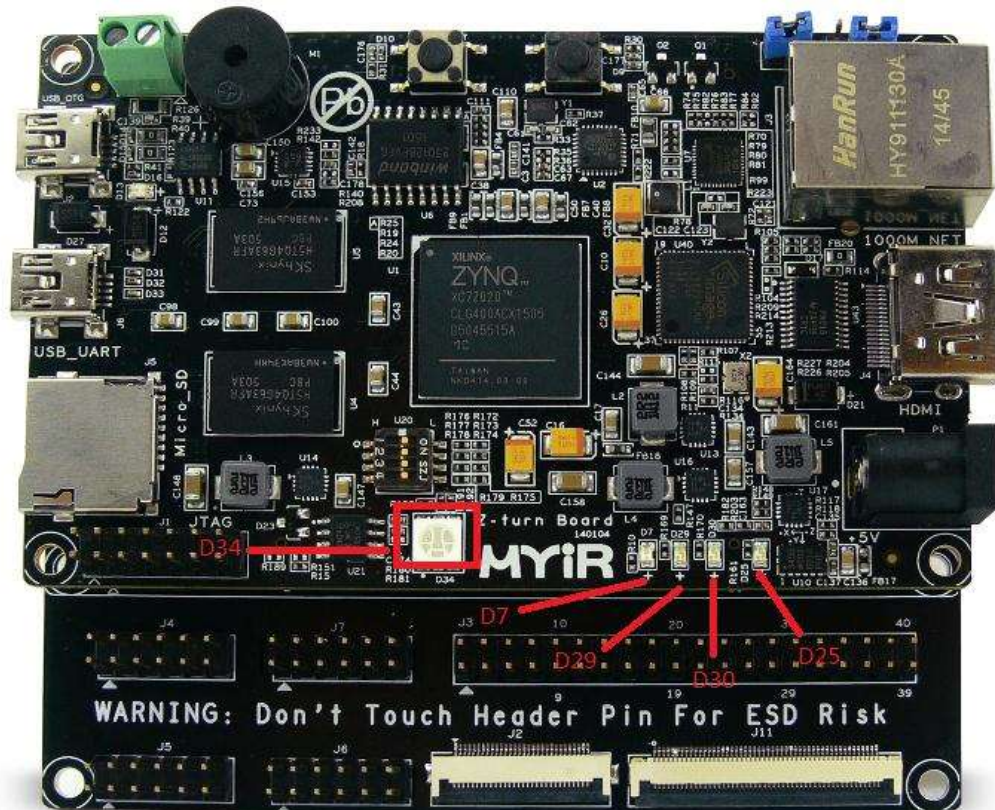
PS_CLK	PS_clock	E7
PS_REST	PS_reset	B10
MIO0	PS_User_LED1	E6
MIO1	QSPI_Flash_CS	A7
MIO2	QSPI_Flash_D0	B8
MIO3	QSPI_Flash_D1	D6
MIO4	QSPI_Flash_D2	B7
MIO5	QSPI_Flash_D3	A6
MIO6	QSPI_Flash_sclk	A5
MIO9	PS_User_LED2	B5
MIO10	UART0_RX	E9
MIO11	UART0_TX	C6
MIO12	I2C_CLK	D9
MIO13	I2C_SDA	E8
MIO14	CAN0_RX	C5
MIO15	CAN0_TX	C8
MIO16	RGMIIO_TX_CLK	A19
MIO17	RGMIIO_TX_D0	E14
MIO18	RGMIIO_TX_D1	B18
MIO19	RGMIIO_TX_D2	D10

MIO20	RGMII0_TX_D3	A17
MIO21	RGMII0_TX_EN	F14
MIO22	RGMII0_RX_CLK	B17
MIO23	RGMII0_RX_D0	D11
MIO24	RGMII0_RX_D1	A16
MIO25	RGMII0_RX_D2	F15
MIO26	RGMII0_RX_D3	A15
MIO27	RGMII0_RX_EN	D13
MIO28	USB1HS_D4	C16
MIO29	USB1HS_DIR	C13
MIO30	USB1HS_STP	C15
MIO31	USB1HS_NXT	E16
MIO32	USB1HS_D0	A14
MIO33	USB1HS_D1	D15
MIO34	USB1HS_D2	A12
MIO35	USB1HS_D3	F12
MIO36	USB1HS_CLK	A11
MIO37	USB1HS_D5	A10
MIO38	USB1HS_D6	E13
MIO39	USB1HS_D7	C18
MIO40	SDIO0_CLK	D14

MIO41	SDIO0_CMD	C17
MIO42	SDIO0_D1	E12
MIO43	SDIO0_D2	A9
MIO44	SDIO0_D3	F13
MIO45	SDIO0_D4	B15
MIO46	SDIO0_CD	D16
MIO47	SDIO0_WP	B14
MIO48	USB_UART1_TX	B12
MIO49	USB_UART1_RX	C12
MIO50	PS_501_SW1	B13
MIO51	PS_501_RESET_OUTN	B9
MIO52	MDIO_CK	C10
MIO53	MDIO_DATA	C11

## 6.2 LED Description

编号	说明	
D7	JTAG program written indicator light	Write successfully, turn red light on
D25	Power indicator on core board	Normal blue light
D29	Can be used by users to connect to the PS side E6	Normal light green
D30	Can be used by users to connect to the PS side B5	Normal light green light
D34	Can be used by users, connected to the PL end, trichromatic lights, R14, Y16, Y17	Red, green, blue





## 6.3 Development board model introduction

JP1	JP2	mode
OFF	ON	SD card boot mode
ON	ON	QSPI_FLASH mode starts

SD card start mode, JP1 open, JP2 closed



When we start from QSPI, the JP1 closes and the JP2 closes

