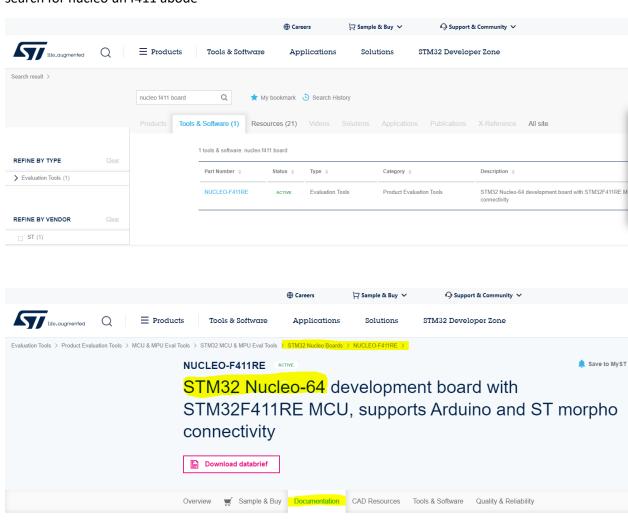
Embedded system using Arm Cortex M4

1- open the ST microtronics website.

https://www.st.com/content/st com/en.html

2- search for nucleo uh f411 abode



Technical Notes & Articles

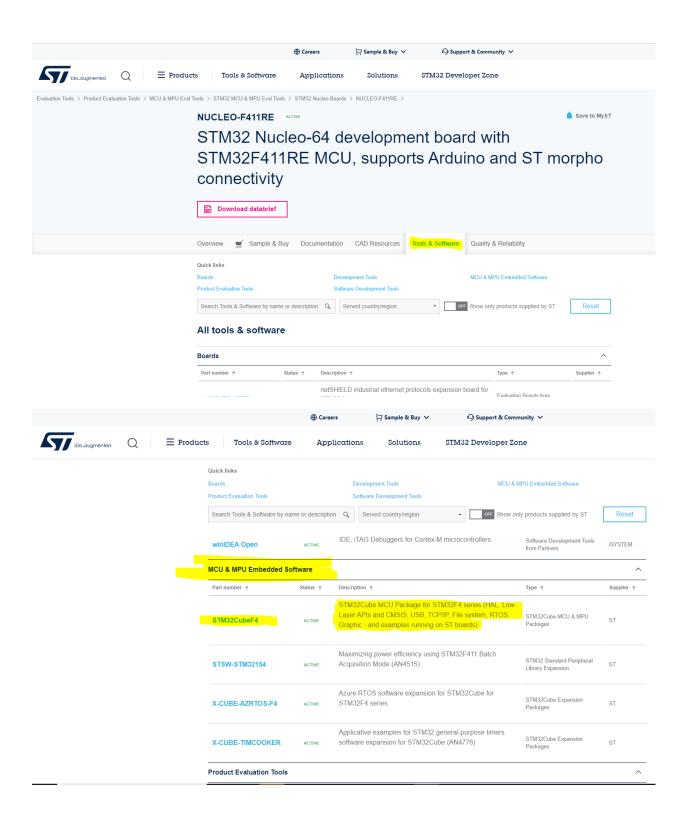
Evaluation Board Terms of Use

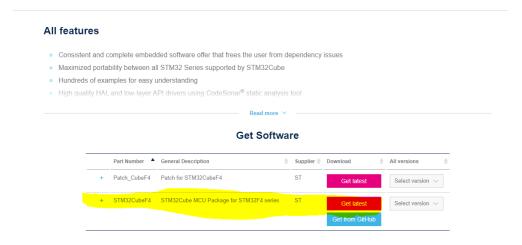
User Manuals
Product Certifications

Product Specifications

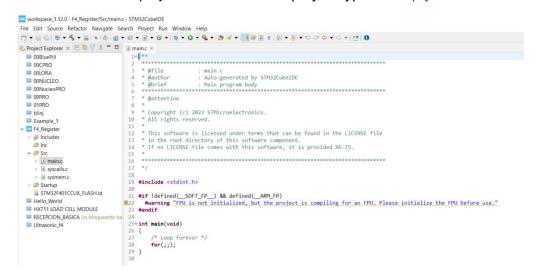
All resources

Expand all categories

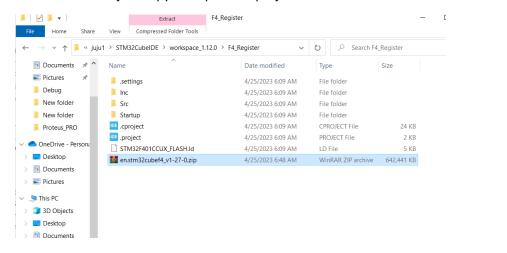




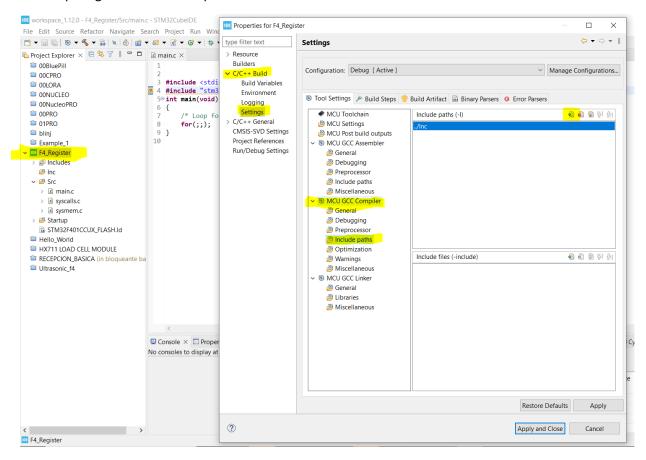
3- create a new stm32 project then choose the project type as empty.

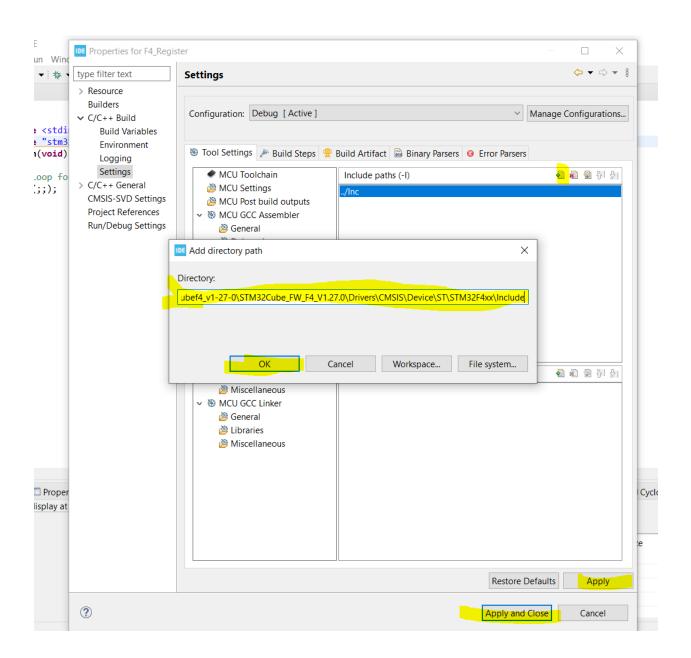


4- After downloads just copy then paste in project folder.



5- right click go for right click go for properties >> c/c++ build >> click settings >> click MCU GCC compiler go for the include paths.





- 7- click build the project to check.
- 8- right click go for open declaration [need to configure the symbol okay F4 11x you just choose this copy this symbol copy this symbol okay

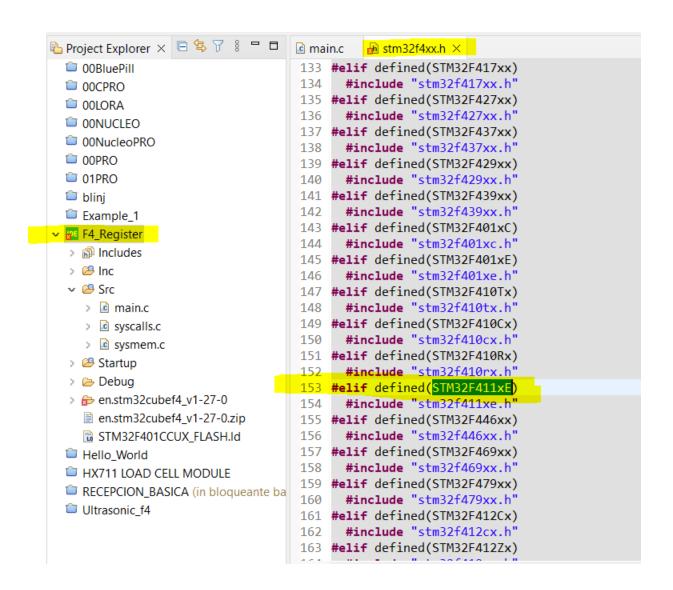
```
Project Explorer × 🖹 🧏 🎖 🖁 🗖 🖟 main.c 🔒 stm32f4xx.h ×
 00BluePill
                                   19/**
 © 00CPRO
                                        * @file stm32f4xx.h
 00LORA
                                        * @author MCD Application Team
                                   4
 © 00NUCLEO
                                        * @brief CMSIS STM32F4xx Device Peripheral Access Layer Header File.
                                   5
 00NucleoPRO
 OOPRO
                                                   The file is the unique include file that the application programmer
 a 01PRO
                                   8
                                                  is using in the C source code, usually in main.c. This file contains:
                                   9
 i blinj
                                                    - Configuration section that allows to select:
 Example 1
                                   10
                                                      - The STM32F4xx device used in the target application
                                   11
                                                       - To use or not the peripheral's drivers in application code(i.e.

√ 

■ F4_Register

                                   12
                                                        code will be based on direct access to peripheral's registers
  > 🔊 Includes
                                                        rather than drivers API), this option is controlled by
                                   13
  > 🕮 Inc
                                   14
                                                         "#define USE_HAL_DRIVER"
  v 🕮 Src
                                   15
    > 🖻 main.c
                                   16
    > 🖻 syscalls.c
                                   17
                                   18
    > 🖻 sysmem.c
                                       * Copyright (c) 2017 STMicroelectronics.
                                   19
  > 🕮 Startup
                                       * All rights reserved.
                                   20
  > 🗁 Debug
                                   21
  > & en.stm32cubef4_v1-27-0
                                       \ensuremath{^{*}} This software is licensed under terms that can be found in the LICENSE file
                                   22
    en.stm32cubef4_v1-27-0.zip
                                       * in the root directory of this software component.
                                   23
                                       * If no LICENSE file comes with this software, it is provided AS-IS.
    STM32F401CCUX_FLASH.Id
                                   24
 Hello_World
                                   25
                                   26
 HX711 LOAD CELL MODULE
                                   27
 RECEPCION_BASICA (in bloqueante ba
                                   28
 Ultrasonic_f4
                                   29⊖/** @addtogroup CMSIS
                                       * @{
                                   30
                                       */
                                   31
                                                                              X | ⊕ ⊕ 5 | ∰ ∰ ≡ ∰ □ → □ → □
                                 CDT Build Console [F4_Register]
                                         #error "Please select first the target STM32F4xx device used in your applicati^
                                make: *** [Src/subdir.mk:25: Src/main.o] Error 1
                                 "make -j4 all" terminated with exit code 2. Build might be incomplete.
                                07:18:15 Build Failed. 2 errors, 0 warnings. (took 249ms)
```

9- copy this symbol



Select the symbol you need.

```
🖰 Project Explorer 🗴 🕒 🥰 🎖 🖁 🗖 🗖
                                   i main.c

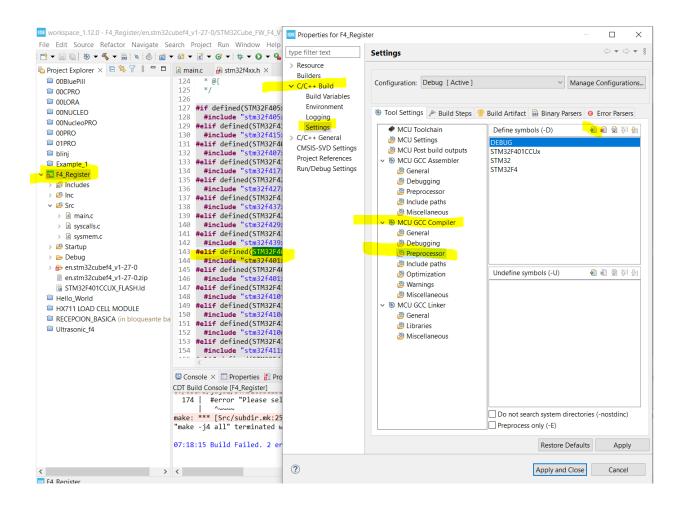
₱ stm32f4xx.h ×

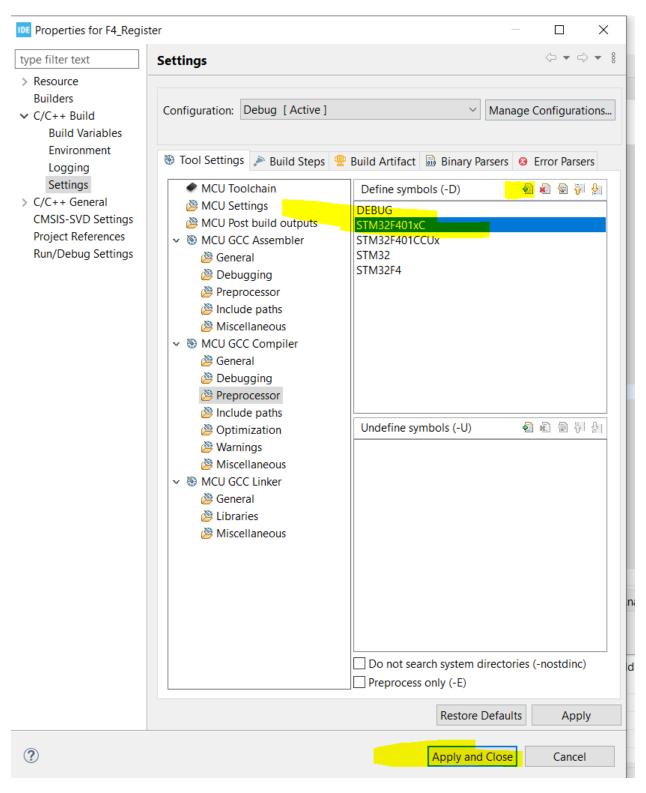
  00BluePill
                                   124
                                   125
  © 00CPRO
                                   126
  00LORA
                                   127 #if defined(STM32F405xx)
  © 00NUCLEO
                                          #include "stm32f405xx.h"
                                   128
  00NucleoPRO
                                   129 #elif defined(STM32F415xx)
  OOPRO
                                   130
                                          #include "stm32f415xx.h"
  O1PRO
                                   131 #elif defined(STM32F407xx)
                                   132
                                          #include "stm32f407xx.h"
  i blinj
                                   133 #elif defined(STM32F417xx)
  Example_1
                                          #include "stm32f417xx.h"
                                   134

√ F4_Register

                                   135 #elif defined(STM32F427xx)
  > 🛍 Includes
                                   136
                                          #include "stm32f427xx.h"
  > 🕮 Inc
                                   137 #elif defined(STM32F437xx)
  🗸 🐸 Src
                                          #include "stm32f437xx.h"
                                   138
    > @ main.c
                                   139 #elif defined(STM32F429xx)
    > c syscalls.c
                                   140
                                          #include "stm32f429xx.h"
                                   141 #elif defined(STM32F439xx)
     > 🖻 sysmem.c
                                   #include "stm32f439xx.h"
  > 🐸 Startup
                                   143 #elif defined(STM32F401xC)
  > 🗁 Debug
                                   144 #include "stm32f401xc.h"
  >  en.stm32cubef4_v1-27-0
                                   145 #elif defined(STM32F401xE)
    en.stm32cubef4_v1-27-0.zip
                                   146
                                          #include "stm32f401xe.h"
                                   147 #elif defined(STM32F410Tx)
    STM32F401CCUX_FLASH.Id
                                          #include "stm32f410tx.h"
                                   148
  Hello_World
                                   149 #elif defined(STM32F410Cx)
  HX711 LOAD CELL MODULE
                                   150
                                          #include "stm32f410cx.h"
  RECEPCION_BASICA (in bloqueante ba
                                   151 #elif defined(STM32F410Rx)
  Ultrasonic f4
                                          #include "stm32f410rx.h"
                                   152
                                   153 #elif defined(STM32F411xE)
                                          #include "stm32f411xe.h"
```

10- right click go for right click go for properties >> c/c++ build >> click settings >> click MCU GCC compiler >> preprocessor >> then add your symbol.



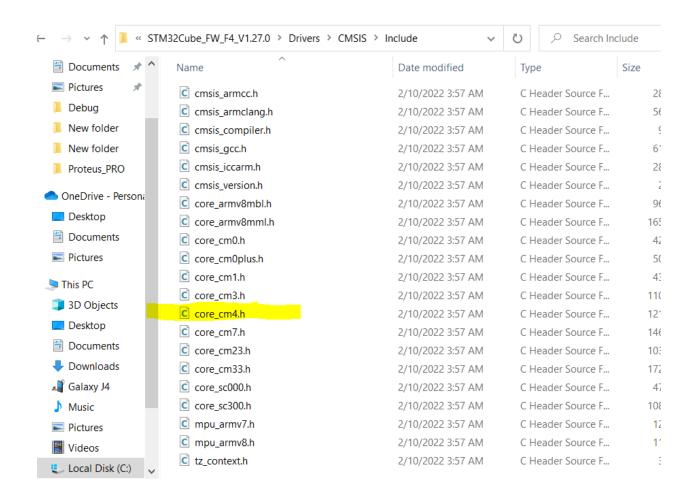


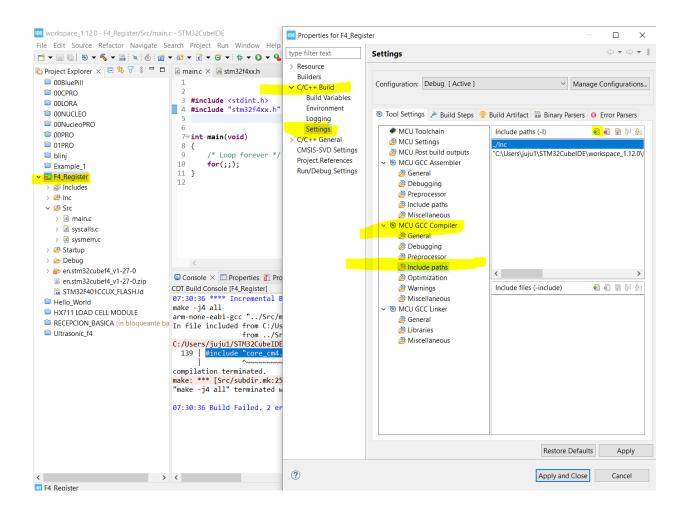
- 11- click build the project to check.
- 12- #include "core_cm4.h"

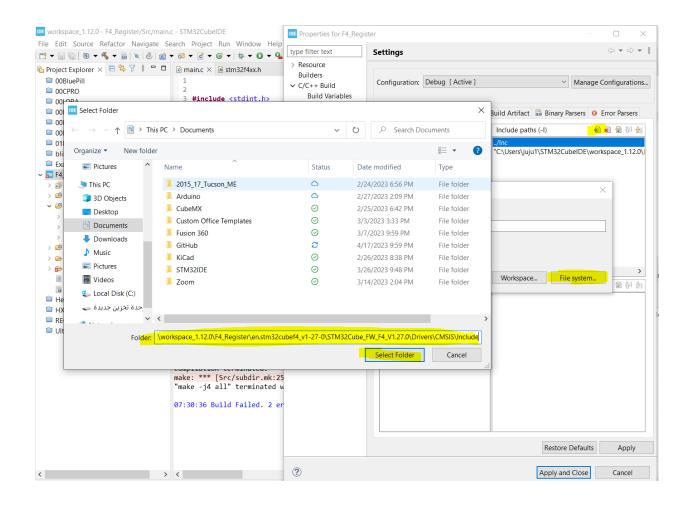
```
i main.c × i stm32f4xx.h
  1
 2
  3 #include <stdint.h>
  4 #include "stm32f4xx.h"
  6
  7⊖ int main(void)
 8 {
  9
        /* Loop forever */
 10
        for(;;);
 11 }
 12
                                               ※ | ⊕ ⊕ 🕏 | 📰 🔐 = 🗟 🗐 💌 🗐 🕶 🗂 🕶 🗆
🗐 Console 🗡 🔳 Properties 🔡 Problems 🙇 Tasks
CDT Build Console [F4_Register]
07:30:36 **** Incremental Build of configuration Debug for project F4_Register ****
make -j4 all
arm-none-eabi-gcc "../Src/main.c" -mcpu=cortex-m4 -std=gnu11 -g3 -DDEBUG -DSTM32F401xC
In file included from C:/Users/juju1/STM32CubeIDE/workspace_1.12.0/F4_Register/en.stm32
                 from ../Src/main.c:4:
C:/Users/juju1/STM32CubeIDE/workspace_1.12.0/F4_Register/en.stm32cubef4_v1-27-0/STM32Cu
  139 | #include "core_cm4.h"
                                         /* Cortex-M4 processor and core peripherals *
compilation terminated.
make: *** [Src/subdir.mk:25: Src/main.o] Error 1
"make -j4 all" terminated with exit code 2. Build might be incomplete.
07:30:36 Build Failed. 2 errors, 0 warnings. (took 246ms)
```

13- Add path

 $[C:\Users\juju1\STM32CubeIDE\workspace_1.12.0\F4_Register\en.stm32cubef4_v1-27-0\STM32Cube_FW_F4_V1.27.0\Drivers\CMSIS\Include]$







14- click build the project to check.

```
Project Explorer × □ 🥏 🎖 📅 □ 🚨 main.c × 庙 stm32f4xx.h
  00BluePill
  © 00CPRO
                                 3 #include <stdint.h>
  00LORA
                                4 #include "stm32f4xx.h"
  © 00NUCLEO
                                 5
  00NucleoPRO
                                 6

□ 00PRO
                                 7⊖ int main(void)
 © 01PRO
                                8 {
 i blinj
                                       /* Loop forever */
                                10
                                      for(;;);
  Example_1
                                11 }

√ IDE F4_Register

                                12
  > 🐉 Binaries
  > 🔊 Includes
  > 🕮 Inc
                                                                          X | ↓ ↑ 5 | □ □ □ = □ □ | □ | ■ □ ■
                               □ Console × □ Properties । Problems । Tasks
  v 🕮 Src
                               CDT Build Console [F4_Register]
    > 🖻 main.c
                               07:38:34 **** Incremental Build of configuration Debug for project F4 Register
    > 🖻 syscalls.c
                               make -j4 all
    > 🖻 sysmem.c
                               arm-none-eabi-gcc "../Src/main.c" -mcpu=cortex-m4 -std=gnu11 -g3 -DDEBUG -DSTM
  > 🕮 Startup
                               arm-none-eabi-gcc "../Src/syscalls.c" -mcpu=cortex-m4 -std=gnu11 -g3 -DDEBUG -
  > 🗁 Debug
                               arm-none-eabi-gcc "../Src/sysmem.c" -mcpu=cortex-m4 -std=gnu11 -g3 -DDEBUG -DS
  arm-none-eabi-gcc -o "F4_Register.elf" @"objects.list" -mcpu=cortex-m4 -T"C:
    en.stm32cubef4_v1-27-0.zip
                               Finished building target: F4 Register.elf
    STM32F401CCUX_FLASH.Id
                               arm-none-eabi-size F4 Register.elf
  Hello World
                               arm-none-eabi-objdump -h -S F4_Register.elf > "F4_Register.list"
  HX711 LOAD CELL MODULE
                                                        dec
                                                                hex filename
                                  text
                                        data
                                                bss
  RECEPCION_BASICA (in bloqueante ba
                                  656
                                                       2232
                                                                8b8 F4_Register.elf
                                         8
                                                1568
  Ultrasonic_f4
                               Finished building: default.size.stdout
                               Finished building: F4 Register.list
                               07:38:36 Build Finished. 0 errors, 0 warnings. (took 1s.504ms)
```