

Cycle	Instruction Retired	Reason
1	NOP	No instruction retired yet
2	NOP	No instruction retired yet
3	NOP	No instruction retired yet
4	NOP	No instruction retired yet
5	lbi r0, 0	N/A
6	lbi r5, 43	N/A
7	lbi r6, 43	N/A
8	lbi r7, 43	N/A
9	ld r1, r0, 0	N/A
10	NOP	RAW dependency - r1 (ld in EX, st in Decode)
11	NOP	RAW dependency - r1 (ld in MEM, st in Decode)
12	st r5, r1, 0	N/A
13	ld r1, r0, 2	N/A
14	NOP	RAW dependency - r1 (ld in EX, st in Decode)
15	NOP	RAW dependency - r1 (ld in MEM, st in Decode)
16	st r6, r1, 1	N/A
17	ld r1, r0, 4	N/A
18	NOP	RAW dependency - r1 (ld in EX, st in Decode)
19	NOP	RAW dependency - r1 (ld in MEM, st in Decode)
20	st r7, r1, 1	N/A
21	halt	N/A