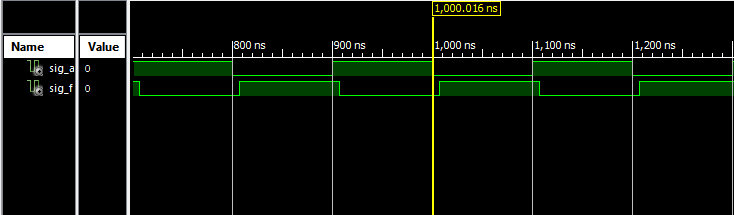
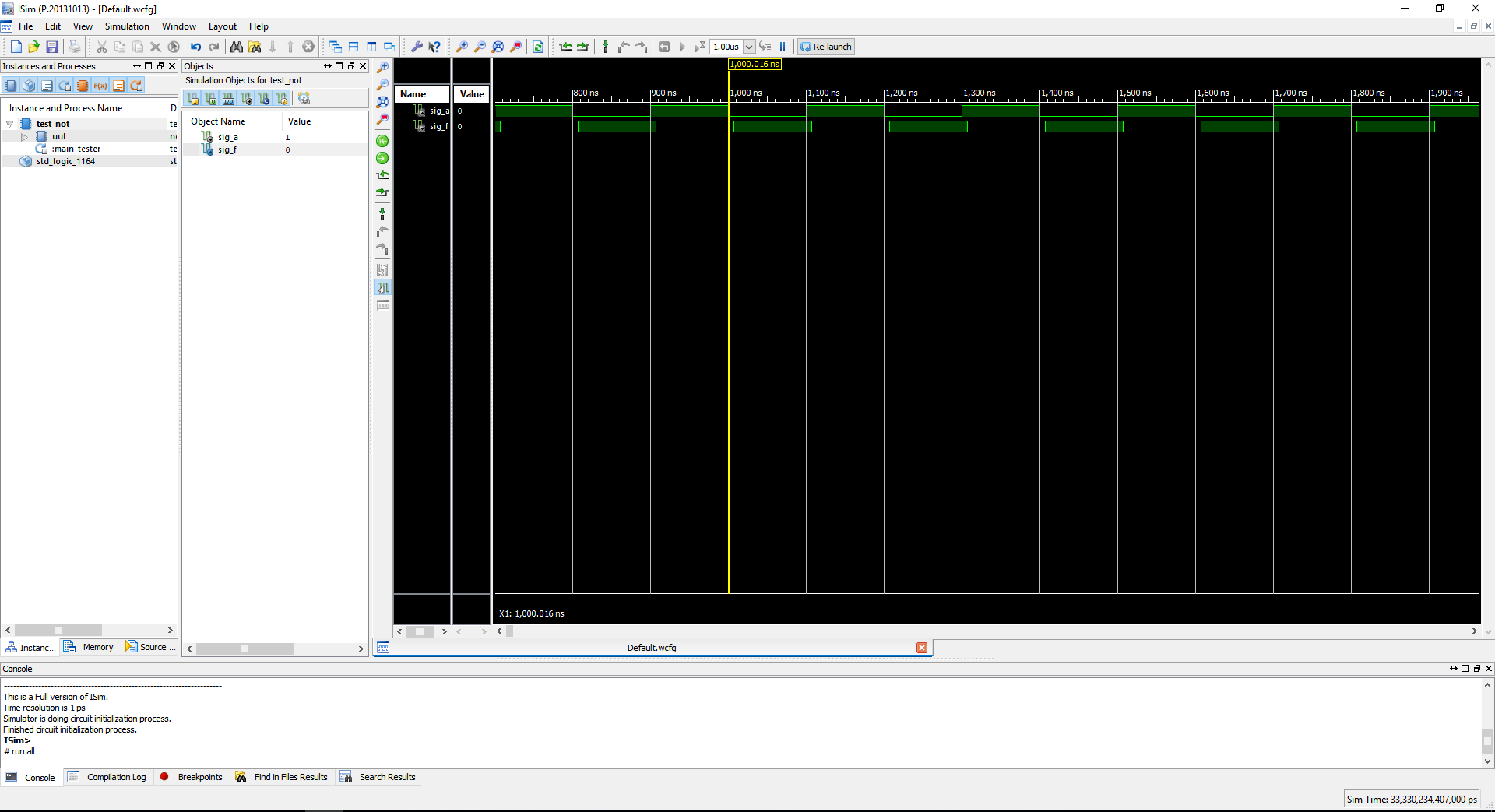
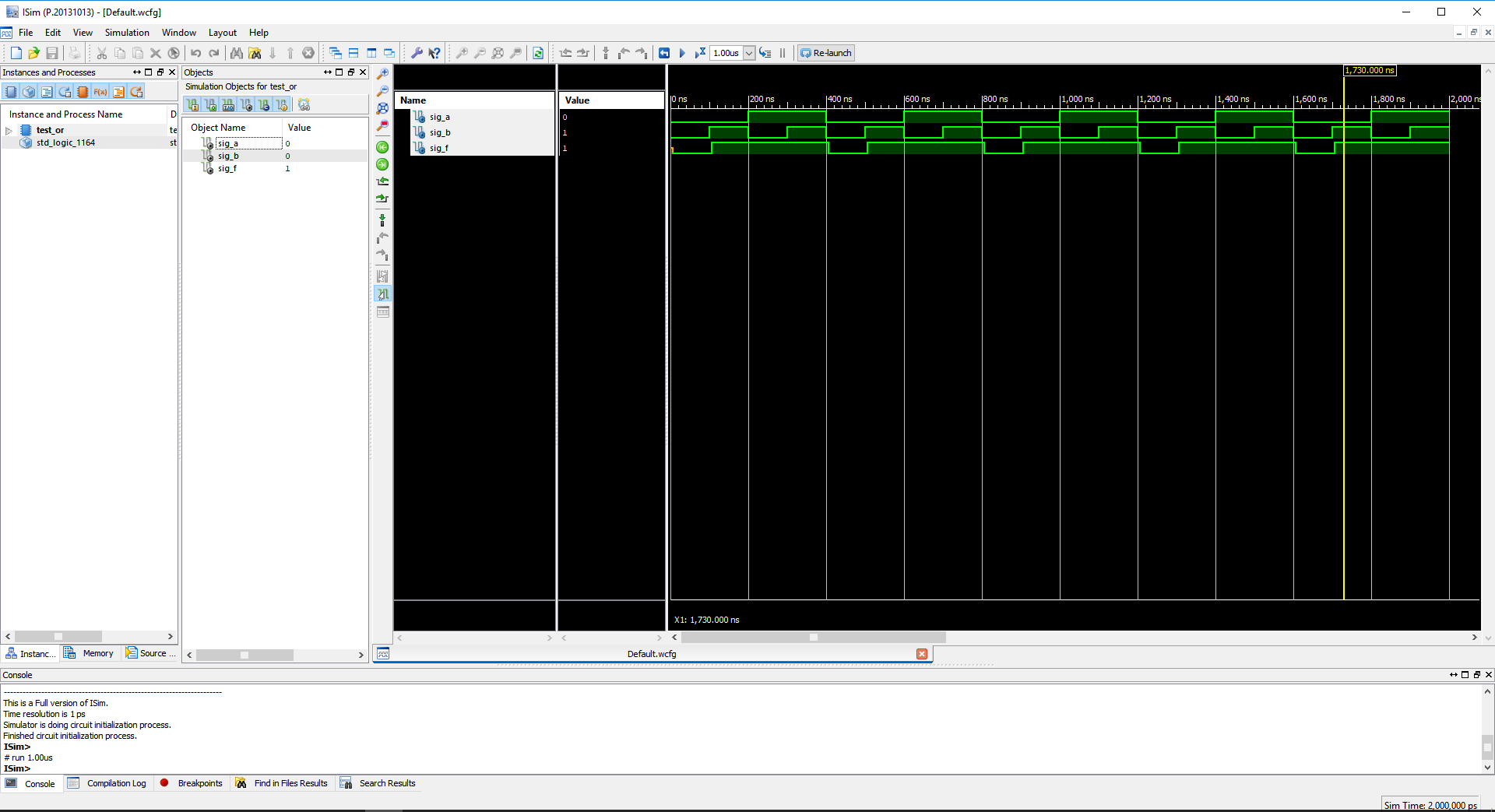
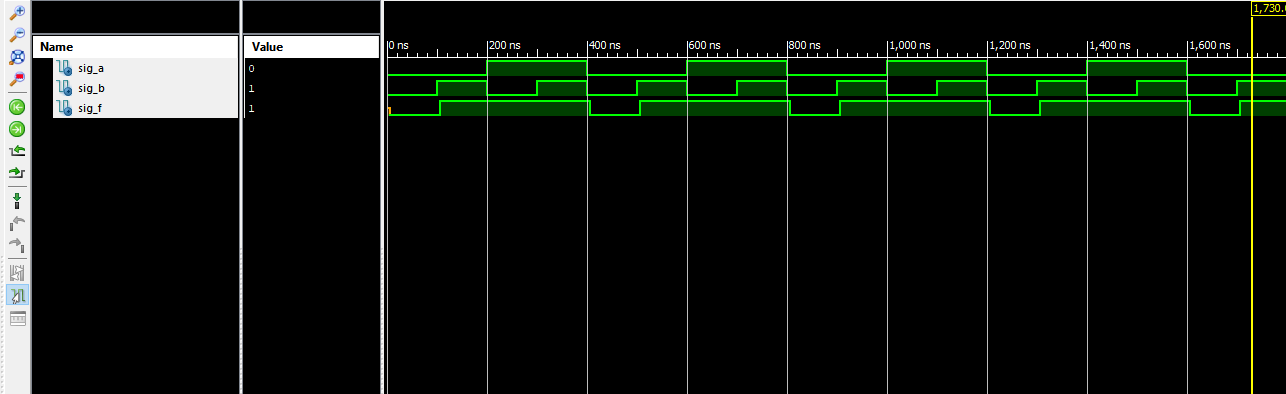
NOT Gate

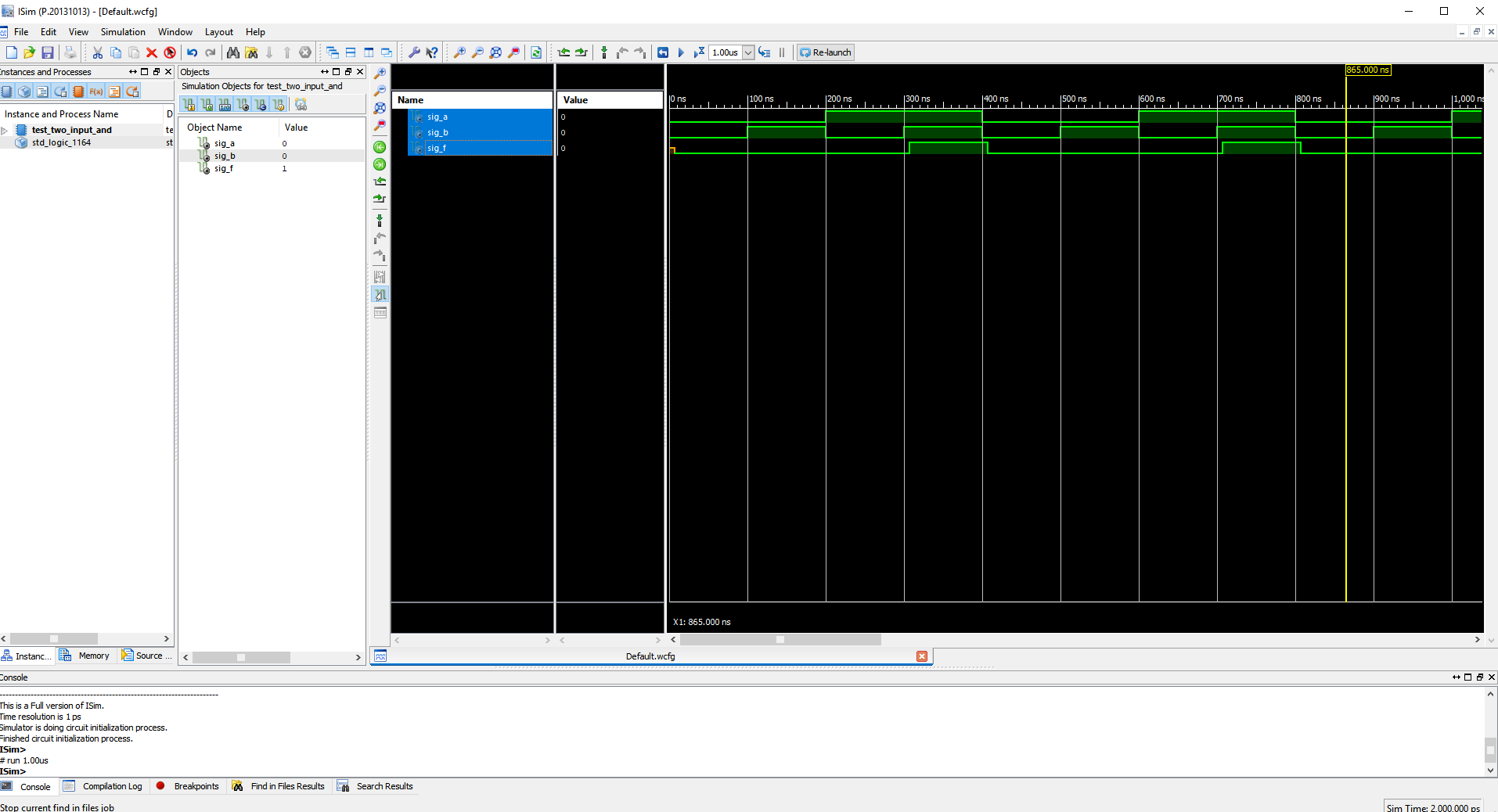


OR Gate



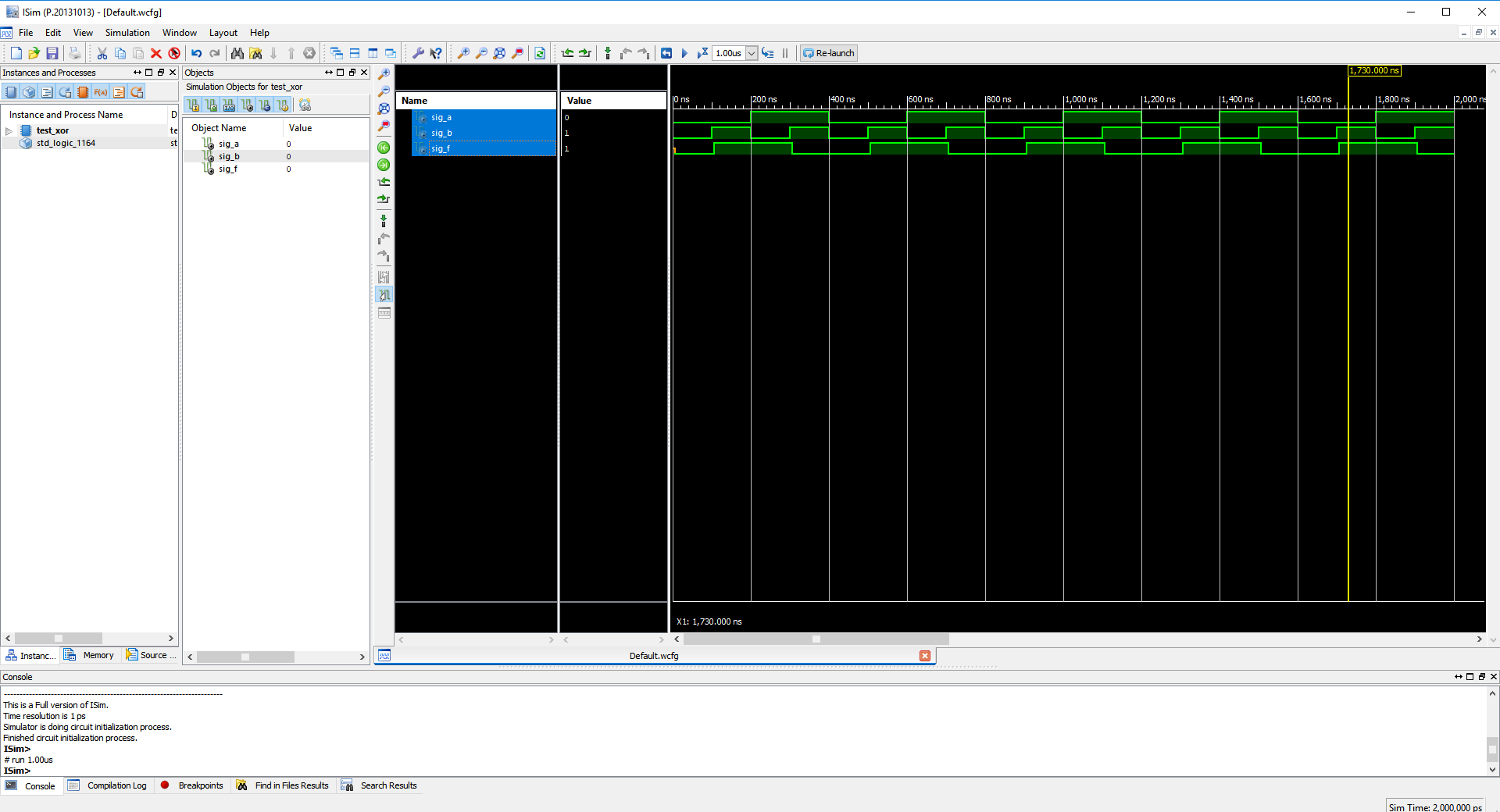


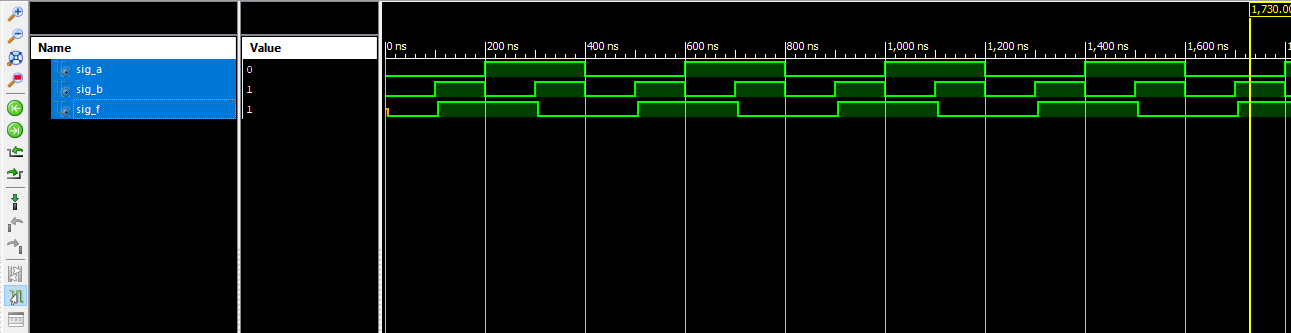
AND Gate



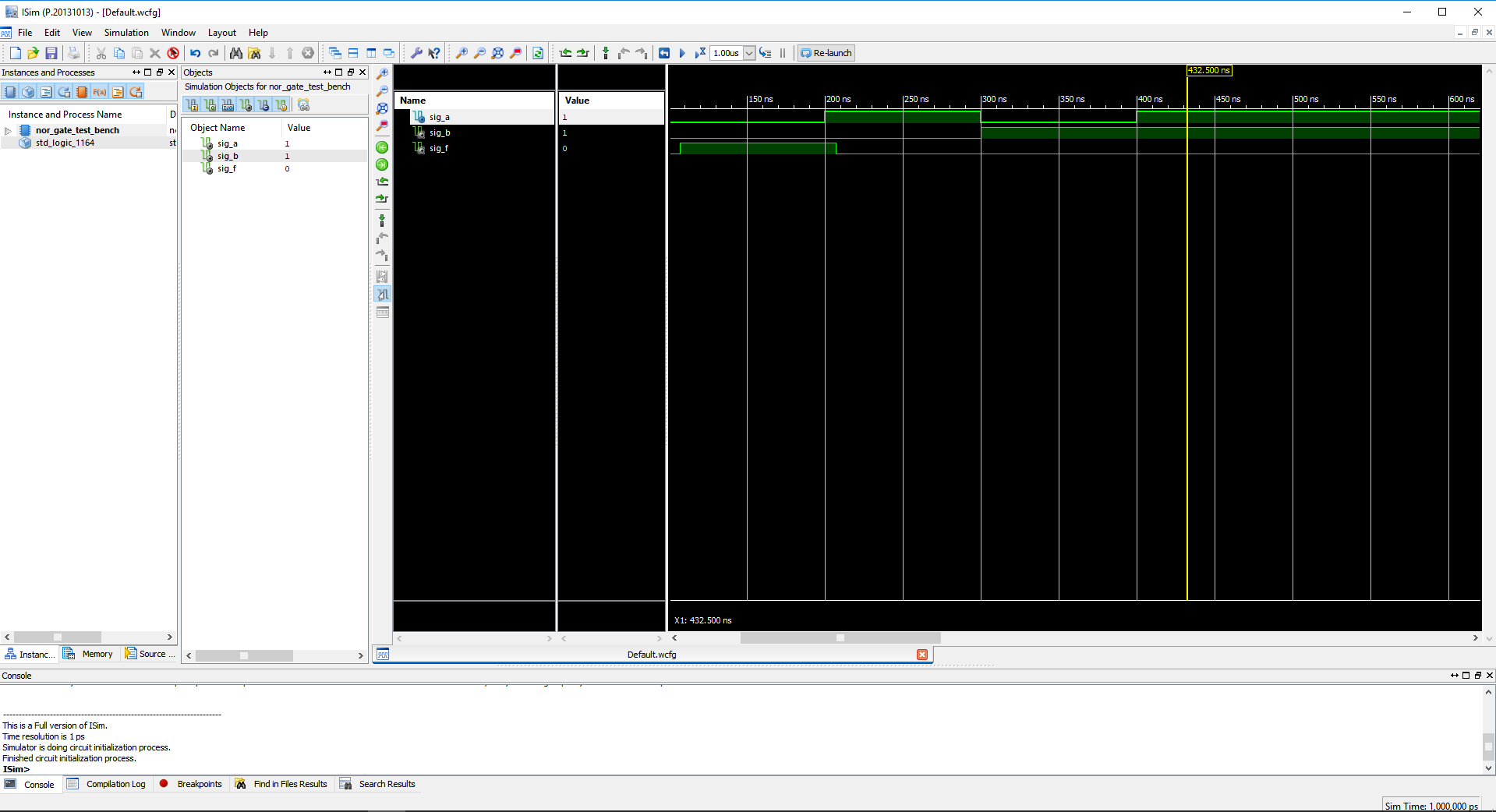


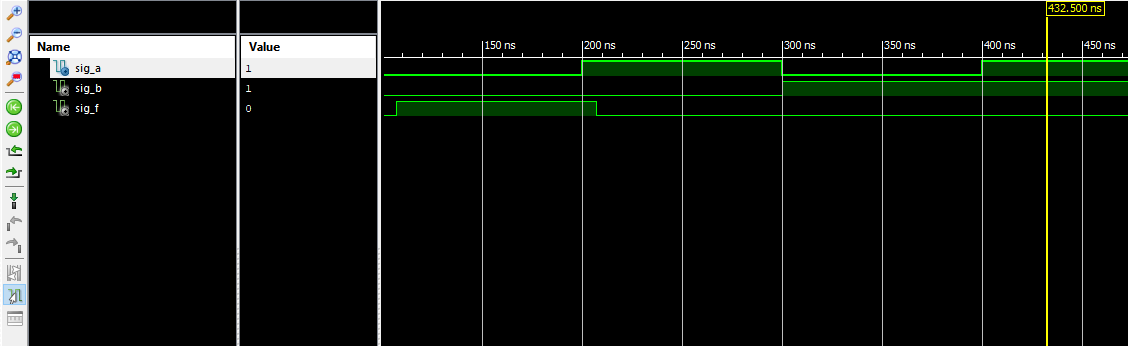
XOR Gate



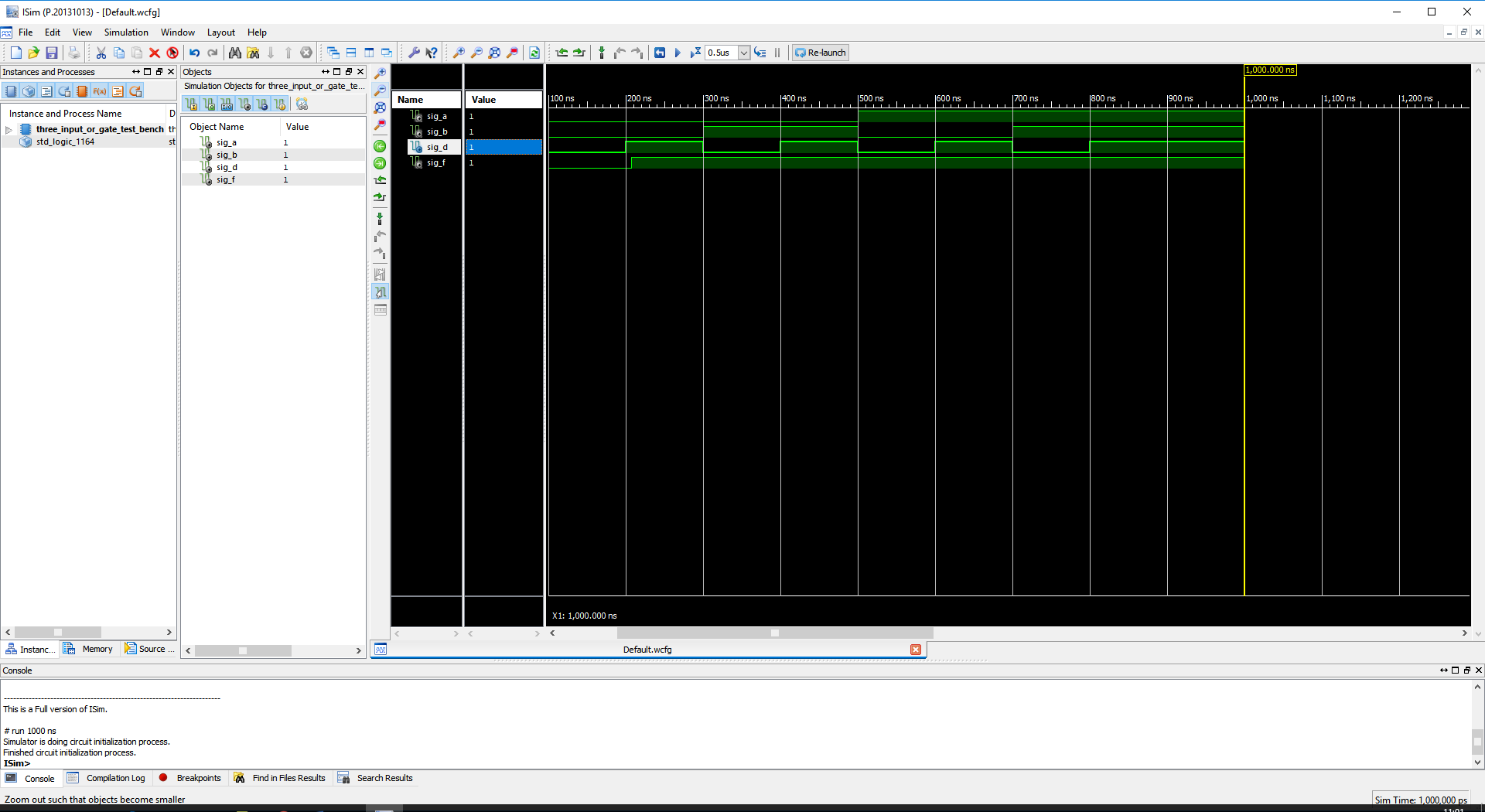


Nor gate



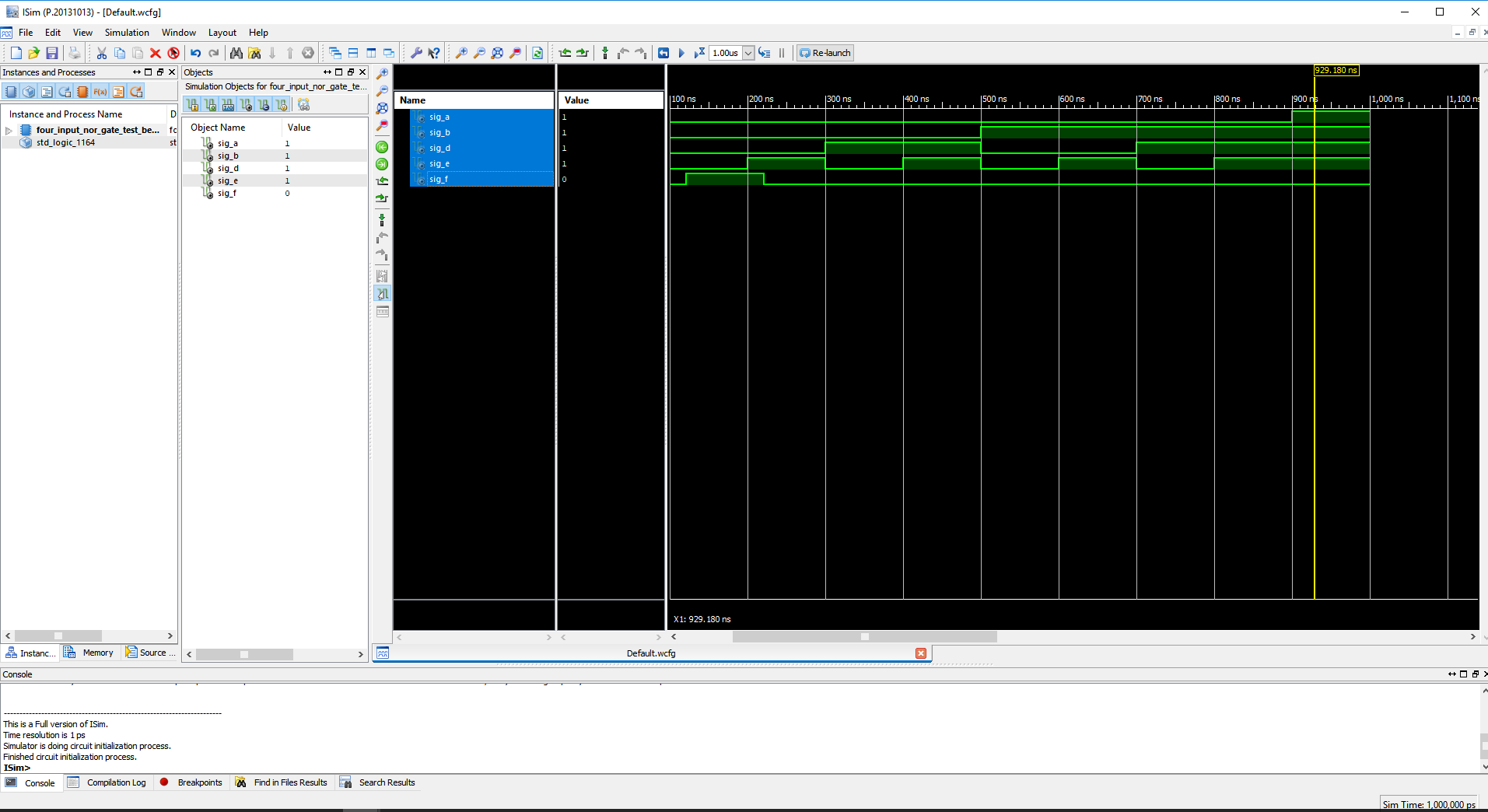


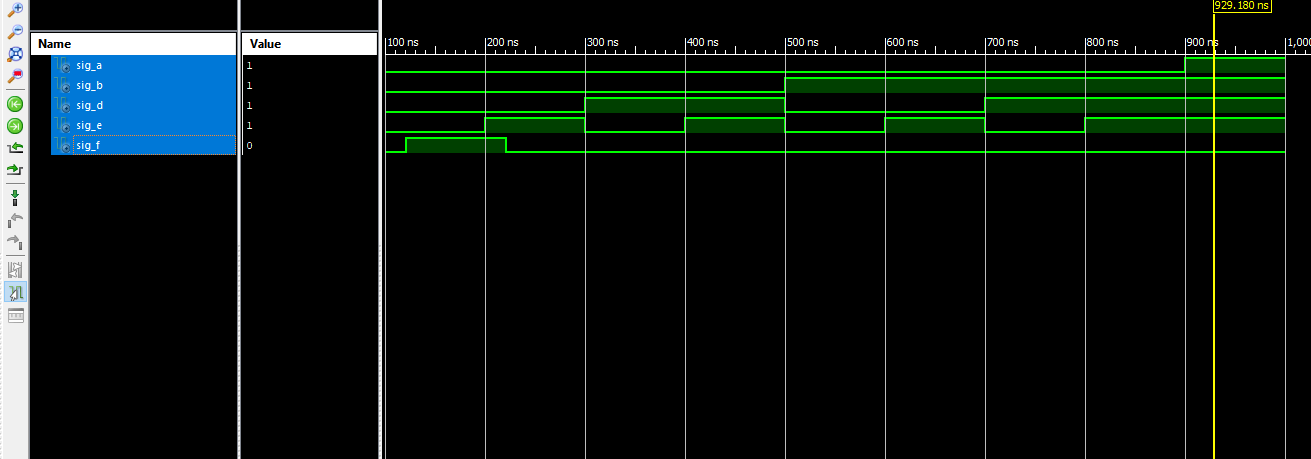
Three input or gate



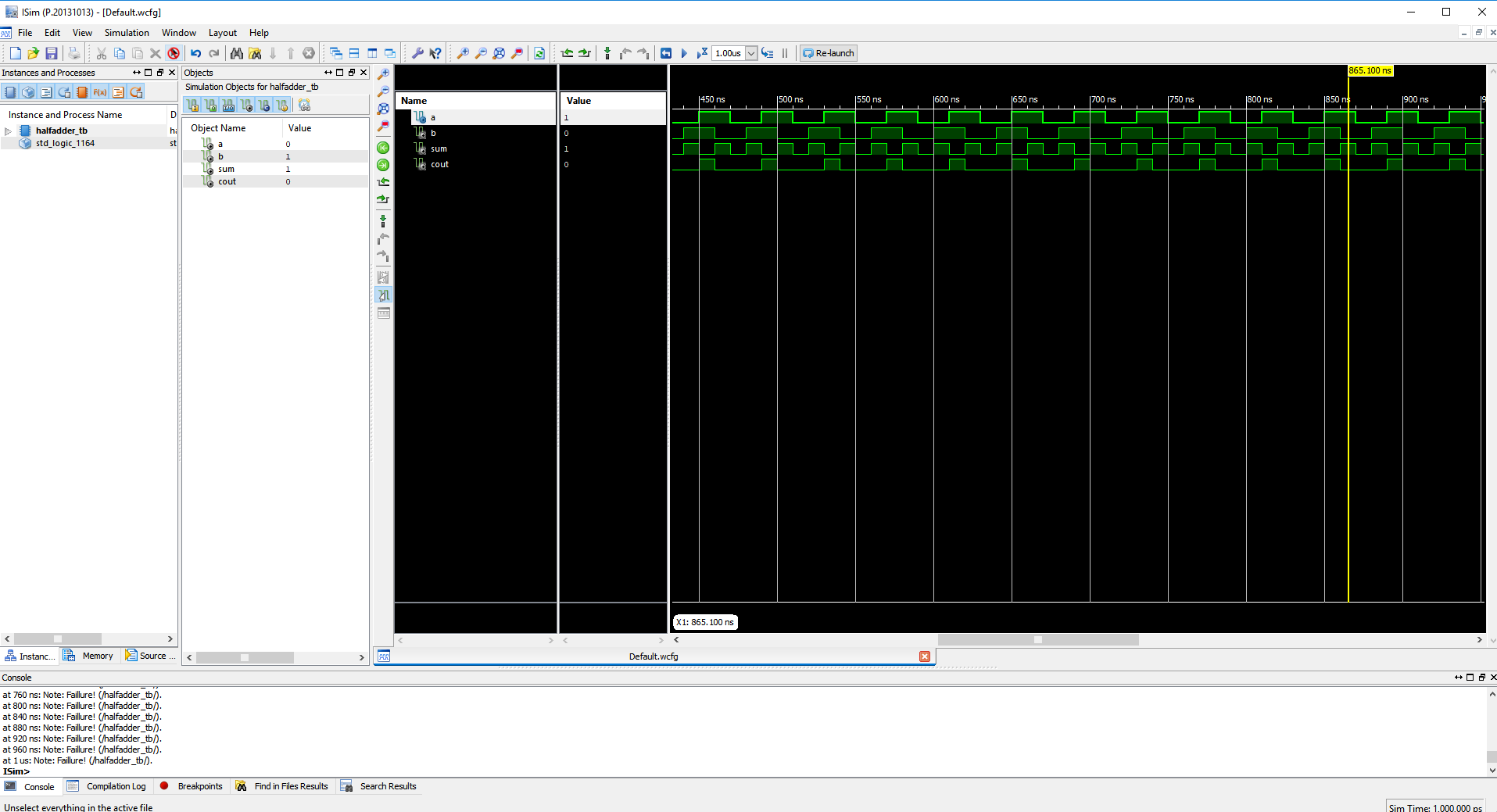


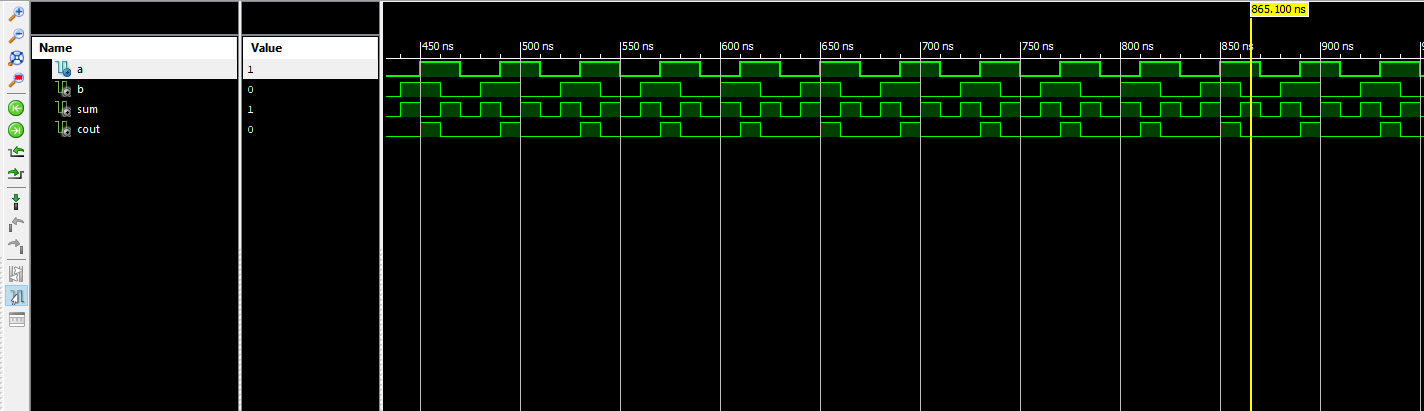
Four input nor



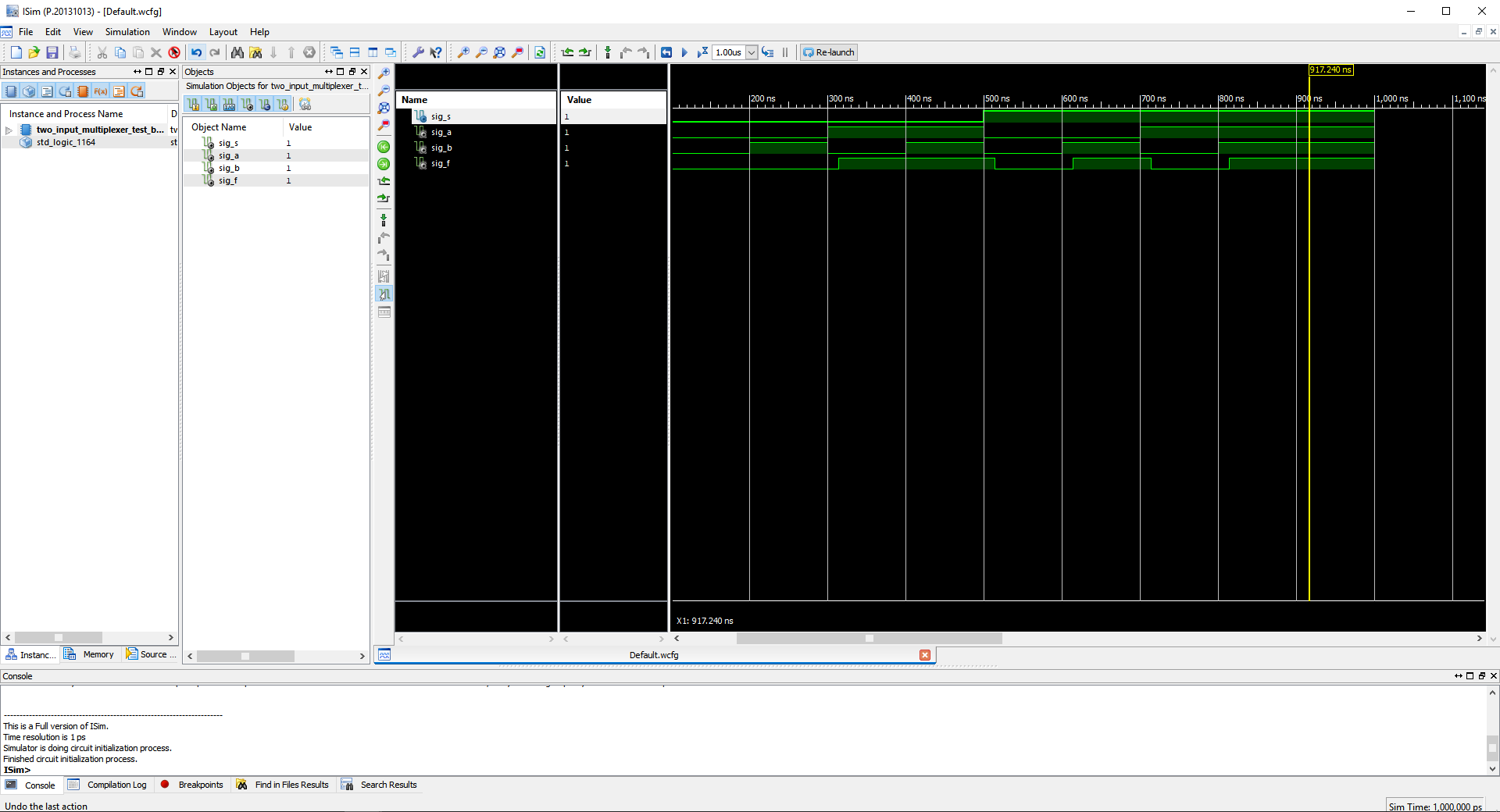


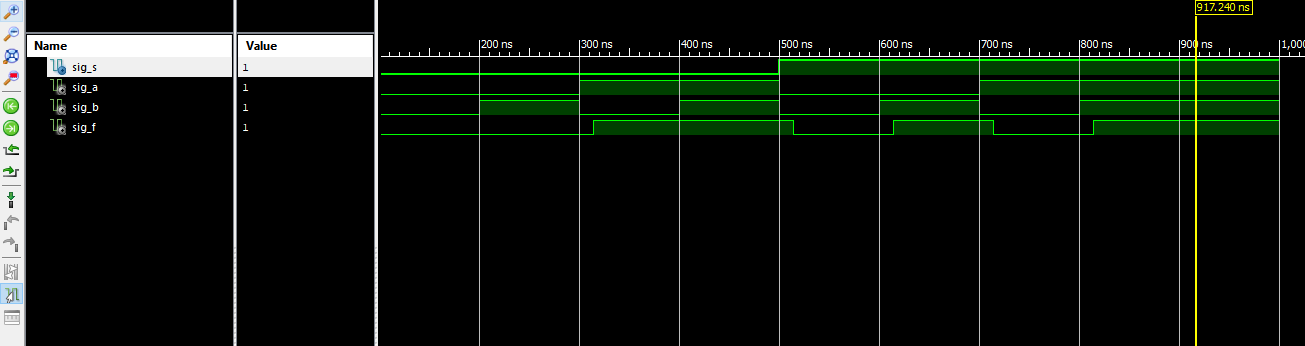
Half adder



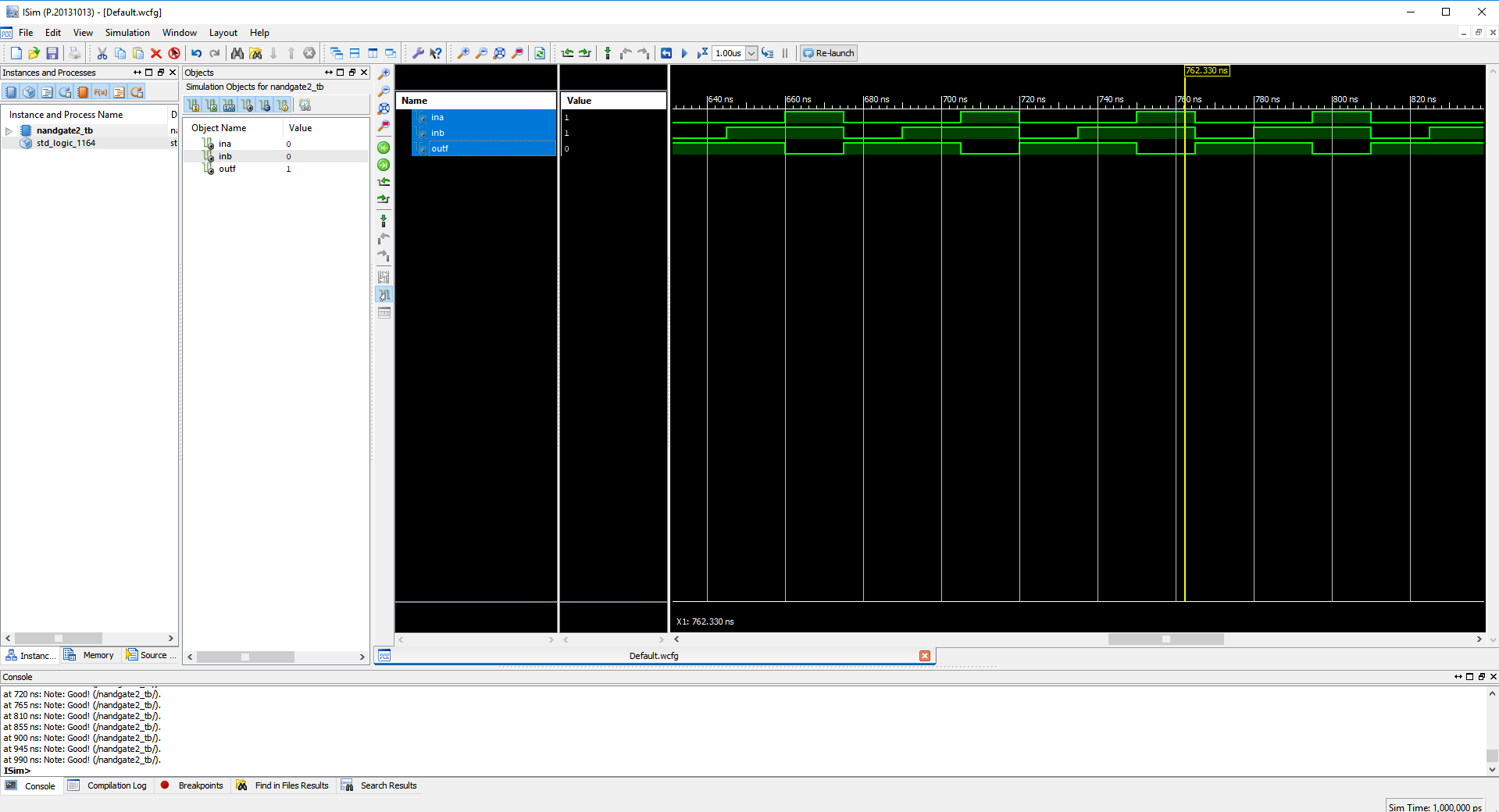


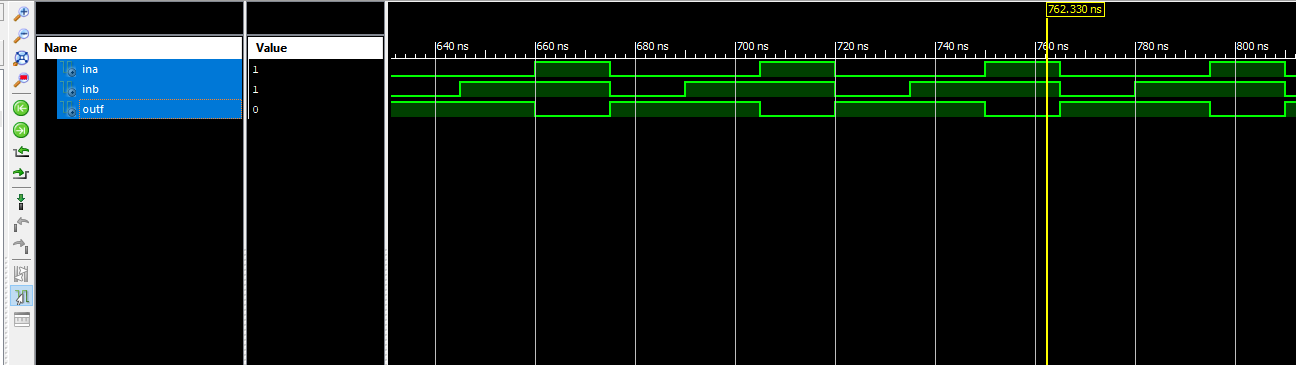
Two input multiplexer





Nand gate





Full adder

