PRIYADARSHINI COLLEGE OF ENGINEERING, NAGPUR
DEPARTMENT OF COMPUTER TECHNOLOGY
ACADEMIC SESSION: 2022 – 2023 (EVEN SEMESTER)
Assignment – 2

Subject	: Compilers Design	Semester	:	VI A/B
Subject Teachers	: Dr.(Mrs) Snehal S. Golait/Mr. C.R.Pote	Assignment No	:	2
Unit	: III,IV and V	Date of Display	:	3/04/2023

CO3   Explain various forms of intermediate code and will be able to demonstrate use of SDTS to translate elementary programming constructs.

CO4
      Describe various optimization techniques and will be able to develop simple code generators.

CO5   Explain storage allocation methods, error recovery techniques and will be able to apply various error recovery techniques in parsers.

	Questions	Mapping with COs	BT Level
1	Translate following statement into Three Address Code If x< y and a>b	CO3	II
2	Differentiate between Loop Optimization and Local Optimization	CO4	IV
3	Explain Peephole optimization with their characteristics.	CO4	II
4	What are issues in generating target code?	CO5	I
5	Construct the given expression into DAG a + a * (b-c)+ (b-c) *d	CO5	III

Dr.(Mrs).Snehal S. Golait / Mr.C.R.Pote   **Dr. (Mrs.) N. M. Thakare**

(Subject Teachers)   **(HOD, CT)**