Verification of competency for ELTR courses

The purpose of these performance assessment activities is to verify the competence of a prospective transfer student with prior work experience and/or formal education in electronics, but where the depth and rigor of the prior learning is unknown. New students with no prior work experience or formal education in electronics must take all ELTR courses and are not allowed to challenge any by completing these activities.

All activities are performance-based. That is, the individual must perform all necessary predictions (calculations) based on conditions and component values specified by the instructor, then must actually build the circuit and use properly test equipment to verify those predictions. Each assessment is pass/fail. Either the individual is able to successfully predict, build, and test the circuit, or the individual is not able to predict, build, and/or test the circuit.

Prospective transfer students are allowed to review the verification activities prior to performing them, but will receive no help from the instructor. They are to study and prepare on their own. Electronic components and test equipment for the activities will be provided by the instructor. No books are allowed during the verification activity, but one page of notes may be used (per activity).

"Given conditions" for each activity will be randomly provided by the instructor at the time of verification, not prior. This way, prospective transfer students must prove mastery of the analysis techniques by successfully working through a set of given conditions they have not seen before.

Competence verification activities

ELTR100 - DC 1

Performance assessment: Series-parallel resistor circuit (Question 1)

ELTR105 - DC 2

Performance assessment: RC time constant circuit (Question 2)

ELTR110 - AC 1

Performance assessment: Passive RC filter circuit with specified cutoff (Question 3)

ELTR115 - AC 2

Performance assessment: Auto-transformer (Question 4)

ELTR120 - Semiconductors 1

Performance assessment: AC-DC power supply (Question 5)

ELTR125 – Semiconductors 2

Performance assessment: BJT amplifier with specified gain (Question 6)

ELTR130 - Opamps 1

Performance assessment: Op-amp amplifier with specified gain (Question 7)

ELTR135 - Opamps 2

Performance assessment: Active RC filter circuit with specified cutoff (Question 8)

$\underline{ELTR140-Digital~1}$

Performance assessment: Logic circuit from truth table (Question 9)

ELTR145 - Digital 2

Performance assessment: Flip-flop counter circuit (Question 10)

Competency: Series-parallel DC resistor circuit Version:			
$\begin{array}{c c} \hline \textbf{Schematic} \\ \hline \\ \textbf{V}_{\text{supply}} & = \\ \hline \\ \textbf{R}_{4} \\ \hline \\ \textbf{R}_{4} \\ \hline \end{array} \qquad \begin{array}{c} \textbf{R}_{1} \\ \textbf{R}_{2} \\ \textbf{R}_{3} \\ \hline \\ \textbf{R}_{4} \\ \hline \end{array}$			
Given conditions			
$\mathbf{V}_{ ext{supply}}= \qquad \qquad \mathbf{R}_1= \qquad \qquad \mathbf{R}_2= \qquad \qquad \mathbf{R}_3= \qquad \qquad \mathbf{R}_4=$			
Parameters			
$\begin{array}{c ccccccccccccccccccccccccccccccccccc$			
Suppose component open other shorted What will happen in the circuit?			

<u>file 01606</u>

Use circuit simulation software to verify your predicted and measured parameter values.

Notes 1

Use a variable-voltage, regulated power supply to supply any amount of DC voltage below 30 volts. Specify standard resistor values, all between 1 k Ω and 100 k Ω (1k5, 2k2, 2k7, 3k3, 4k7, 5k1, 6k8, 8k2, 10k, 22k, 33k, 39k 47k, 68k, 82k, etc.).

Competency: RC disch	arge circuit	Version:
V _{supply} =	construction switch	R ₁ V Meter
Given conditions		
$V_{supply} =$	$C_1 =$	$R_1 =$
$t_1 =$	$t_2 =$	$t_3 =$
Parameters		
$egin{array}{c c} V_{t1} & & & & & \\ \hline V_{t2} & & & & & \\ \hline V_{t3} & & & & & \\ \hline \end{array}$	leasured	
Calculations		

file 01648

Use circuit simulation software to verify your predicted and measured parameter values.

Notes 2

I recommend choosing resistor and capacitor values that yield time constants in the range that may be accurately tracked with a stopwatch. I also recommend using resistor values significantly less than the voltmeter's input impedance, so that voltmeter loading does not significantly contribute to the decay rate.

Good time values to use (t_1, t_2, t_3) would be in the range of 5, 10, and 15 seconds, respectively.

Competency: Passive RC filter circuit design Version:			
Description			
Design and build an RC filter circuit, either high pass or low pass, with the specified cutoff frequency.			
Given conditions			
(instructor checks one)			
$f_{-3dB} =$ High-pass Low-pass			
Parameters			
Predicted Measured			
$ m f_{-3dB}$			
$\theta_{ ext{-3dB}}$			
Schematic			
V _{signal}			

 $\underline{\mathrm{file}\ 02095}$

Use circuit simulation software to verify your predicted and measured parameter values.

Notes 3

Use a sine-wave function generator for the AC voltage source. Specify a cutoff frequency within the audio range.

I recommend setting the function generator output for 1 volt, to make it easier for students to measure the point of "cutoff". You may set it at some other value, though, if you so choose (or let students set the value themselves when they test the circuit!).

I also recommend having students use an oscilloscope to measure AC voltage in a circuit such as this, because some digital multimeters have difficulty accurately measuring AC voltage much beyond line frequency range. I find it particularly helpful to set the oscilloscope to the "X-Y" mode so that it draws a thin line on the screen rather than sweeps across the screen to show an actual waveform. This makes it easier to measure peak-to-peak voltage.

Competency: Auto-transformers	Version:		
Description			
Connect a step-down transformer as an auto-transformer, to either boost or buck the input voltage. There must be no exposed line power conductors!			
Given conditions			
$V_{ m supply} =$	Boost		
Transformer step-down ratio =	Buck		
Schematic			
	Vout		
(Be sure to note the transformer's polarity using dot convention)			
Parameters			
Predicted Measured Voutput			

<u>file 02131</u>

Use circuit simulation software to verify your predicted and measured parameter values.

Notes 4

The *real* challenge in this assessment is for students to determine their transformers' "polarities" before connecting them to the AC voltage source! For this, they should have access to a small battery and a DC voltmeter (at their desks).

You may use a Variac at the test bench to provide variable-voltage AC power for the students' transformer circuits. I recommend specifying load resistance values low enough that the load current completely "swamps" the transformer's magnetization current. This may mean using wire-wound power resistors instead of $\frac{1}{4}$ watt carbon composition resistors.

Note that there may very well be a shock hazard associated with this circuit! Be sure to take this into consideration when specifying load resistor values. You may also want to use low supply voltage levels (turn the Variac way down).

Competency: AC-DC p	ower supply cir	cuit	Version:
Description			
Build a "brute force" AC-DC power supply circuit, consisting of a step-down transformer, full-wave bridge rectifier, capacitive filter, and load resistor.			
Given conditions			
$V_{ m supply} =$	$C_{filter} =$	$R_{load} =$	
Schematic			
Parameters			
Predicted	Measured		
V _{out(DC)}			
out(DC)			
V _{out(ripple)}			

<u>file 01622</u>

Use circuit simulation software to verify your predicted and measured parameter values.

Notes 5

Use a Variac at the test bench to provide variable-voltage AC power for the students' power supply circuits.

Question 6

Competency: Class-A BJT amplifier w/specified gain Version:

Description

Design and build a class-A BJT amplifier circuit with a voltage gain (A_V) that is within tolerance of the gain specified.

You may use a potentiometer to adjust the biasing of the transistor, to make the design process easier.

Given conditions

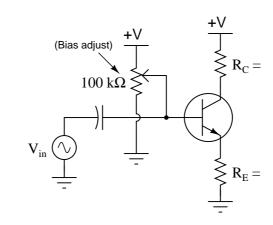
$$V_{in} =$$

$$+V = A_V =$$

$$A_{\rm v} =$$

$$\mathsf{Tolerance}_{\mathsf{A}_\mathsf{V}} =$$

Schematic



Parameters

Measured V_{in}

Calculated

$$\frac{A_{V(actual)} - A_{V(ideal)}}{A_{V(ideal)}} \times 100\%$$

<u>file 01935</u>

Use circuit simulation software to verify your predicted and measured parameter values.

Notes 6

Students are allowed to adjust the bias potentiometer to achieve class-A operation after calculating and inserting the resistance values R_C and R_E . However, they are not allowed to change either R_C or R_E once the circuit is powered and tested, lest they achieve the specified gain through trial-and-error!

A good percentage tolerance for gain is +/- 10%. The lower you set the target gain, the more accuracy you may expect out of your students' circuits. I usually select random values of voltage gain between 2 and 10, and I strongly recommend that students choose resistor values between 1 k Ω and 100 k Ω . Resistor values much lower than 1 k Ω lead to excessive quiescent currents, which may cause accuracy problems (r'_e drifting due to temperature effects).

Competency: Op-amp amplifier	circuit w/specif	fied gain Version:	
Description			
Design and build an op-amp amplifier circuit with a voltage gain (A_{ν}) that is within tolerance of the gain specified.			
Given conditions			
$V_{in} =$	Inverting		
A_V (ratio) =	Non-inverting		
$Tolerance_{A_{V}} =$			
Schematic Show all componen	t values!		
Parameters			
$\begin{array}{c c} & \text{Measured} \\ V_{\text{in}} & & & A_{V} \text{ (ratio)} \\ V_{\text{out}} & & & \text{Error}_{A_{V}} \end{array}$	Calculated	$\frac{A_{\text{V(actual)}} - A_{\text{V(ideal)}}}{A_{\text{V(ideal)}}} \times 100\%$	

 $\underline{\mathrm{file}\ 02132}$

Use circuit simulation software to verify your predicted and measured parameter values.

Notes 7

Competency: Active RC filter circuit design Version:			
Description			
Design and build an active RC filter circuit with a cutoff frequency specified by the instructor.			
Given conditions			
f _{-3dB} = High-pass Low-pass			
Schematic Show all component values!			
Parameters			
$\begin{array}{c c} & \text{Predicted} & \text{Measured} \\ \\ f_{\text{-3dB}} & \hline \end{array}$			

 $\underline{\mathrm{file}\ 02133}$

Use circuit simulation software to verify your predicted and measured parameter values.

Notes 8

Use a sine-wave function generator for the AC voltage source. Specify a cutoff frequency within the audio range.

I recommend setting the function generator output for 1 volt, to make it easier for students to measure the point of "cutoff". You may set it at some other value, though, if you so choose (or let students set the value themselves when they test the circuit!).

I also recommend having students use an oscilloscope to measure AC voltage in a circuit such as this, because some digital multimeters have difficulty accurately measuring AC voltage much beyond line frequency range. I find it particularly helpful to set the oscilloscope to the "X-Y" mode so that it draws a thin line on the screen rather than sweeps across the screen to show an actual waveform. This makes it easier to measure peak-to-peak voltage.

Competency: Gate circuit from truth table				Versio	n:
Truth table					
Given		,	Act	ual	_
A B C Output	_	В		Output	
0 0 0	0	0	0		
0 0 1 0	0	0	0		_
0 1 1	0	1	1		
1 0 0	1	0	0		-
1 0 1	1	0	1		
1 1 0	1	1	0		
1 1 1	1	1	1		
Schematic					
Concination					
R _{pullup} N _{DD} V _{DD} R _{pullup} C					R_{limit}

file 02134

Use circuit simulation software to verify your predicted and actual truth tables.

Notes 9

It should be noted that the input states in this circuit are defined by the voltage levels, not by the contact status. In other words, a closed contact equals a "low" (0) logic state.

Suggested truth tables include the following (encoded as Boolean SOP statements):

- $AB\overline{C} + ABC$
- $\overline{A}B\overline{C} + \overline{A}BC$
- $\bullet \ \overline{A}B\overline{C} + \overline{A}BC + \overline{A}\ \overline{B}\ \overline{C}$
- $A\overline{B}\overline{C} + A\overline{B}C$
- $AB\overline{C} + A\overline{B}\overline{C} + \overline{A}\overline{B}\overline{C}$
- $\overline{A}BC + \overline{A}\overline{B}C + \overline{A}\overline{B}\overline{C}$
- $ABC + \overline{A}BC + AB\overline{C}$
- $A\overline{B}C + \overline{A}\overline{B}C + \overline{A}\overline{B}\overline{C}$
- $ABC + A\overline{B}C + \overline{A}\overline{B}C$

I strongly recommend having students build their logic circuits with CMOS chips rather than TTL, because of the less stringent power supply requirements of CMOS. I also recommend drawing a combinational circuit using four gates, because this is the common number of two-input gates found on 14-pin DIP logic chips.

Competency: 4-bit flip-	flop counter circu	uit Version:
Description		
Build a 4-bit co	ounter circuit using	individual J-K flip-flops
Schematic		
	R_{limit} R_{limit}	$\begin{array}{c c} & & & \\ \hline \\ \text{it} & \\ \hline \end{array} \qquad \begin{array}{c} & \\ \\ \hline \end{array} \qquad \begin{array}{c} \\ \\ \hline \end{array} \qquad \begin{array}{c} \\ \\ \\ \\ \\ \\ \\ \\ \end{array} \qquad \begin{array}{c} \\ \\ \\ \\ \\ \\ \end{array} \qquad \begin{array}{c} \\ \\ \\ \\ \\ \\ \end{array} \qquad \begin{array}{c} \\ \\ \\ \\ \\ \\ \\ \end{array} \qquad \begin{array}{c} \\ \\ \\ \\ \\ \\ \\ \end{array} \qquad \begin{array}{c} \\ \\ \\ \\ \\ \\ \\ \\ \end{array} \qquad \begin{array}{c} \\ \\ \\ \\ \\ \\ \\ \\ \\ \end{array} \qquad \begin{array}{c} \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \end{array} \qquad \begin{array}{c} \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \end{array} \qquad \begin{array}{c} \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\$
Count sequence		
Time ↓	Predicted	Actual

 $\underline{\mathrm{file}\ 02135}$

Use circuit simulation software to verify your predicted and actual truth tables.

Notes 10

I strongly recommend having students build their logic circuits with CMOS chips rather than TTL, because of the less stringent power supply requirements of CMOS.