C Programming 1DT301

Lecture 2



ASM / C Comparison (Blinking LED on an ATmega328P)

```
.org 0x0000
  jmp main
main:
 ;----- Setup stack
 ldi r16, high(RAMEND);
 out SPH, r16
 ldi r16, low(RAMEND)
 out SPL, r16
  ;-----
  sbi DDRB, DDB5 ; Set Pin 5 to OUTPUT
loop:
  sbi PORTB, PB5; LED ON
 rcall Delay200ms
 cbi PORTB, PB5; LED OFF
 rcall Delay200ms
  rjmp loop
```

```
#define F_CPU 16000000UL

#include <avr/io.h>
#include <util/delay.h>

int main(void)
{
    DDRB = 1 << DDB5;

    while (1)
    {
        PORTB = PORTB | (1 << PB5);
        _delay_ms(200);
        PORTB = PORTB & ~(1 << PB5);
        _delay_ms(200);
    }

    return 1;
}</pre>
```

Delay200ms

```
; Generated by delay loop calculator
                       at http://www.bretmulvey.com/avrdelay.html
                       Delay 3 200 000 cycles
                      200ms at 16.0 MHz
                     Delay200ms:
                         ldi r18, 17
                             r19, 60
                         ldi
                         ldi
                              r20, 204
                                                                  push/pop required? Most likely!
Added by You
                     L1: dec r20
                         brne L1
                         dec r19
                         brne L1
                         dec r18
                         brne L1
                         ret
```

Float C -> ASM

```
int main(void)
{
  float myFloat = 0x1234;

  myFloat = myFloat + 1;

  return 1;
}
```

That call leads to some 200+ lines of Assembly code.

```
int main(void)
80:
                   cf 93
                                      push
                                                         r28
                   df 93
82:
                                      push
                                                         r29
                   00 d0
                                                                            ; 0x86 <main+0x6>
 84:
                                      rcall
                                                         .+0
                   00 d0
                                                         .+0
                                                                            ; 0x88 <main+0x8>
86:
                                     rcall
 88:
                   cd b7
                                     in
                                                         r28, 0x3d
                                                                            ;61
8a:
                   de b7
                                     in
                                                         r29, 0x3e
                                                                            ; 62
float myFloat = 0x1234;
                                     ldi
                                                         r24, 0x00
                                                                            ; 0
8c:
                   80 e0
8e:
                   90 ea
                                     ldi
                                                         r25, 0xA0
                                                                            ; 160
90:
                   a1 e9
                                     ldi
                                                         r26, 0x91
                                                                            ; 145
92:
                   b5 e4
                                     ldi
                                                         r27, 0x45
                                                                            ; 69
 94:
                   89 83
                                     std
                                                         Y+1, r24
                                                                            ; 0x01
 96:
                   9a 83
                                     std
                                                         Y+2, r25
                                                                            ; 0x02
 98:
                   ab 83
                                     std
                                                         Y+3, r26
                                                                            ; 0x03
                   bc 83
 9a:
                                      std
                                                         Y+4, r27
                                                                            ; 0x04
                   myFloat = myFloat + 1;
9c:
                   20 e0
                                      ldi
                                                         r18, 0x00
                                                                            ; 0
 9e:
                   30 e0
                                      ldi
                                                         r19, 0x00
                                                                            ; 0
                   40 e8
 a0:
                                     ldi
                                                         r20, 0x80
                                                                            ; 128
                   5f e3
                                     ldi
                                                         r21, 0x3F
a2:
                                                                            ; 63
a4:
                   6981
                                     ldd
                                                         r22, Y+1
                                                                            ;0x01
a6:
                   7a 81
                                      ldd
                                                         r23, Y+2
                                                                            ; 0x02
 a8:
                   8b 81
                                     ldd
                                                         r24, Y+3
                                                                            ; 0x03
                                     ldd
                                                         r25, Y+4
 aa:
                   9c 81
                                                                            ; 0x04
                                                         0xd0
                  0e 94 68 00
                                      call
                                                                            ; 0xd0 <__addsf3>
ac:
b0:
                   dc 01
                                      movw
                                                         r26, r24
                   cb 01
                                                         r24, r22
b2:
                                     movw
b4:
                   89 83
                                     std
                                                         Y+1, r24
                                                                            ; 0x01
                                                         Y+2, r25
                   9a 83
                                                                            ; 0x02
 b6:
                                      std
 b8:
                   ab 83
                                     std
                                                         Y+3, r26
                                                                            ; 0x03
ba:
                   bc 83
                                     std
                                                         Y+4, r27
                                                                            ; 0x04
                   return 1:
 bc:
                   81 e0
                                     ldi
                                                         r24, 0x01
                                                                            ; 1
 be:
                   90 e0
                                      ldi
                                                         r25, 0x00
                                                                            ; 0
                   0f 90
                                                         r0
 c0:
                                      pop
 c2:
                   0f 90
                                                         r0
                                      pop
 c4:
                   0f 90
                                                         r0
                                      pop
                   0f 90
 c6:
                                      pop
                                                         r0
                   df 91
                                                         r29
 c8:
                                      pop
                   cf 91
                                                         r28
 ca:
                                      pop
                   08 95
 cc:
                                      ret
```

Control Flow

Control flow

- Determines in what way your program flows.
- If the sun is shining and the temperature is more than 20 °C then go bathing, otherwise go sleeping at home.

if-else

```
if(logical expression)

→ statement1;
else

statement2;
```

What if more statements are needed?

else-if

```
true
                           false
if(logical expression 1)
      Statement1;
                           false
    true
else if (logical expression 2) -
      Statement2;
else
      Statement3;
```

switch

```
switch(expression)
{
   case constant1:
      break;
   case constant2:
      break;
   case constant_n:
      break;
   default:
}
```

Break can be omitted → new meaning!

Loops

Loops

- Used for repeating execution.
- There need to be a logical expression telling when to stop repeating.
 If not, the loop will go on forever.
- while, for, do-while
- goto + labels
- Considered to be bad¹.
- Compare to ASM

while

```
2. while(logical expression)

{

Statements;

Statements;
```

Executed zero or more times.

for

```
2. If true

2. If false

1. Start

4.

for(int cntr=0; cntr<5; cntr++)

{

Statements;
}

Statements;
```

for (more formal)

```
for(init expr;logical expr;loop expr)
{
         Statements;
}
Statements;
```

for vs while

```
for(int cntr=0; cntr<5; cntr++)
{
         Statements;
}
Statements;</pre>
```

break/continue

```
int cntr=0;
while(cntr<5)
{
    Statements;
    if(cntr % 2 == 0)
        break;

    if(cntr % 3 == 0)
        continue;

    Statements;
    cntr++;
}
> Statements;
```

Works with all loop constructs!

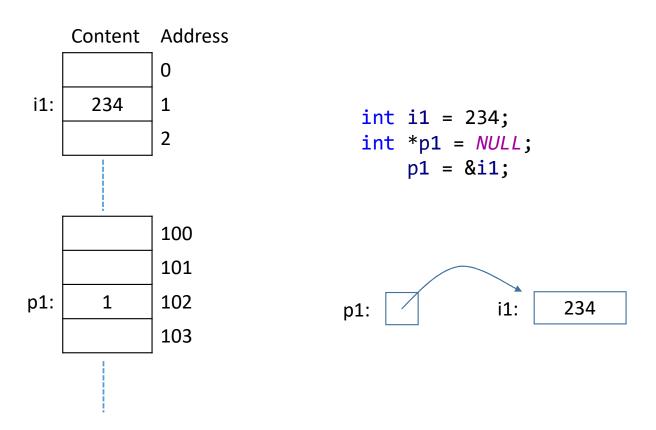
- Variable referring to a memory location.
- Useful when, for instance,
 - Allocating memory
 - Manipulating function arguments.
 - Pointing to different variables
- This is the same thing as in the Assembly language, but the notation is different!

- Declaring
 - type *name;
- Getting an address of a variable
 - Put & character in front of the variable.
- A pointers type is important.

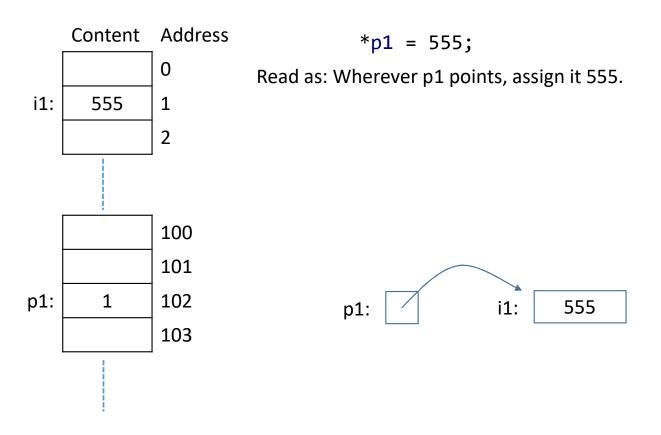
```
    int i1 = 234;
    int *p1 = NULL;
    p1 = &i1;
```

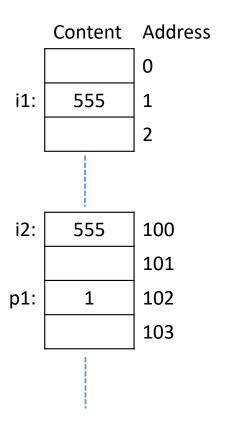
- 1. Declare a variable of type int.
- 2. Declare a pointer variable of type int.
- 3. Get the address of i1 and assign it to the pointer p1 (meaning: p1 points at i1 so you can reference i1 through p1)

Pointer (memory layout)

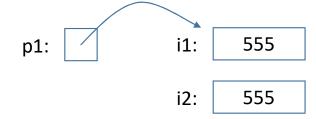


Dereferencing

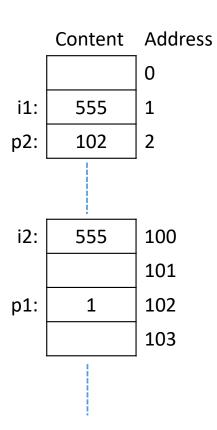




Read as: wherever p1 points get that value and assign it to i2.



Pointer to a pointer



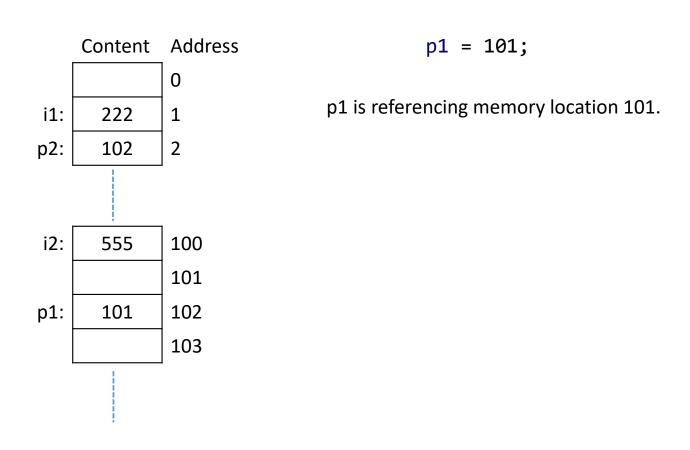
```
int **pp2 = &p1;
```

pp2 is a pointer to a pointer.

How are the pointers setup?







That C program again

Demystifying DDRB

Digging into iom2560.h and sfr_defs.h reveals

Initial Value

```
#define DDRB SFR IO8(0x04)
  #define _SFR_IO8(io_addr) _MMIO_BYTE((io_addr) + __SFR_OFFSET)
  #define _MMIO_BYTE(mem_addr) (*(volatile uint8_t *)(mem_addr))
  #define SFR OFFSET 0x20
So DDRB turns into (*(volatile uint8_t *)(0x24)), that is, a pointer dereferenced to memory
address 0x24.
13.4.6
       DDRB - Port B Data Direction Register
                                                         3
                                                                2
                                                                      1
                                                                             0
                    0x04 (0x24)
                             DDB7
                                    DDB6
                                           DDB5
                                                 DDB4
                                                        DDB3
                                                               DDB2
                                                                     DDB1
                                                                            DDB0
                                                                                   DDRB
                              R/W
                                    R/W
                                           R/W
                                                        R/W
                                                               R/W
                                                                             R/W
                    Read/Write
                                                  R/W
                                                                      R/W
```

(From page 100 in 2549_ATmega2560.pdf (aka the manual))

0

So this explains PORTB and all other I/O memory registers.

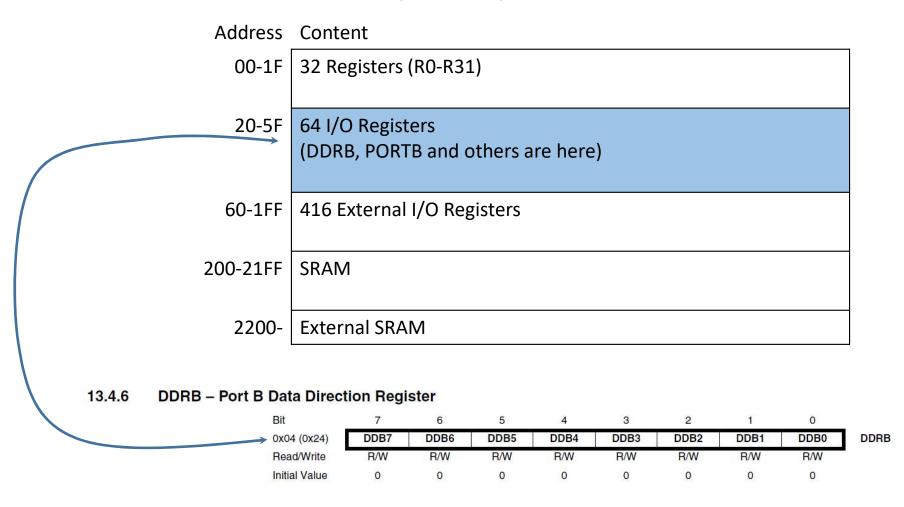
0

0

0

0

2560 Data Memory Map



So...

Wherever DDRB points assign...

Wherever PORTB points....

```
#include <avr/io.h>
#include <util/delay.h>

int main(void)
{
    DDRB = 1 << DDB5;

    while (1)
    {
        PORTB = PORTB | (1 << PB5);
        _delay_ms(200);
        PORTB = PORTB & ~(1 << PB5);
        _delay_ms(200);
    }

    return 1;
}</pre>
```