

Computer Technology I

Lab. 1: How to use the PORTs, Digital input/output, Subroutine call



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Semester: Autumn 2019 Area: Computer Science Course code: 1DT301

Contents

For the first task the goal was to get a light blinking. This was done by setting the data direction register to output, and after that setting the LED port low.

```
;>>>>>>>>>>>>
      1DT301, Computer Technology 1
      Date: 09-09-2019
;
       Authors:
             Roel de Vries
              Anas Kwefati
      Lab number 1
      Title: How to use the PORTS, digital IO, subroutine call
      Hardware: STK600, CPU ATmega 2560
      Function: Turn on LED 2
      Input ports: None
      Output ports: PORTB, used for LEDS
       Subroutines: None
       Included files: m2560def.inc
      Other information: None
       Changes in program:
             09-09-2019 > file created
;<<<<<<<<<<<<<<
.include "m2560def.inc"
main:
       SBI DDRB, 2
       CBI PORTB, 2
```

Here is the flowchart:

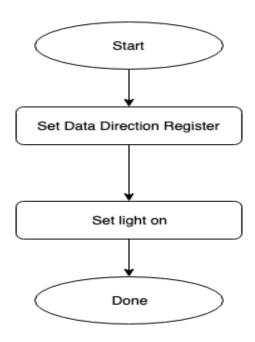


Figure 1: Task 1 flowchart

For the second task the aim is to read switches and light to corresponding LED. This was done by using a data direction register

```
;>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>
; 1DT301, Computer Technology I
; Date: 2019-09-95
; Author:
   Roel de Vries
   Anas Kwefati
      Lab number 1
      Title: How to use the PORTS, digital IO, subroutine call
      Hardware: STK600, CPU ATmega 2560
      Function: Turn on Led n if switch n is pressed
      Input ports: PORTA, used for the switches
      Output ports: PORTB, used for LEDS
      Subroutines: None
      Included files: m2560def.inc
      Other information: None
      Changes in program:
             09-09-2019 > file created
; TASK_2
```

```
;Load pre-configured files for the ports and memory adresses
.include "m2560def.inc"
ldi r16, 0xFF ; load 0b1111 1111 to r16
out DDRB, r16; we set the Data Direction Register B to be ready to
   give an output to turn on the light (0 is on and 1 is off) so now
   we are outputting 1
ldi r17, 0x00; we load 0b0000 0000 to the register 17
out DDRD, r17; we set the Data Direction Register D to take an input
ldi r16, 0xFF;
out PORTB, r16; we are setting the PORTB to give an output of 0b1111
   1111 like that the light is off (for LED 0 is on and 1 is off)
; we are creating an infinite loop to check if the switch port is on or
    off
infinite_loop_switch:
        in r18, PIND ; The input data received from the input PIND is
           stored in the register r18
        out PORTB, r18; The PORTB will take the data of r18 and output
           it to the LED and turn on the light if it is correct
rjmp infinite_loop_switch ;we repeat the process
```

Here is the flowchart for this task:

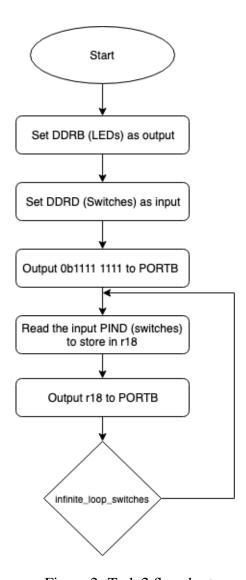


Figure 2: Task 2 flowchart

In task 3 the goal was to turn on led 0, only if switch 5 was pressed. by checking if the bit for switch 5 is high we are able to turn the led on at the right moment

```
;>>>>>>>>>>>>>>>>>
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       Date: 09-09-2019
;
       Authors:
              Roel de Vries
              Anas Kwefati
       Lab number 1
       Title: How to use the PORTS, digital IO, subroutine call
       Hardware: STK600, CPU ATmega 2560
       Function: Turn on Led 0 when you press led 5
       Input ports: PORTA, used for the switches
       Output ports: PORTB, used for LEDS
       Subroutines: None
       Included files: m2560def.inc
       Other information: None
       Changes in program:
              09-09-2019 > file created
;<<<<<<<<<<<<<<
.include "m2560def.inc"
main:
       SBI DDRB, 0 ; set output
       CBI DDRA, 5 ; set input
lightloop:
       SBIS PINA, 5
       CBI PORTB, 0
       SBIC PINA, 5
       SBI PORTB, 0
```

4 Task 4

In task 4 we needed to run the task 3 code in the simulator, as seen in the screenshots below, this worked.

TBD

For task 5 we needed to create a ring counter. This was done by creating a loop which constantly shifts the PORTB register one sideways with a delay.

```
;>>>>>>>>>>>>>>>>>
       1DT301, Computer Technology 1
       Date: 09-09-2019
;
;
       Authors:
               Roel de Vries
;
               Anas Kwefati
       Lab number 1
       Title: How to use the PORTS, digital IO, subroutine call
      Hardware: STK600, CPU ATmega 2560
       Function: Creates a ring counter, updates every 0.5 seconds
       Input ports: None
       Output ports: PORTB, used for LEDS
       Subroutines: Timer
       Included files: m2560def.inc
       Other information: None
       Changes in program:
               09-09-2019 > file created
;<<<<<<<<<<<<<<
.include "m2560def.inc"
main:
       ; Initialize SP, Stack Pointer
       ldi r20, HIGH(RAMEND) ; R20 = high part of RAMEND address
       out SPH,R20 ; SPH = high part of RAMEND address
       ldi R20, low(RAMEND) ; R20 = low part of RAMEND address
       out SPL,R20 ; SPL = low part of RAMEND address
       LDI r20, 0xFF
       OUT DDRB, r20
       CBR r20, 1 ; set output
       OUT PORTB, r20
       call timer
lightloop:
       LSL r20
       BRCS setbit
lightloopcont:
       OUT PORTB, r20
       call timer
       call lightloop
setbit:
       SBR r20, 1
       CLC
       call lightloopcont
```

The Johnson counter was created by using two smaller loops who constantly call eachother, the first which increases the amount of leds on, and a second which decreases the amount of leds on.

```
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       Date: 09-09-2019
       Authors:
;
              Roel de Vries
              Anas Kwefati
       Lab number 1
       Title: How to use the PORTS, digital IO, subroutine call
       Hardware: STK600, CPU ATmega 2560
       Function: Creates a johnson counter, updates every 0.5 seconds
       Input ports: None
       Output ports: PORTB, used for LEDS
       Subroutines: Timer
       Included files: m2560def.inc
       Other information: None
       Changes in program:
              09-09-2019 > file created
;<<<<<<<<<<<<<
.include "m2560def.inc"
main:
       ; Initialize SP, Stack Pointer
       ldi r21, HIGH(RAMEND) ; R20 = high part of RAMEND address
       out SPH,R21 ; SPH = high part of RAMEND address
       ldi R21, low(RAMEND) ; R20 = low part of RAMEND address
       out SPL,R21 ; SPL = low part of RAMEND address
       CBR r16, 0; counter
       SBR r17, 255; light state
```

```
OUT DDRB, r17
        ldi r16, 8
incloop:
        LSL r17
        OUT PORTB, r17
        call timer
        dec r16
   brne incloop
       ldi r16, 8
        call decloop
decloop:
       LSR r17
       SBR r17, 128
        OUT PORTB, r17
        call timer
       dec r16
   brne decloop
        ldi r16, 8
        call incloop
timer:
; Generated by delay loop calculator
; at http://www.bretmulvey.com/avrdelay.html
       ldi r18, 5
    ldi r19, 20
    ldi r20, 175
L1: dec r20
   brne L1
   dec r19
   brne L1
    dec r18
    brne L1
   rjmp PC+1
       ret
```