llvm-mca

```
xorq   4264096(,%rax,8), %rdx   D================eeeeeeER
cmpq   %rcx, %rdi                DeE--------------------R
addq   $1, %rdi                  DeE--------------------R
movl   %edx, %eax                 D====================eER
shrq   $8, %rdx                   D====================eER
xorb   -1(%rdi), %al              D==================eeeeeeER
movzbl %al, %eax                   D==========================eER
xorq   4264096(,%rax,8), %rdx      D==========================eeeeeeER
```

IACA

```
xorq 4264096(,%rax,8), %rdx
   TYPE_LOAD (1 uops)            A--------sdeeeew----R-------p
   TYPE_OP (1 uops)             A-------------sdw----R-------p
cmpq %rcx, %rdi
   TYPE_OP (1 uops)             sdw-----------------R-------p
addq $1, %rdi
   TYPE_OP (1 uops)              sdw----------------R-------p
movl %edx, %eax
   TYPE_OP (0 uops)              A-------------w-----R-------p
shrq $8, %rdx
   TYPE_OP (1 uops)             A-----------sdw----R-------p
xorb -1(%rdi), %al
   TYPE_LOAD (1 uops)           As--deeeew----------R-------p
   TYPE_OP (1 uops)             A-----------sdw----R-------p
movzbl %al, %eax
   TYPE_OP (0 uops)              A------------w-----R-------p
xorq 4264096(,%rax,8), %rdx
   TYPE_LOAD (1 uops)           A--------------sdeeeew----R-------p
   TYPE_OP (1 uops)            A--------------------sdw----R-------p
```