



Assignment -01

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Subject	Microprocessor theory and applications
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Q-1

Solution: 01

Finding highest among five numbers (13h, 2ch, 63h, 58h, 5ch)

Assume CS: Code, DS: data

DATA SEGMENT

Array DB 13h, 2ch, 63h, 58h, 5ch

Max DB 0

DATA ENDS

CODE SEGMENT

START:

MOV AX, data ; Load data segment address into AX

MOV DS, AX

MOV SI, OFFSET Array

MOV AL, [SI] : Move the first number (13h) into AL

MOV CX, 4 : Loop Counter set to 4

INC SI : Increment SI to point to second element

Find-MAX:

MOV BL, [SI]

CMP AL, BL

JAE NEXT : If $AL \geq BL$ jump to next

MOV AL, BL : else $AL \leftarrow BL$

NEXT:

INC SI : SI points to next number

LOOP FIND-MAX : Repeat until CX reaches zero

MOV M, AL

INT 21h

CODE ENDS

END START

Assembler directives

- ① Segment: Tells us in which physical segment we are currently working. e.g. → DATA SEGMENT, CODE SEGMENT
- ② DB: Defines a byte-sized variable (e.g. Max)
- ③ ENDS: Mark the end of a segment (DATA ENDS)
- ④ Assume: Tells the assembler the name of the logical segment it should use for a specified segment.
- ⑤ MOV: Move instruction to copy data ~~flow~~ from source to destination
- ⑥ END: Marks the end of the program and specifies the starting address (END START)

Q:2 a) CS: 1200H, IP: DE00H

Solution: 02 a) physical address = $CS \times 10H + OFFSET$

$$\begin{aligned} &= 1200H \times 10H + DE00H \\ &= 12000H + DE00H \\ &= 1FE00H \end{aligned}$$

$$\begin{array}{r} 12000 \\ + DE00 \\ \hline 1FE00 \end{array}$$

b) DS = 73A2H, SI = 3216H

Solution

physical address = $DS \times 10H + OFFSET$

$$\begin{aligned} &= 73A2H \times 10H + 3216H \\ &= 73A20H + 3216H \\ &= 76C36H \end{aligned}$$

$$\begin{array}{r} 73A20H \\ + 3216H \\ \hline 76C36H \end{array}$$

c) CS = 7370H, IP = 561EH

Solution

physical address = $CS \times 10H + IP$

$$\begin{aligned} &= 7370H \times 10H + 561EH \\ &= 73700H + 561EH \\ &= 78D1EH \end{aligned}$$

Solution

(i) MOV BX, 2050H \Rightarrow BX = 2050H = 0010 0000 0101 0000
(In binary)

(ii) MOV CL, 05H \Rightarrow CL = 05H = 5 (decimal)

(iii) SHL BX, CL

Shifting the bits of BX to left 5 times

1 shift: 0100 0000 1010 0000

2 shift: 1000 0001 0100 0000

3 shift: 0000 0010 1000 0000

4 shift: 0000 0101 0000 0000

5 shift: 0000 1010 0000 0000
 0 A 0 0

BX = 0000 1010 0000 0000 = 0A00H in hexadecimal
(Answer)

4 ii) Serial data into parallel using SID line of 8085

Solution

: Program to read 8 bits serially from SID and store as Parallel data

: Result will be stored in memory location 2030H

MVI B, 08 ; Counter for 8 bits

MVI A, 00H ; Initialize Accumulator

Loop: RIM : read SID bit into accumulator

RAL : rotate accumulator left (brings CY to LSB)

DCRB : decrement counter

JNZ Loop

STA 2030H : store parallel data in memory

HLT : stop program

(i) Parallel data into serial and transmit into SOD line

: Data to be transmitted is stored in memory location 2050

LDA 2050H : $A \leftarrow [2050]$

MVI B, 08H : Counter for 8-bits

Loop : RAL : Rotate left accumulator (MSB goes to CY)

MOV C, A : Save remaining bits

MVI A, 00H : Clear accumulator

INC ZERO : if CY=0, jump to ZERO.

MVI A, 80H : Set SOD bit ($A_1 = 1$ for SOD output)

ZERO : SIM : Output bit through SOD

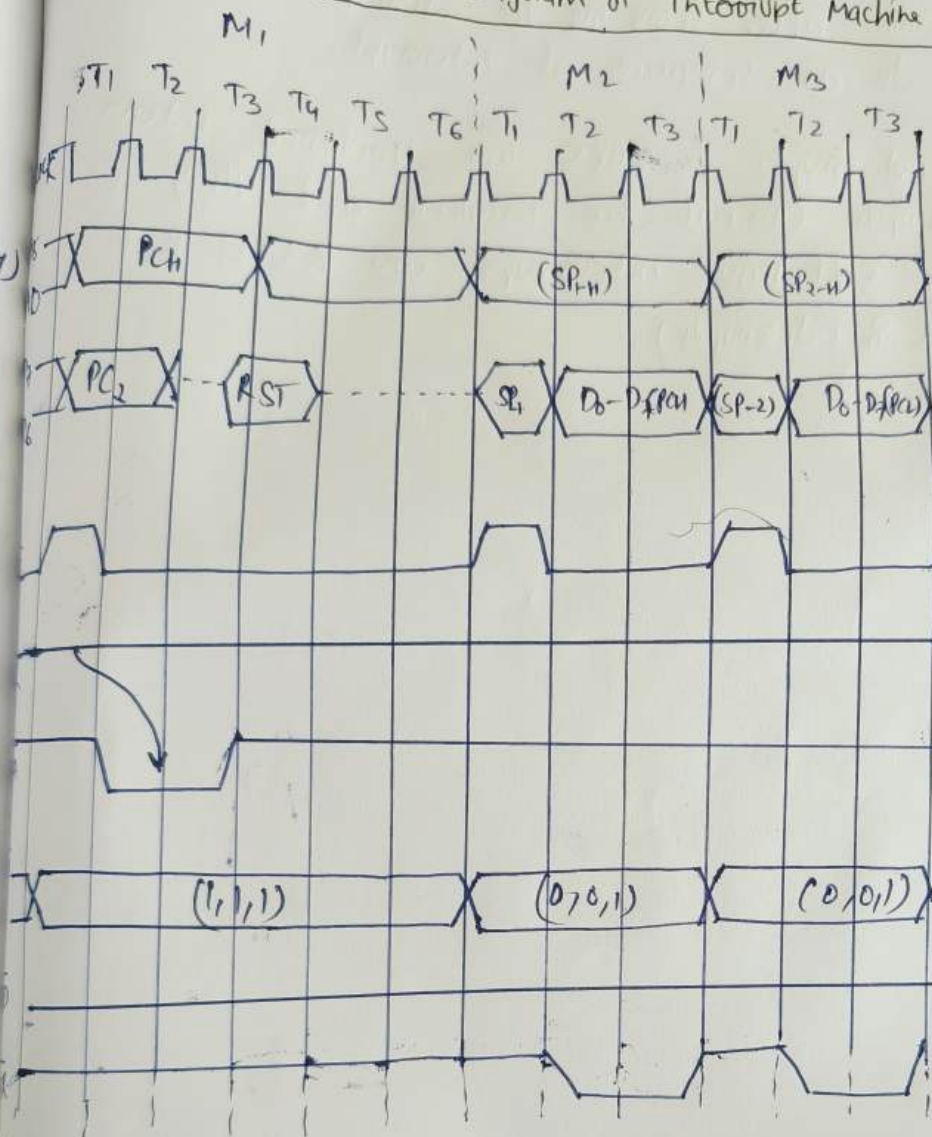
MOV A, C : Restore remaining bits

DCR B : Decrement B Counter

JNZ Loop : If not zero, Continue sending bits

HLT : Stop program

Timing Diagram of Interrupt Machine Cycle



Use of RST₀ - RST₇ interrupts :

The software instructions are included at the appropriate (or required) location in the main program. When the processor encounters the software instructions, it pushes the content of PC (Program Counter) to stack. It then loads the vector address in PC and starts executing the interrupt service routine (ISR) stored in this vector address. At the end of ISR, a return instruction - RET will be placed.

When the RET instruction is executed, the processor pop the content of stack to PC. Hence the processor control returns to the main program after servicing the interrupt. Execution of ISR is referred to as servicing of interrupt.

All Software interrupts of 8085 are Vectored interrupts. The Software interrupt can not be masked and they can not be disabled. The Software interrupts are RST₀, RST₁, RST₂, RST₃, ..., RST₇ (8 interrupts).