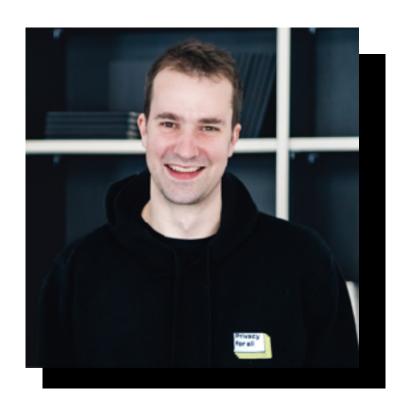
# Real life TFHE Concrete Compiler to the rescue

## Meet The Team



**Quentin Bourgerie**Head of Concrete



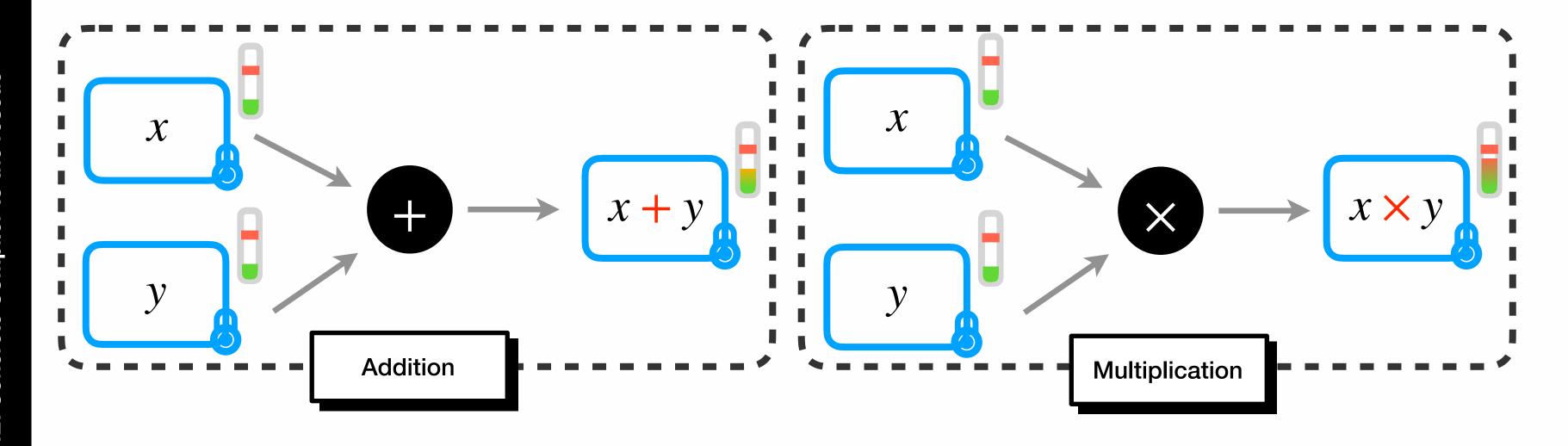
Samuel Tap
Scientific Advisor

# Agenda

| Introduction       | 04 |
|--------------------|----|
| Concrete Python    | 13 |
| Concrete Compiler  | 20 |
| Concrete Optimizer | 27 |
| Conclusion         | 36 |

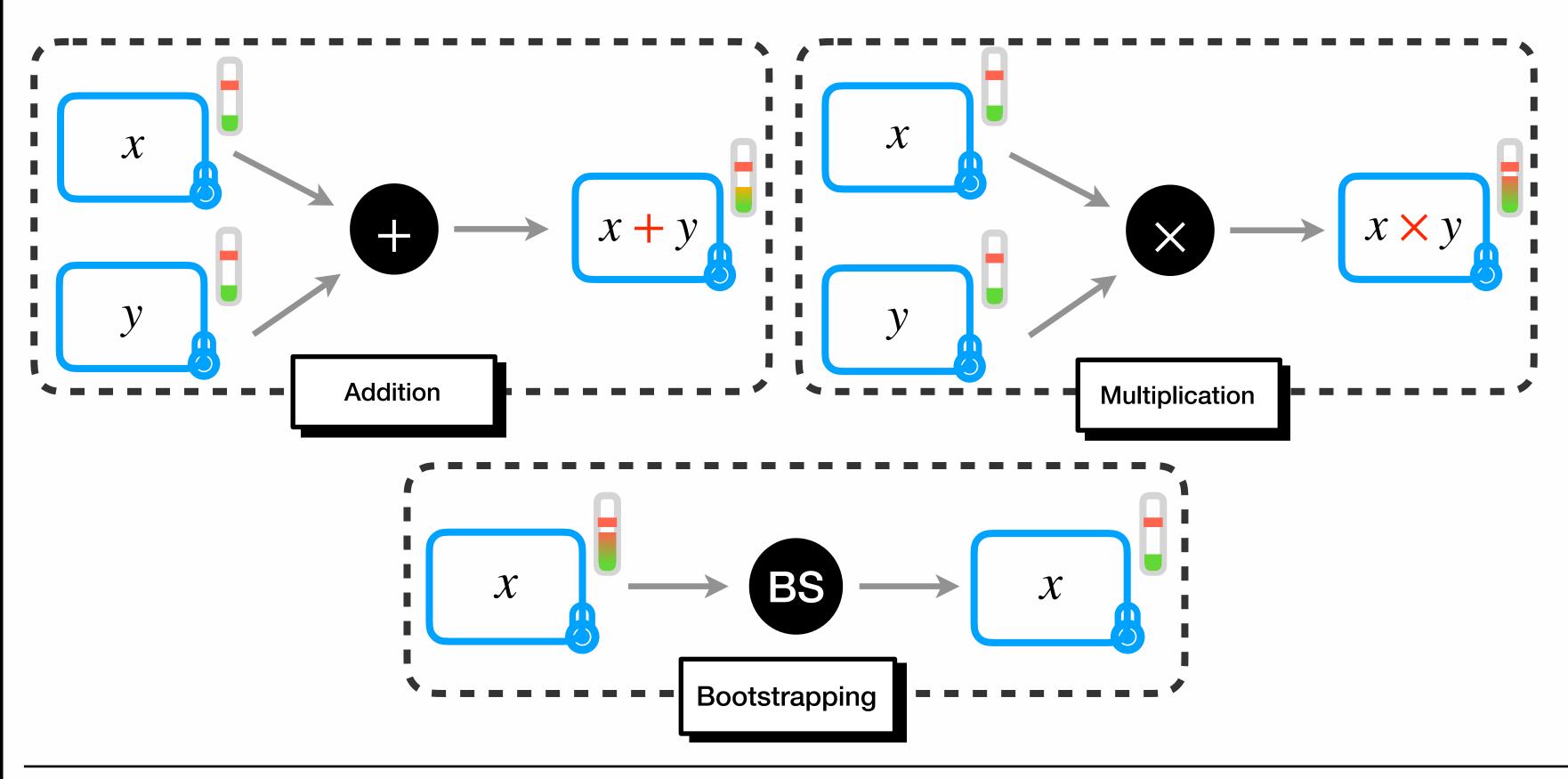
# Introduction

## FHE

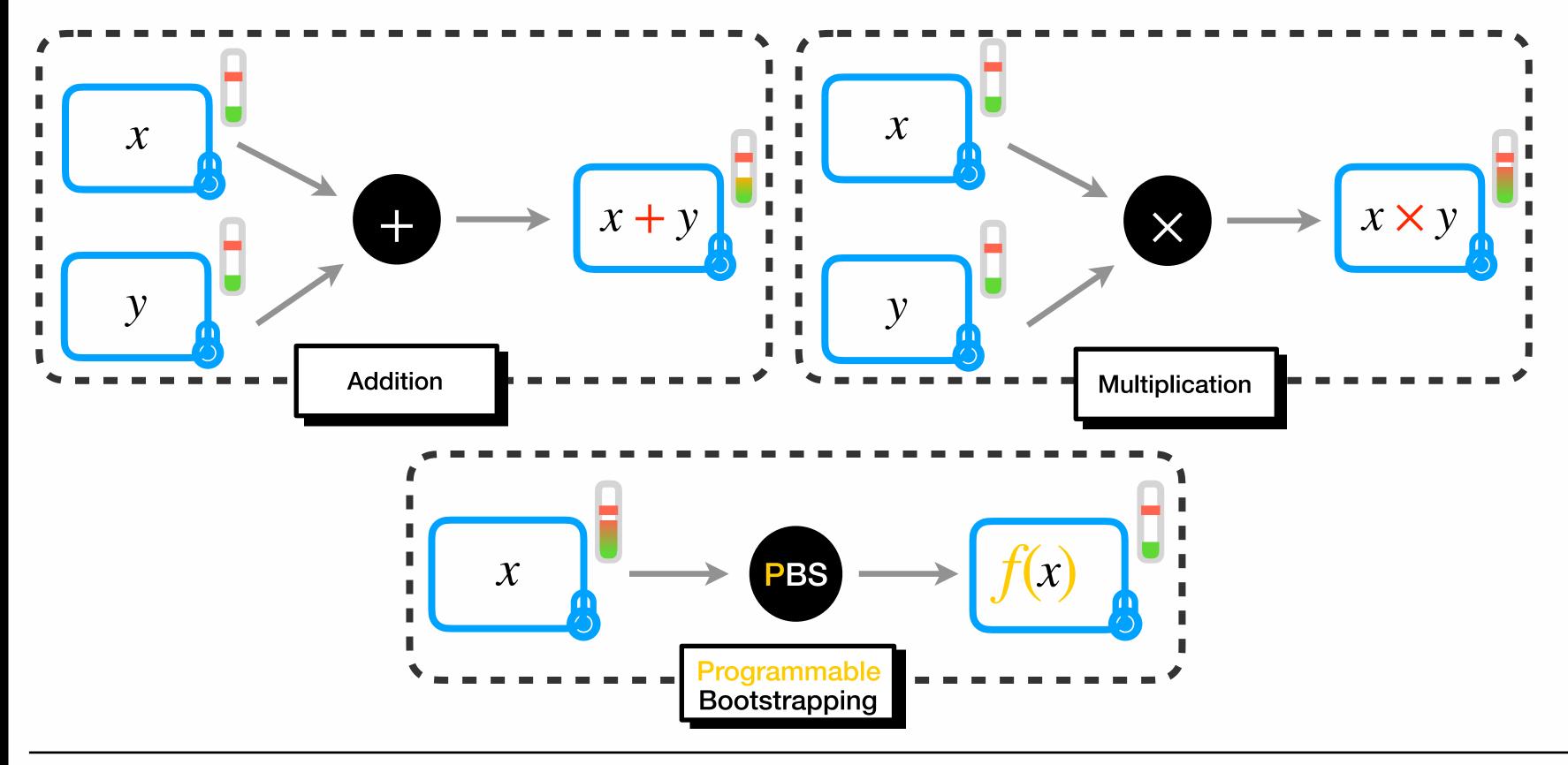




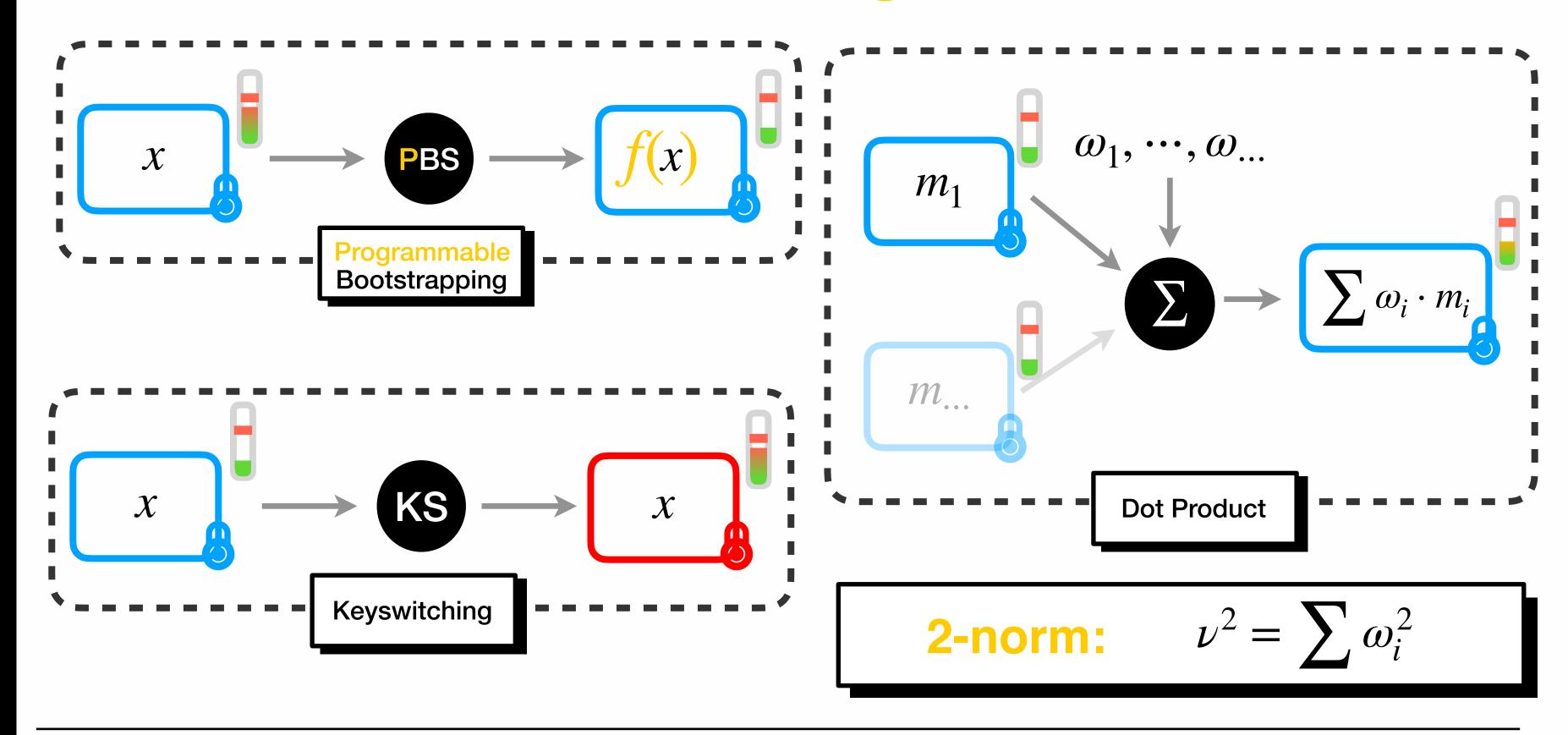
## **FHE**



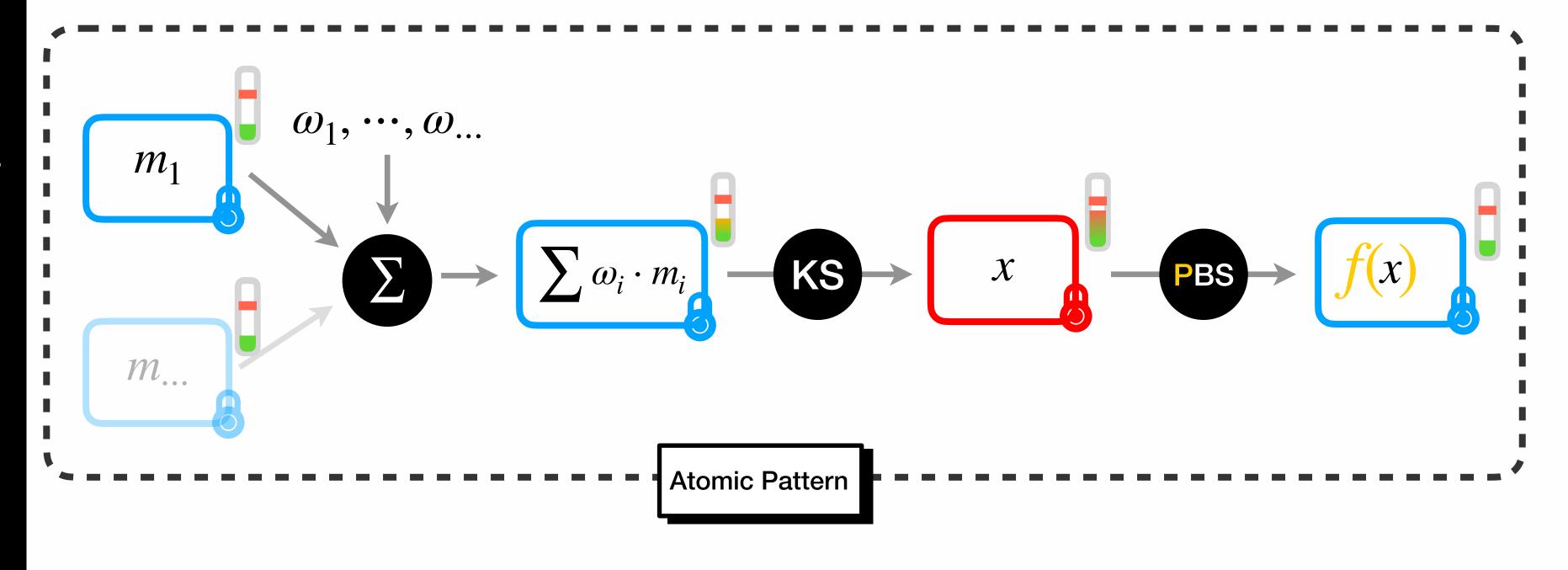
## **TFHE**



## TFHE building blocks



## Optimal arrangement of TFHE building blocks



## **Enhancing TFHE**



#### **New paradigms**

Short integers (1-10 bits) instead of boolean-only

One or several parameter set for a given DAG

Failure probability at DAG level



#### **New algorithms**

CRT/Radix encoding

WoP-PBS

Rounded-PBS

Approximate-PBS





#### **Needs**

Pick <u>parameters</u>

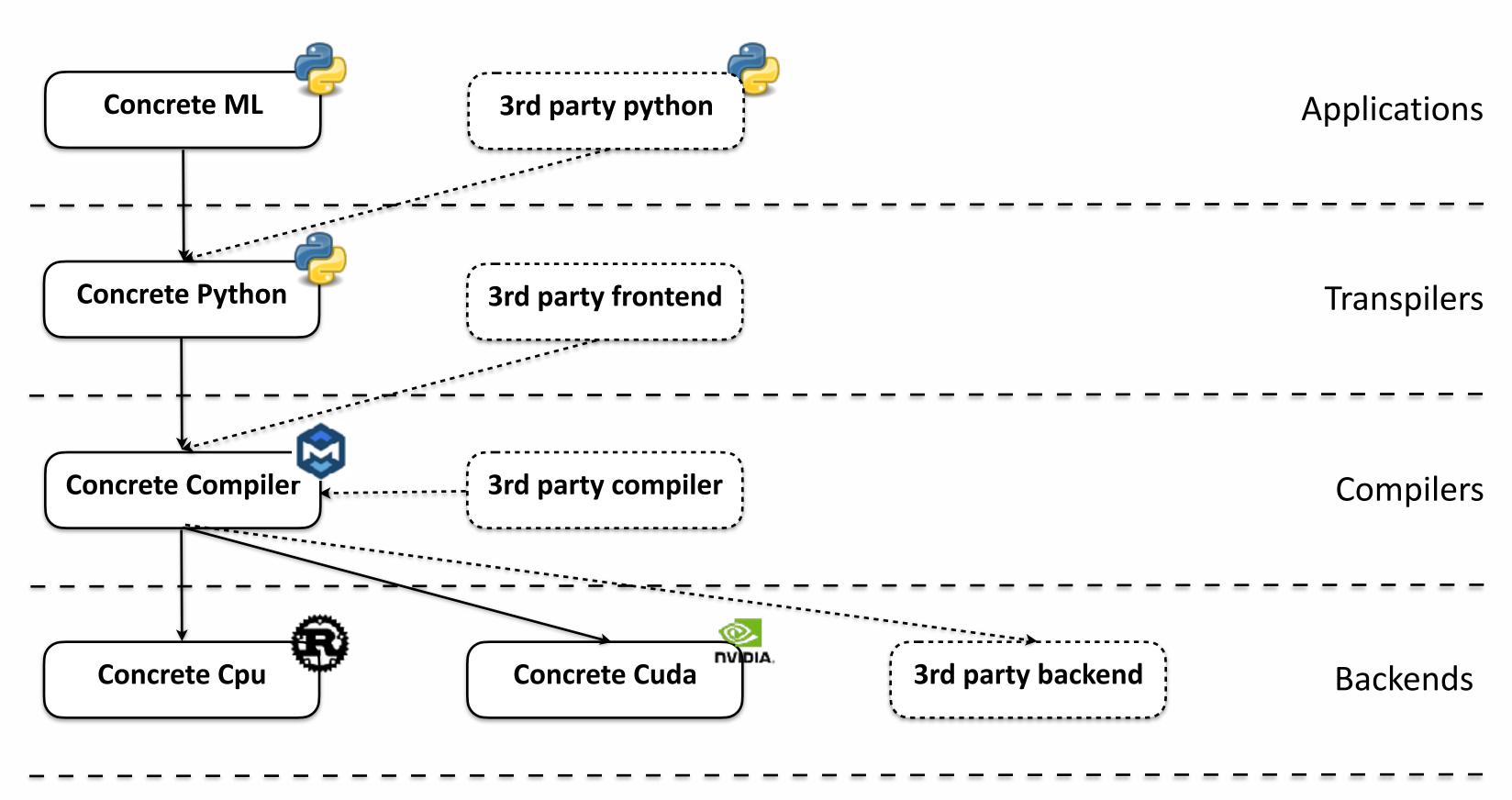
Optimal translation of a plain DAG into a TFHE DAG

Easy-to-use toolchain

# Concrete

A modular framework for FHE applications

## Concrete: a modular framework



# Concrete Python

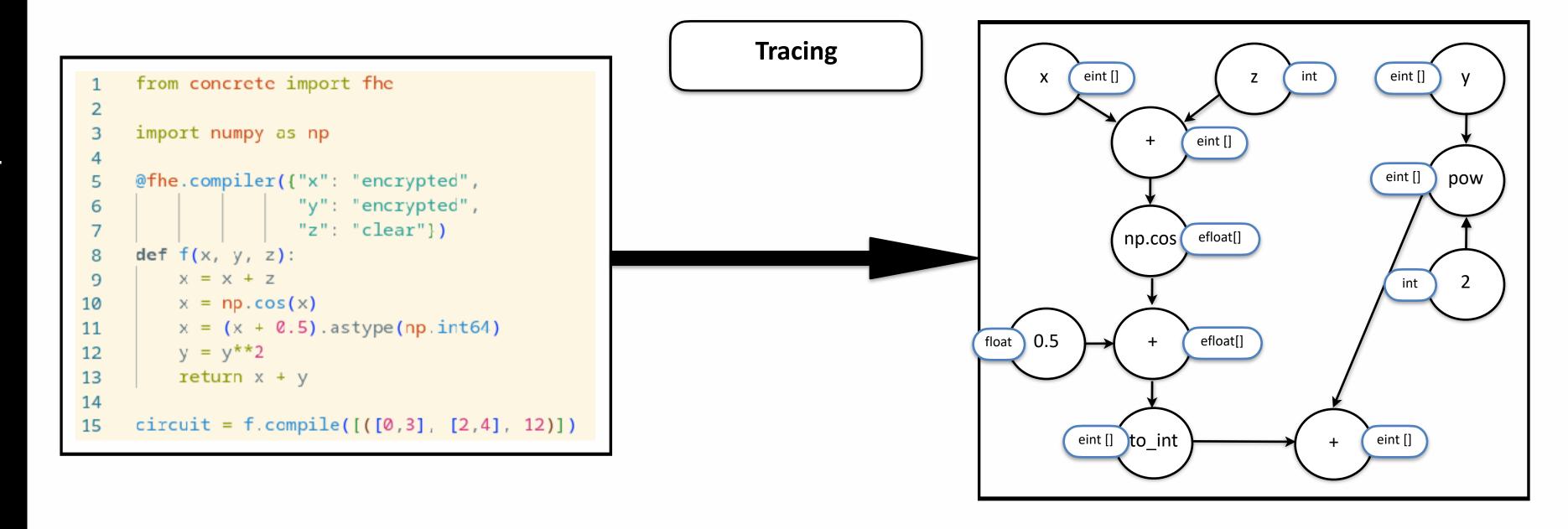
Transpiler for Concrete Compiler

## **Concrete Python**

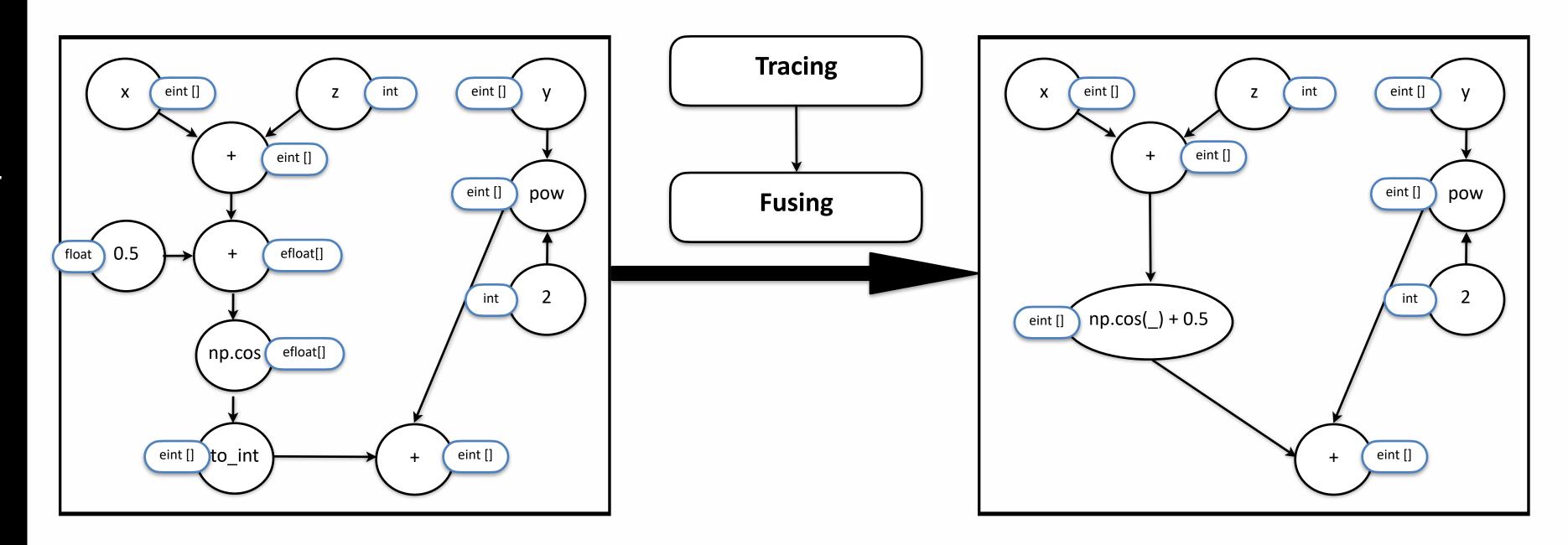
#### An easy to use frontend

- Simple interface to compile python programs
- Type inference based on the dataset evaluation
- Client and server API to run FHE evaluation
- Seemless conversion of univariate floating point and integer function to table lookup
- Extensive support of python and numpy standard functions on scalar and tensor values

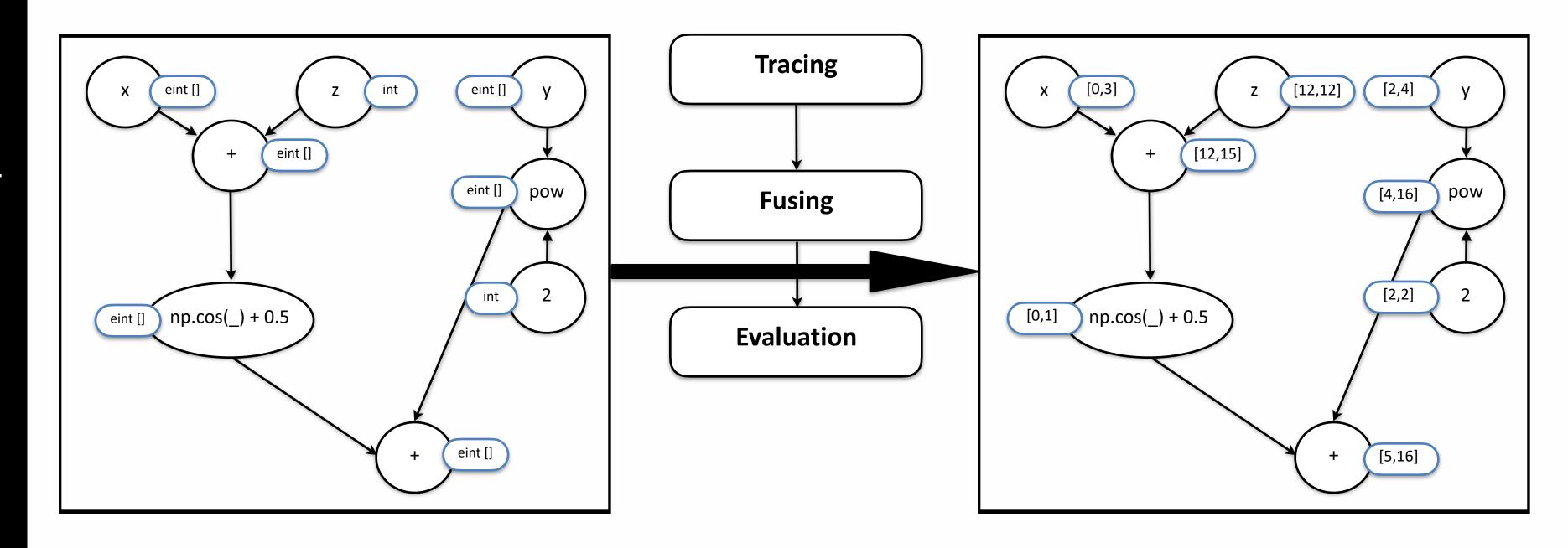
```
from concrete import fhe
     import numpy as np
     # Define your standard python function
     @fhe.compiler({"x": "encrypted", "y": "encrypted"})
     def f(x, y):
         return (x + y) ** 2
10
     # Compile with an input set
     circuit = f.compile([(0, 2), (3, 4)], verbose=True)
11
12
     # Encrypt data and export public evaluation material
13
     encrypted_args = circuit.client.encrypt(1, 2)
14
     eval_keys = circuit.client.evaluation_keys
15
16
     # Evaluate on encrypted data
17
     public_res = circuit.server.run(encrypted_args, eval_keys)
18
19
     # Decrypt and assert that is equal to the clear evaluation
20
     assert(f(1,2) == circuit.client.decrypt(public_res))
```



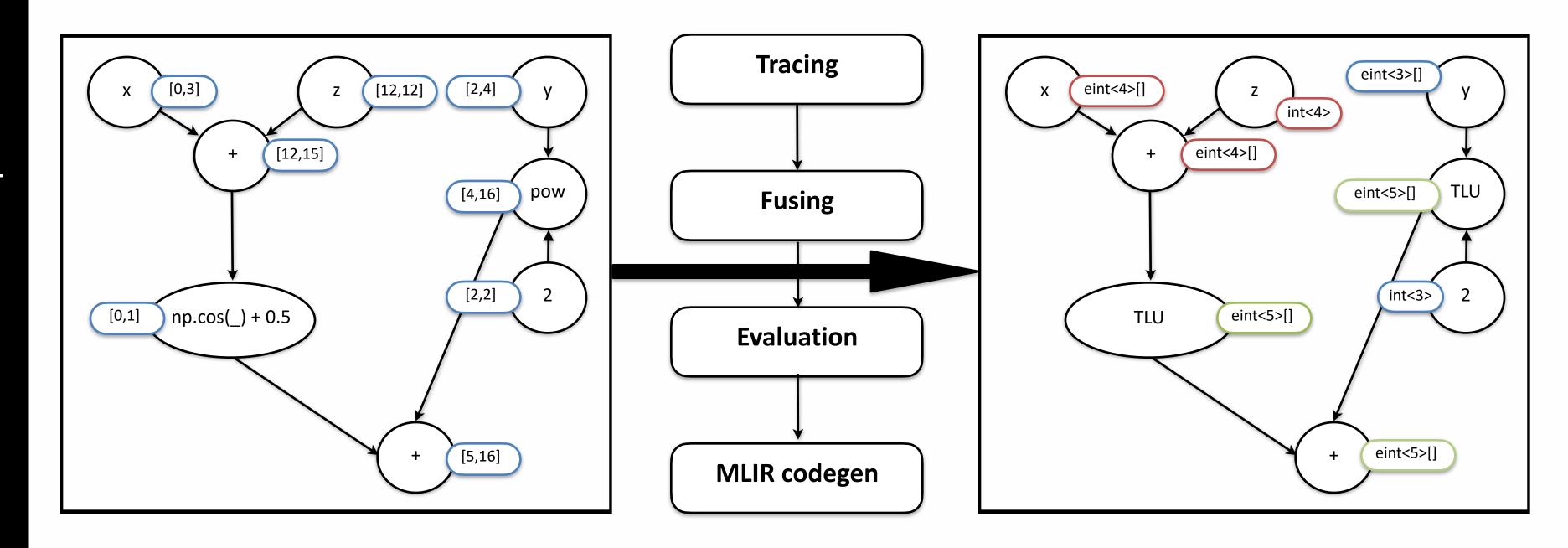
Trace the execution of the python function to build a computation dag



Fuse floating point subgraph to an integer node



Evaluate the computation dag with the input set to compute nodes bounds



Assign bitwidth to connected TLU free subgraph and generate FHE MLIR code

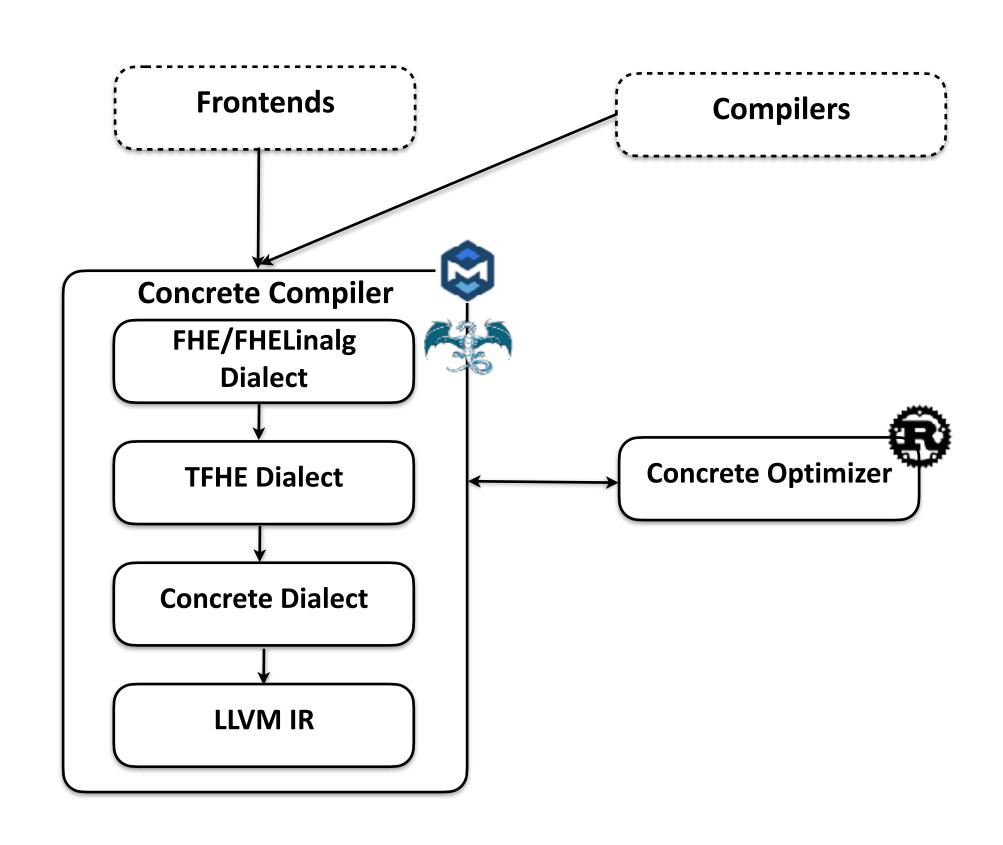
## Concrete Python: MLIR generated code

```
func.func @main(%arg0: tensor<2x!FHE.eint<4>>, %arg1: tensor<2x!FHE.eint<3>>, %arg2: i5) -> tensor<2x!FHE.eint<5>> {
         // Boiler plate code to transform scalar integer to one element tensor
        %from_elements = tensor.from_elements %arg2 : tensor<1xi5>
        // x + z
        %0 = "FHELinalg.add_eint_int"(%arg0, %from_elements)
             : (tensor<2x!FHE.eint<4>>, tensor<1xi5>) -> tensor<2x!FHE.eint<4>>
        // np.cos(_) + 0.5
        %cst = arith.constant dense<[1, 1, 0, 0, 0, 0, 1, 1, 0, 0, 0, 0, 1, 1, 0, 0]> : tensor<16xi64>
10
        %1 = "FHELinalg.apply_lookup_table"(%0, %cst)
11
             : (tensor<2x!FHE.eint<4>>, tensor<16xi64>) -> tensor<2x!FHE.eint<5>>
12
13
        // pow(_{-}, 2)
14
        %cst_0 = arith.constant dense<[0, 1, 4, 9, 16, 25, 36, 49]> : tensor<8xi64>
15
        %2 = "FHELinalg.apply_lookup_table"(%arg1, %cst_0)
16
             : (tensor<2x!FHE.eint<3>>, tensor<8xi64>) -> tensor<2x!FHE.eint<5>>
17
18
        // +
19
        %3 = "FHELinalg.add_eint"(%1, %2)
20
             : (tensor<2x!FHE.eint<5>>, tensor<2x!FHE.eint<5>>) -> tensor<2x!FHE.eint<5>>
21
        return %3 : tensor<2x!FHE.eint<5>>
22
23
```

A modular framework for FHE application

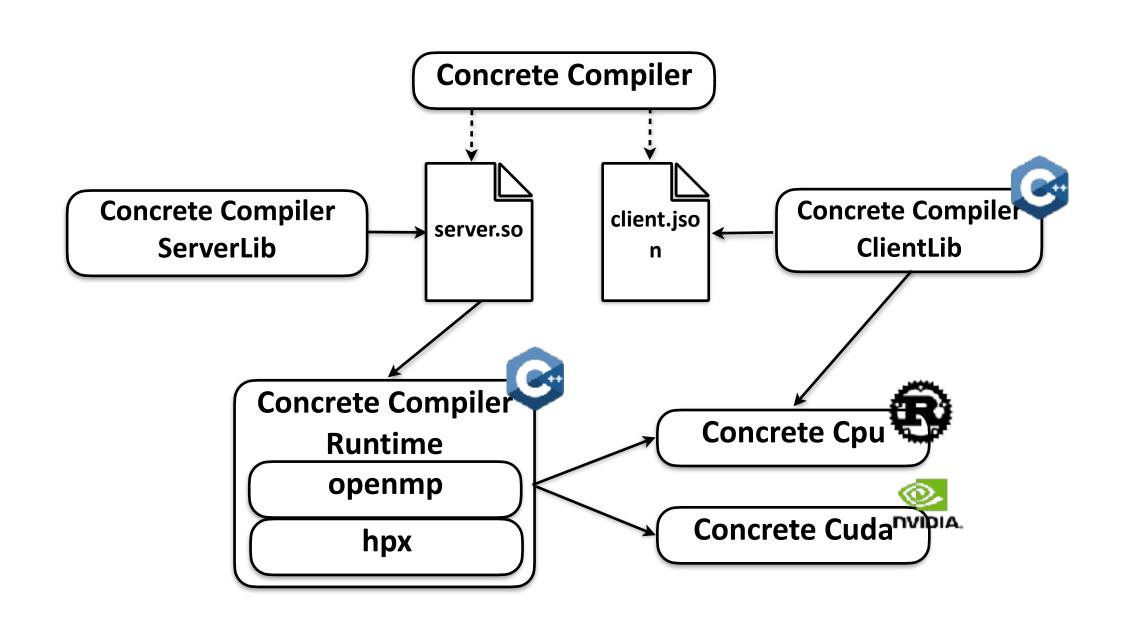
**High-Level Overview (1)** 

- MLIR-based compiler to be reusable and leverage community effort on common problems
- Concrete Optimizer to solve TFHE parametrization problems
- **LLVM Toolchain** to produce binary library



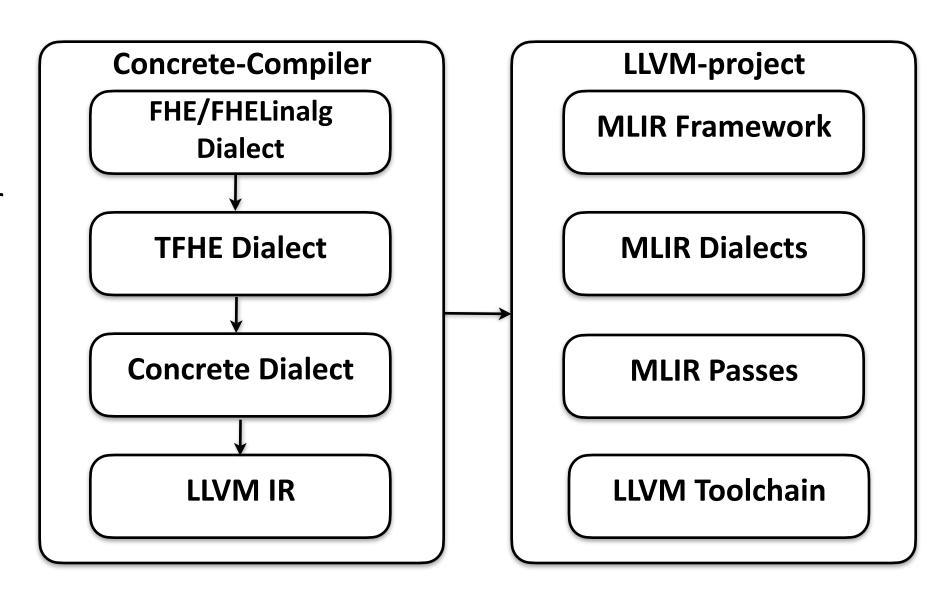
**High-Level Overview (2)** 

- Runtime linked with OpenMP and HPX libraries for loop parallelism and task scheduling
- Runtime linked to Concrete CPU/GPU backends to use the fastest hand optimized TFHE implementation
- Client and Server toolkit to use compilation artifacts



#### Why MLIR?

- MLIR infrastucture allows to reduce the cost of building Concrete Compiler
- Standard MLIR Dialects to model common compiler abstraction (tensor, memref, linalg, scf, .omp, ..)
- Standard MLIR Passes to solve common compiler problems (canonicalization, linalg generalization, dead code elimination, bufferization...)
- Leverage LLVM toolchain to produce efficient binaries
- Allows for a reusable definition of FHE-specific dialect and optimization passes



#### **Specific Dialects**

- FHE Dialect defines crypo-free FHE types and scalar operators
- FHELinalg Dialect defines very high level tensor operators
- **TFHE Dialect** introduces crypto-system dependent parameters and operators
- Concrete Dialect represents unabstracted implementation operators to prepare the codegen

```
1 %0 = "FHELinalg.matmul_eint_int"(%x, %y):
2 (tensor<4x3x!FHE.eint<2>>, tensor<3x2xi3>) -> tensor<4x2x!FHE.eint<2>>
```

```
1 %2 = "TFHE.keyswitch_glwe"(%0) {key = #TFHE.ksk<sk<0,1,1280>, sk<1,1,677>, 3, 4>}
2 | : (!TFHE.glwe<sk<0,1,1280>>) -> !TFHE.glwe<sk<1,1,677>>
3 %3 = "TFHE.bootstrap_glwe"(%2, %1) {key = #TFHE.bsk<sk<1,1,677>, sk<0,1,1280>, 256, 5, 1, 15>}
4 | : (!TFHE.glwe<sk<1,1,677>>, tensor<256xi64>) -> !TFHE.glwe<sk<0,1,1280>>
```

#### **High Level Pipeline**

- A set of analysis passes to build the FHE constraint DAG
- A set of transformations' passes to translate non natively supported TFHE operators
- A set of conversion passes to go from FHE/FHELinalg dialects to TFHE following Concrete Optimizer rewriting guidelines

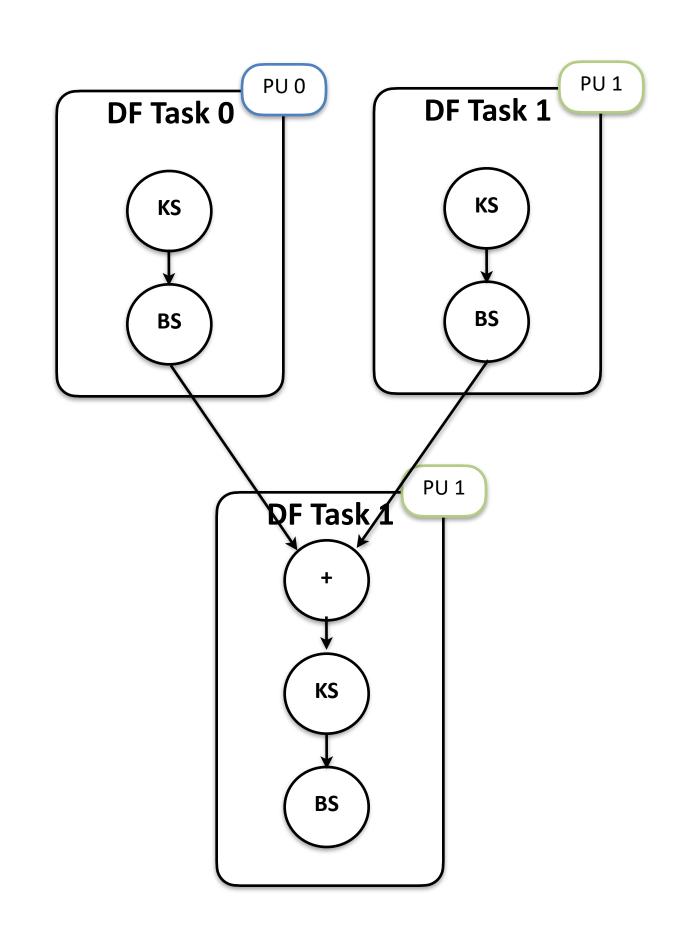
```
FHE Transformations
                                 FHE Analysis
                             TFHE Optimization
                                  Conversion
%c4611686018427387904_i64 = arith.constant 4611686018427387904 : i64
%cst = arith.constant dense<[0, 0, 1, 0]> : tensor<4xi64>
%cst_0 = arith.constant dense<[0, 0, 1, 2]> : tensor<4xi64>
%0 = "TFHE.add_qlwe"(%arg0, %arg1) : (!TFHE.qlwe<sk<0,1,1280>>, !TFHE.qlwe<sk<0,1,1280>>)
%1 = "TFHE.encode_expand_lut_for_bootstrap"(%cst_0) {isSigned = false, cutputBits = 2 : i3
%2 = "TFHE.keyswitch_glwe"(%0) {key = #TFHE.ksk<sk<0,1,1280>, sk<1,1,677>, 3, 4>} : (!TFHE
%3 = "TFHE.bootstrap_qlwe'(%2, %1) {key = #TFHE.bsk<sk<1,1,677>, sk<0,1,1280>, 256, 5, 1,
%4 = "TFHE.neq_qlwe"(%arg1) : (!TFHE.qlwe<sk<0,1,1280>>) -> !TFHE.qlwe<sk<0,1,1280>>
%5 = "TFHE.add_glwe"(%arg0, %4) : (!TFHE.glwe<sk<0,1,1280>>, !TFHE.glwe<sk<0,1,1280>>) ->
%5 = "TFHE.encode_expand_lut_for_bootstrap"(%cst) {isSigned = true, outputBits = 2 : i32,
%7 = "TFHE.add_qlwe_int"(%5, %c4611686018427387904_i64) : (!TFHE.qlwe<sk<0,1,1280>>, i64)
%8 = "TFHE.keyswitch_qlwe"(%7) {key = #TFHE.ksk<sk<0,1,1280>, sk<1,1,677>, 3, 4>} : (!TFHE
%9 = "TFHE.bootstrap_qlwe'(%8, %6) {key = #TFHE.bsk<sk<1,1,677>, sk<0,1,1280>, 256, 5, 1,
%10 = "TFHE.neq_glwe"(%9) : (!TFHE.glwe<sk<0,1,1280>>) -> !TFHE.glwe<sk<0,1,1280>>
%11 = "TFHE.add_glwe"(%3, %10) : (!TFHE.glwe<sk<0,1,1280>>, !TFHE.glwe<sk<0,1,1280>>) ->
```

(!FHE.eint<2>, !FHE.eint<2>) -> (!FHE.eint<2>)

%0 = "FHE.mul\_eint"(%arg0, %arg1):

#### Parallelism and distribution

- Automatic loop parallelism using high level information to lower to openmp
- Automatic dataflow parallelism by generating a dataflow task graph using high level information, and tiling
- A **SDFG** Dialect (Static DataFlow Graph) instantiated with high level TFHE primitives to **offload** a whole **TFHE subgraph** (pipeline) for **hardware accelerators** (GPU, ...)
- A dataflow runtime based on HPX to implement dataflow tasks' parallelism and distributed computation
- A GPU SDFG runtime to schedule GPU kernels over multi-GPU hosts



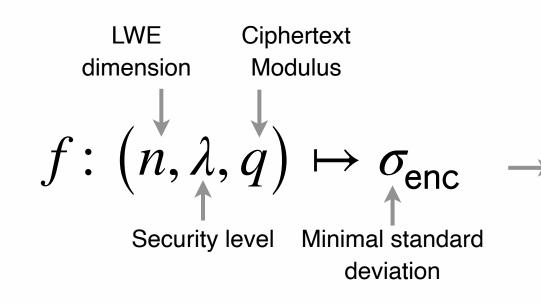
# Concrete Optimizer

An optimizer for TFHE

## Goals



Security

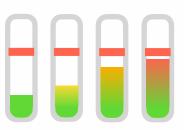


Using the lattice estimator



Correctness







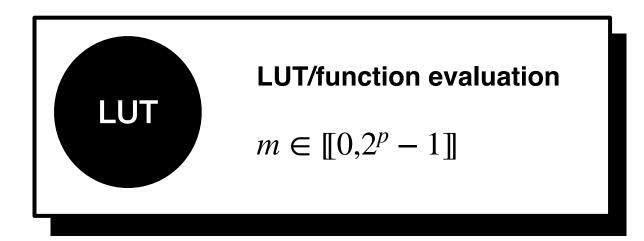
Noise Model to track the noise along the computation

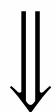


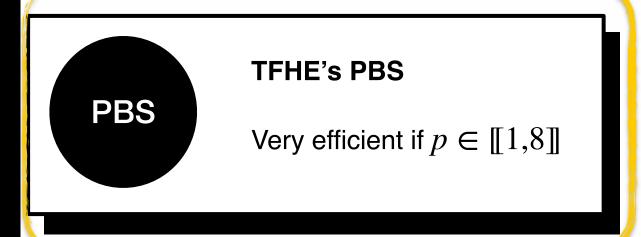
**Efficiency** 

Cost Model as a surrogate of the execution time

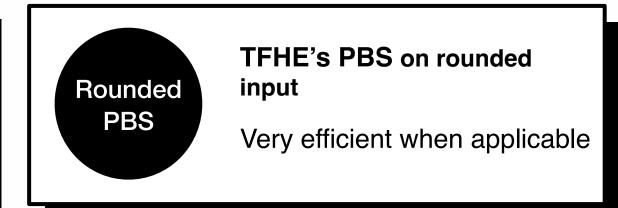
## Choice of algorithm



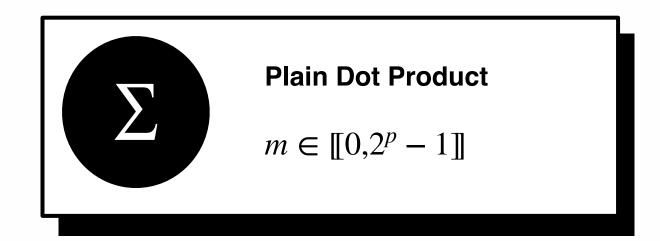


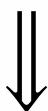


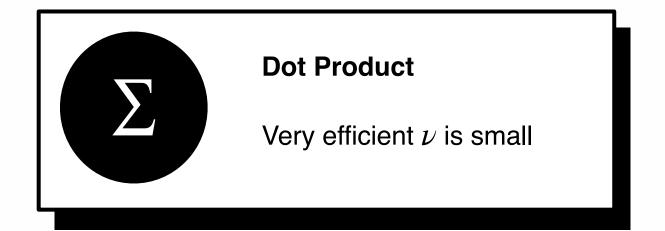


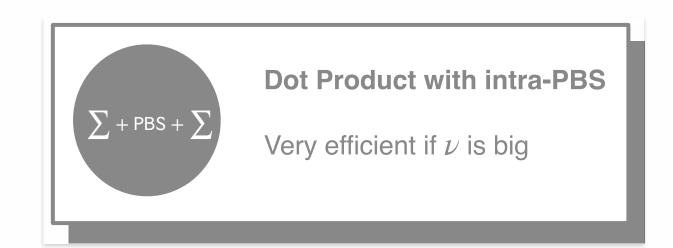


## Choice of algorithm

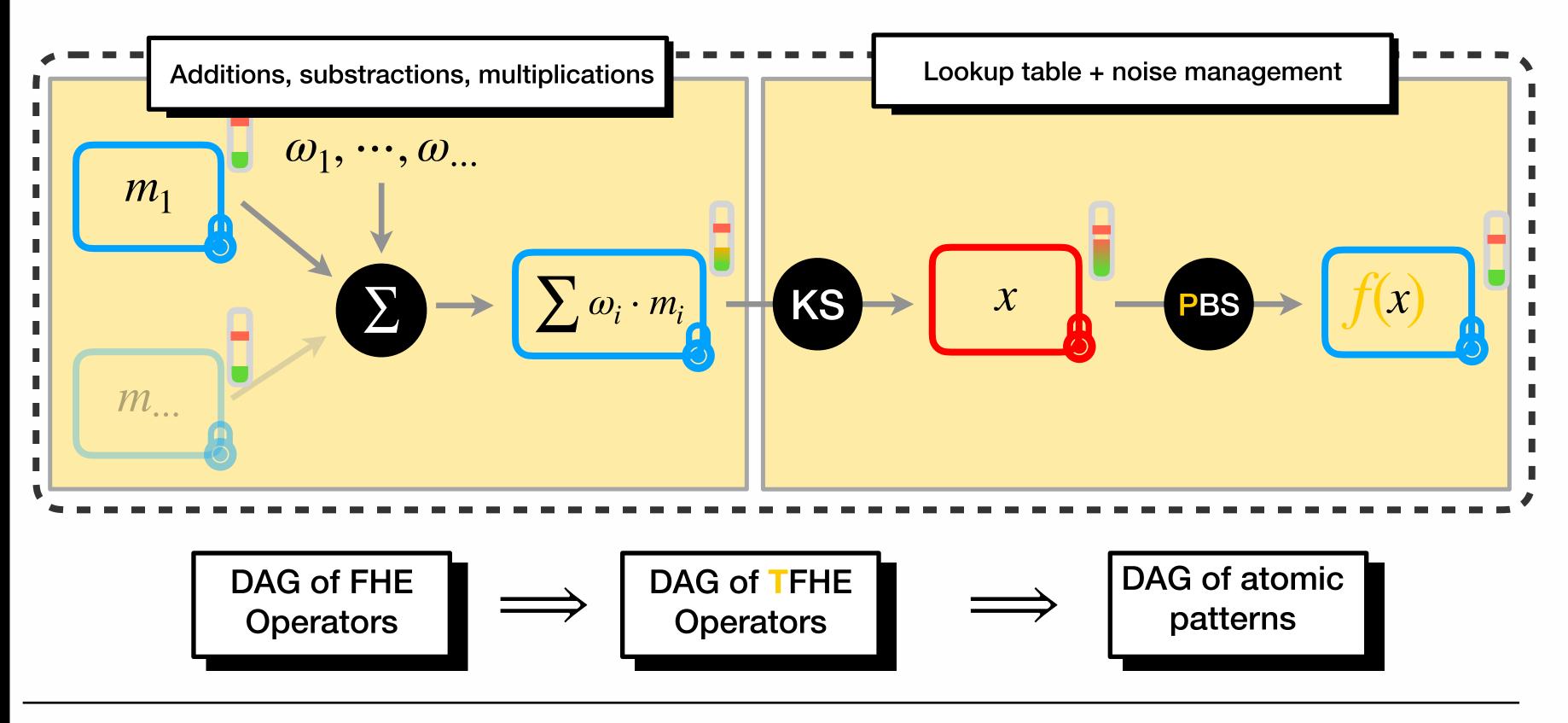




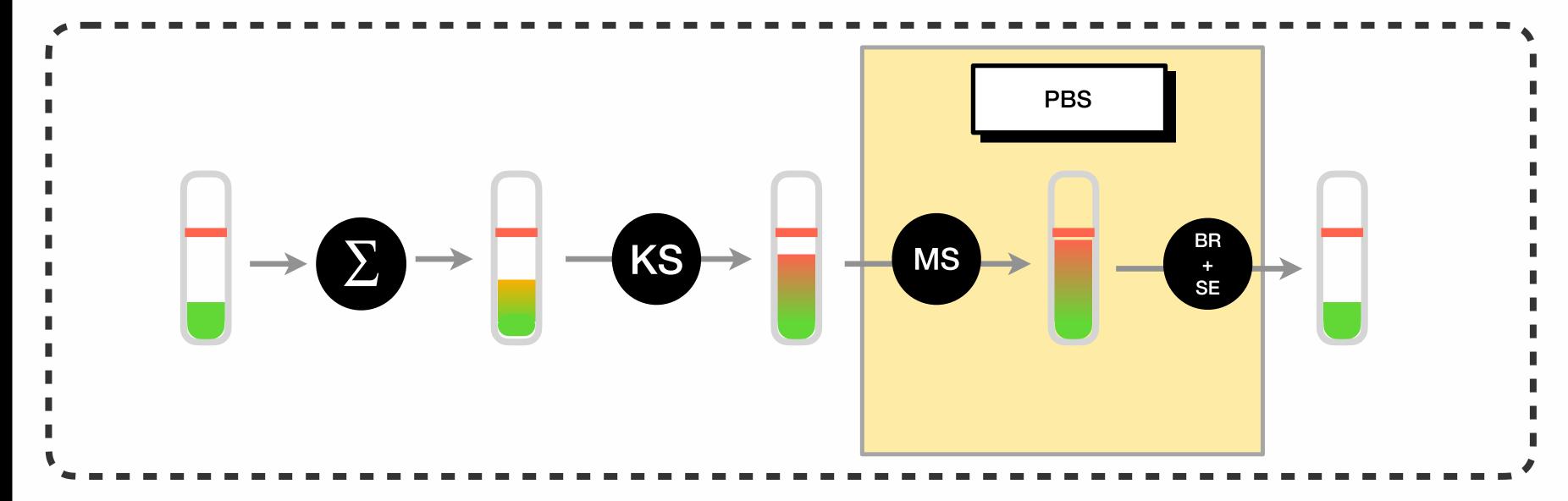




### **Translation**

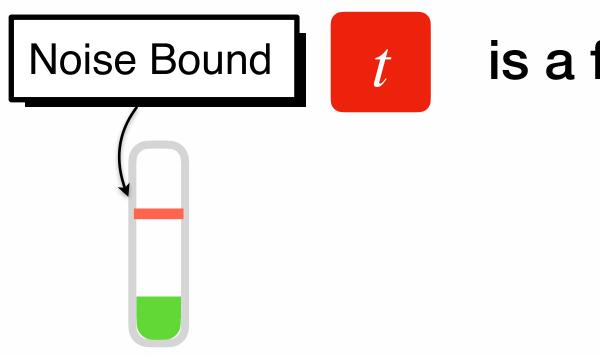


## Noise analysis of an Atomic Pattern



Noise is increasing between two modulus switching

## **Noise Bound**



is a function of

message precision

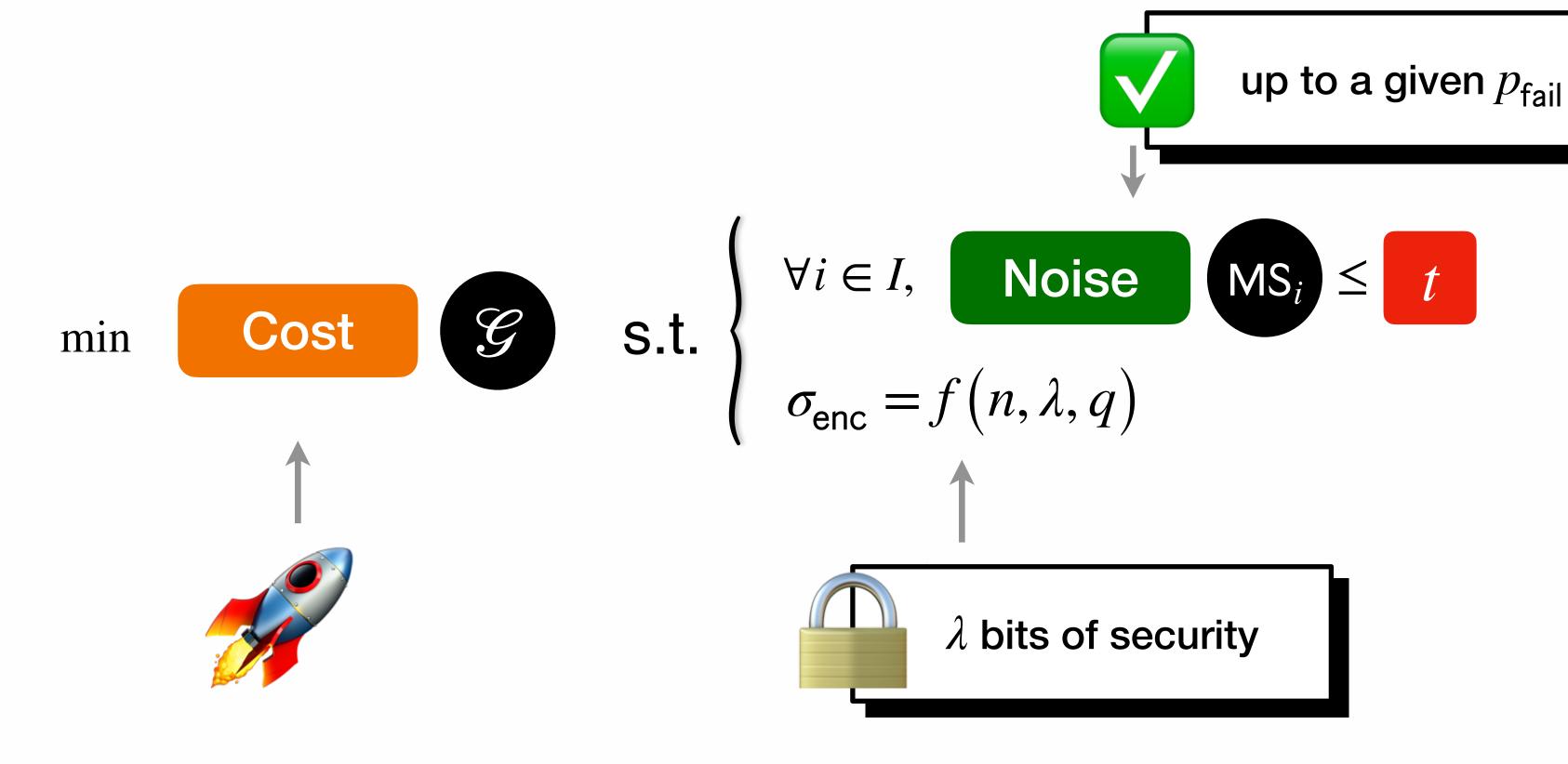
encoding

Natif, CRT, Radix

failure probability

**DAG** dependent

## **Optimization Problem**



## Reducing the search space

# Constraint between parameters

For one atomic pattern

| search space |  $\approx 2^{40} \Longrightarrow$  | search space |  $\approx 2^{30}$ 

Remove unnecessary noise constraints

For a DAG  $\mathscr{G}=\{A_i\}_{i\in I}$  and P the set of precision in  $\mathscr{G}$  |constraints |  $\propto |I| \Longrightarrow$  |constraints |  $\propto |P|$ 

One vs several parameter set

For one parameter set

| search space |  $\propto 2^{30 \cdot |I|} \Longrightarrow$  | search space |  $\approx 2^{30}$ 

For X parameter sets

| search space |  $\propto 2^{30 \cdot |X|} \Longrightarrow$  | search space |  $\approx 2^{30} \cdot |X|$ 

# Conclusion

## Concrete



#### A growing community

821 githubs stars

39 contributors

2916 commits



#### A complete stack for FHE

An easy to use frontend

A reusable compiler infrastructure

Multi backend integrations



#### **Built to be fast**

TFHE native integer

A specific TFHE optimizer

A compiler pipeline and runtime to scale

# Thank you.



# Contact and Links



