Sameen Dharia

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Profile

Passionate and driven Computer Science student seeking opportunities that foster growth, challenge technical abilities, and create meaningful real-world impact. Dedicated to innovation, collaboration, and continuous learning.

Education

Wilfrid Laurier University, Waterloo, ON
Bachelor of Science in Computer Science

- Relevant Coursework: Computer Networks, Data Structures I & II, Digital Electronics, Intro to Microprocessors, Databases I (Winter 2026)

Jean Augustine Secondary School, Brampton, ON
High School Diploma

- Relevant Coursework: Business Leadership, Computer Science (ICS4U), Computer Engineering

Experience

SMT Stager, Evertz Microsystems – Burlington, ON

- Resolved 30+ feeder malfunctions through mechanical servicing and recalibration, reducing line downtime.
- Partnered with engineers to analyze stencil alignment issues, ensuring PCB assembly met quality standards.
- Assisted SMT line operations to meet daily manufacturing and quality targets.
- Interpreted bills of materials to correctly prepare component setups, minimizing setup delays.
- Maintained inventory accuracy by organizing and returning unused parts efficiently.
- Improved safety and workflow by upholding a clean and hazard-free environment.
- Located and prepared correct stencils for each PCB run, preventing production interruptions.
- Loaded/unloaded feeders and restored malfunctioning feeders to full operational status.

Member, Computer Engineering Club – Jean Augustine SS

- Refurbished 10+ desktop systems by reformatting drives and reinstalling Windows.
- Built electronic prototypes using logic gates, circuit analysis, and Arduino microcontrollers.
- Conducted experiments with resistors and transistors to measure voltage–current relationships.
- Increased student engagement by creating promotional content that boosted membership by 25%.

Projects

Multi-Client Chat and File Transfer System — Python

- Built a concurrent TCP-based communication system enabling multiple client connections.
- Implemented reliable message delivery, acknowledgments, and custom communication protocols.
- Designed real-time chat and file-transfer features with safe disconnection handling.
- Improved UX with connection logs, message routing, and fault-tolerant threading.
- Tools: Python, Sockets, TCP/IP, Threading

RV32I Single-Cycle CPU (Milestone 1) — VHDL

- Designed a full RV32I single-cycle CPU in VHDL (ALU, Control, Register File, ImmGen).
- Verified 15+ RISC-V instructions through custom testbenches and waveform analysis.
- Debugged hazards by improving write-back and branch logic, achieving accurate datapath operation.
- Used GTKWave to visualize branching, register states, and memory updates.
- Tools: VHDL, GHDL, GTKWave, Digital Logic Design

Skills

Programming: Python, C, C++, VHDL
Systems & Networking: Sockets/TCP-IP, Linux
Tools: Git, GHDL, GTKWave, Office 365
Operating Systems: macOS, Windows
Hardware/Design: Digital Logic Design