



Sameer Pande

Education

- 2017-Present **B.Tech. Computer Science and Engineering, Indian Institute Of Technology, Delhi, CGPA 9.45/10.**
- 2017 **Class XII Maharashtra State Board, Shri Shivaji Science College, Nagpur, -93.5%.**
- 2015 **Class X CBSE, Narayana Vidyalayam, Nagpur, Percentage - 98.0%.**

Scholastic Achievements

- 2017-18 **IIT Delhi Merit Award**, for being in **top 7%** among **850** students for 1st and 2nd semester
- 2017 Secured **All India Rank 104** in **JEE Advanced 2017** for admission to IITs among 0.2 million students who qualified JEE Mains 2017
- 2017 Secured **All India Rank 68** in **JEE Main 2017** among 1.2 million students
- 2015-2016 Selected as **Indian National Mathematical Olympiad Awardee(INMO)** for being in **top 35** students at national level and selected for **Orientation cum Selection Camp for International Mathematical Olympiad(IMO)** conducted by **HBCSE, TIFR**
- 2015-2016 Selected as **Kishore Vaigyanik Protsahan Yojana(KVPY) Scholar** after securing **All India Rank 89** in KVPY examination, by **IISc Bangalore**
- 2015 Selected as **NTSE(National Talent Search Exam) Scholar** for being in **top 1000** students by **NCERT**(National Council for Educational Research and Training)

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Projects

- May 2019 - **Summer Research Internship**, *Dr. Ben Leong*, National University of Singapore.
Present Working on
- Worked on a tool called 'TCPProbe' that enables us to probe different websites and collect information regarding evaluation of congestion window over time;
 - Scaling the tool by testing adding different levels of granularity to the system;
 - Designing a central scheduler that manages data collection from different viewpoints across the world by adequately assigning jobs to multiple workers across the world.;
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- April 2019 **"Clean It Up"- Andorid Game**, *Dr. Rijurekha Sen*, IIT Delhi.
Built an Angry-Birds like game in Unity to promote awareness regarding segregation of waste.
- March 2019 **Traffic Simulator**, *Dr. Rijurekha Sen*, IIT Delhi.
Built a 3D traffic simulator in OpenGL to simulate the behaviour of Indian Traffic such as overtaking, breaking of traffic rules, accidents etc.
- January-May **Multicycle ARM based processor**.
2019 Developed an ARM based 32-bit multi-cycle processor in VHDL on a Basys3 board
- April 2019 **Krivine and SECD interpreter**, *Dr. Sanjiva Prasad*, Course Project.
Built two interpreters (one Krivine and other SECD) for a toy language which with features such as definitions, recursions, closures etc.
- October 2018 **Asynchronous Serial Receiver and Transmitter**, *Prof. Anshul Kumar*, IIT Delhi.
Receive and transmit serial information without a shared clock. Download information from a file on a computer to FPGA based design and vice-versa. Store the received data in the memory of the FPGA board and transmit it at any time later as per requirement
- September **Mobile Phone Tracking System**, *Prof. Amitabha Bagchi*, IIT Delhi.
2018 Used tree data structure to implement the following
 - Stores mobile phones in exchange nodes;
 - Stores the exchange towers as nodes of a tree;
 - Switch on/off a mobile and updating its location in the entire tree;
 - Determines the smallest path two mobile phones to optimize the calling process.

Courses

- Basic Introduction to Biology for Engineers, Linear Algebra and Differential Equations, Calculus, Probability and Stochastic Processes*, Introduction to Electrical Engineering, Electromagnetic Waves and Quantum Mechanics
- Computer Science Data Structures and Algorithms, Digital Logic and System Design, Discrete Mathematics, Computer Architecture, Signals and Systems, Programming Languages,Design Practices

Skills

- Programming Languages Java, C++, Python, VHDL
- Web Development HTML, CSS, JavaScript,Nodejs

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