

PWM/Enable port connect  
to 8 Backlight ICs.

PWM port  
Input Impedance: 12k $\Omega$

Enable port  
Input Impedance: 1.25k $\Omega$

OMAP 5 board

Back light PWM

Back light Enable

ALPINE custom board

MCU  
R5F104-MJAFB

Buck +3.3V

Buck +6.0V

B/L MAX16813

B/L MAX16813

B/L MAX16813

B/L MAX16813

Secondary 15.6"  
1920x1080 (FHD)  
18bit (6bit\*3)

Upper LED  
54pcs  
9pcs\*3line  
2circuit  
100mA/line

Lower LED  
54pcs  
9pcs\*3line  
2circuit  
100mA/line

ALPINE custom board

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R5F104-MJAFB

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- Suggested backlight PWM Freq: 162 HZ
- Inputs are Logic High Driven (>3V)
- PWM duty cycle 0 to 100%
- OMAP5 Outputs need to be capable of
- supplying 4 milliamps high side drive current at 5V.