

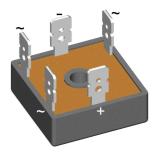
Standard Rectifier Module

3~ Rectifier			
V_{RRM}	=	1600 V	
I_{DAV}	=	27 A	
I _{FSM}	=	550 A	

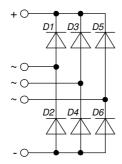
3~ Rectifier Bridge

Part number

VUO36-16NO8







Features / Advantages:

- Planar passivated chips
- Very low leakage current Very low forward voltage drop
- Improved thermal behaviour

Applications:

- Diode for main rectification
- For three phase bridge configurations
- Supplies for DC power equipment
- Input rectifiers for PWM inverter • Battery DC power supplies
- Field supply for DC motors

Package: FO-B

- Isolation Voltage: 3000 V~
- Industry standard outline
- RoHS compliant
- 1/4" fast-on terminals
- Easy to mount with one screw

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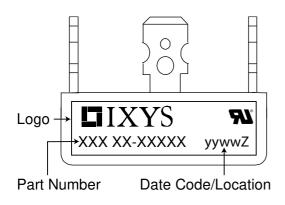




1	1
max.	Unit
1700	V
1600	V
40	μΑ
1.5	mΑ
1.04	٧
1.23	٧
0.93	٧
1.18	٧
27	Α
	i I I
0.76	٧
9.1	mΩ
7	K/W
1	K/W
17	W
550	Α
595	Α
470	Α
505	Α
1.52	kA2s
1.48	kA2s
1.11	kA2s
1.06	kA2s
3	рF
18	550 595 470 505 1.52 1.48



Package FO-B			Ratings					
Symbol	Definition	Conditions			min.	typ.	max.	Unit
I _{RMS}	RMS current	per terminal					100	Α
T _{VJ}	virtual junction temperature	9			-40		150	°C
T _{op}	operation temperature				-40		125	°C
T _{stg}	storage temperature				-40		125	°C
Weight						20		g
M _D	mounting torque				1.8		2.2	Nm
d _{Spp/App}	creepage distance on surface striking distance through air		terminal to terminal	9.0	7.0			mm
$d_{Spb/Apb}$	creepage distance on suna	ace Striking distance through an	terminal to backside	10.0	10.0			mm
V _{ISOL}	isolation voltage	t = 1 second			3000			٧
	t = 1 minute		50/60 Hz, RMS; IsoL ≤ 1 mA		2500			٧

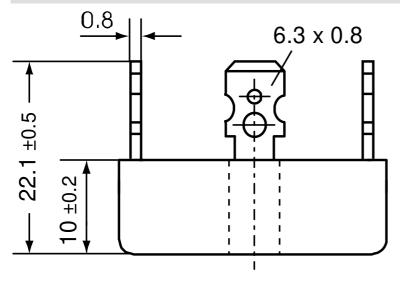


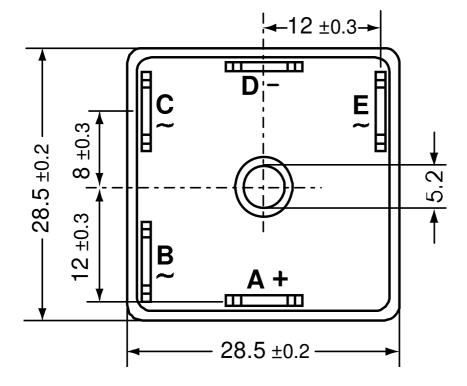
Ordering	Ordering Number	Marking on Product	Delivery Mode	Quantity	Code No.
Standard	VUO36-16NO8	VUO36-16NO8	Box	50	465178

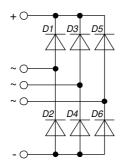
Equiva	lent Circuits for	Simulation	* on die level	$T_{VJ} = 150$ °C
$I \rightarrow V_0$	-R _o -	Rectifier		
V _{0 max}	threshold voltage	0.76		V
R _{0 max}	slope resistance *	7.9		mΩ



Outlines FO-B









Rectifier

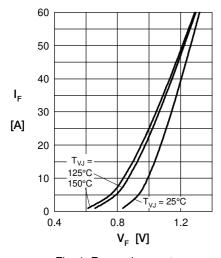


Fig. 1 Forward current vs. voltage drop per diode

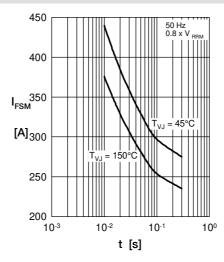


Fig. 2 Surge overload current vs. time per diode

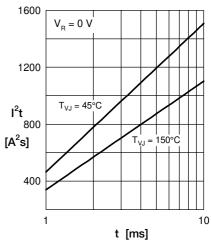


Fig. 3 I²t vs. time per diode

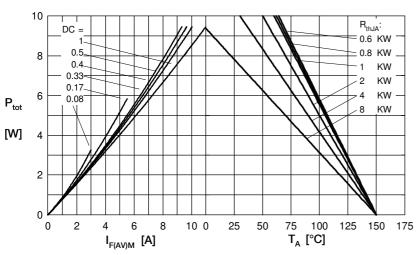


Fig. 4 Power dissipation vs. forward current and ambient temperature per diode

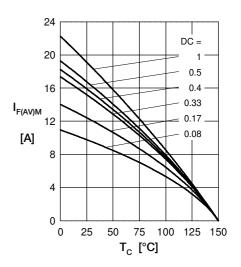


Fig. 5 Max. forward current vs. case temperature per diode

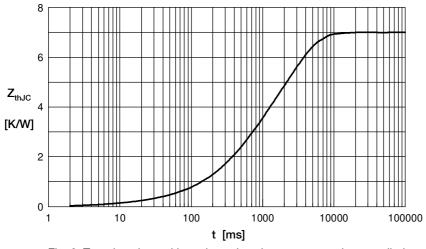


Fig. 6 Transient thermal impedance junction to case vs. time per diode

Constants for Z_{thJC} calculation:

i	R_{th} (K/W)	t _i (s)
1	0.040	0.005
2	0.150	0.030
3	1.710	0.400
4	5.100	2.300