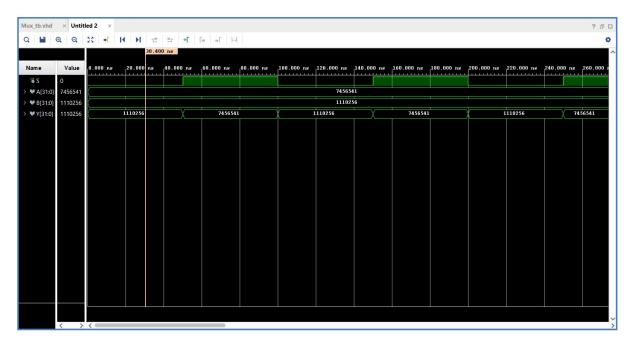
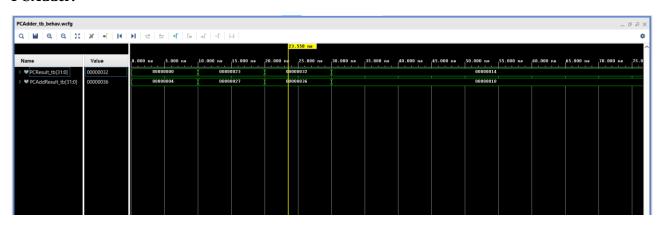
Digital Design Project: Simulation Results

The simulation results for each component are given below.

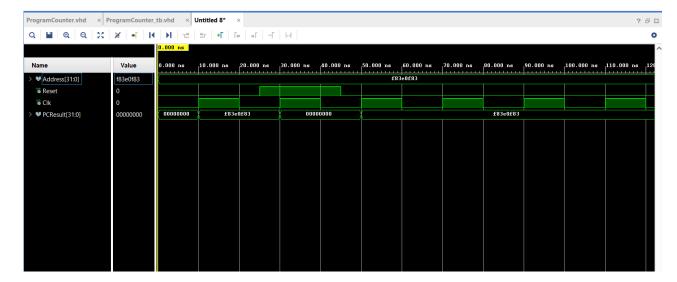
Mux:



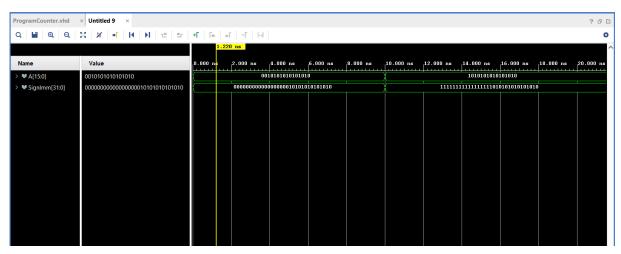
PCAdder:



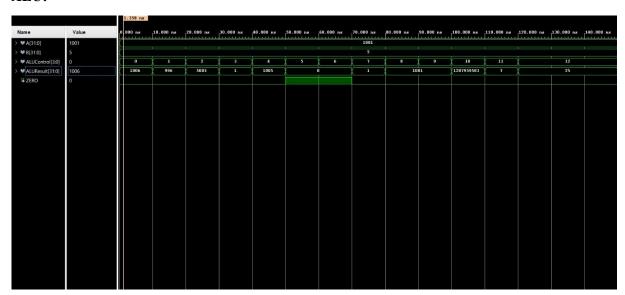
ProgramCounter:



SignExtension:



ALU:





Register:

