# ECE 2101L Section 8 Electrical Circuit Analysis II Lab

# LAB 13 Four Pole Low Pass Filter

12/07/2023

### **ABSTRACT**

A four-pole low-pass filter will have its voltages examined through a load resistor to understand the filter's effect to various input frequencies. On the other hand, a state variable filter is a type of multiple feedback filter that can produce all three filter responses, low pass, high pass and band pass simultaneously.

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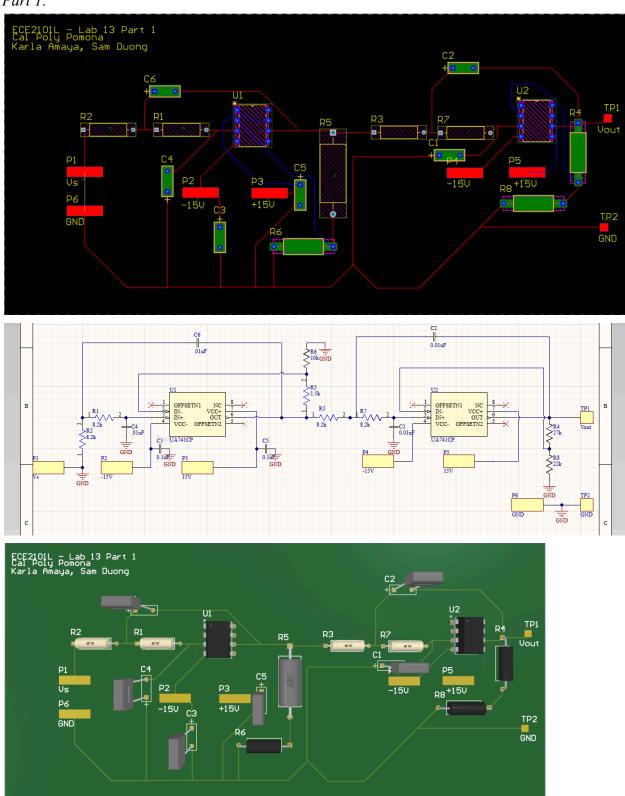
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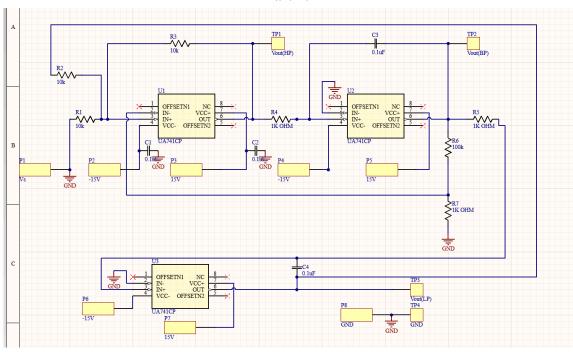
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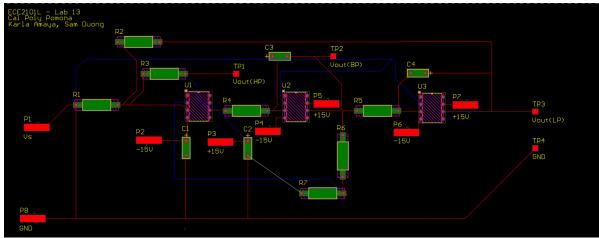
#### **Schematic + PCB**

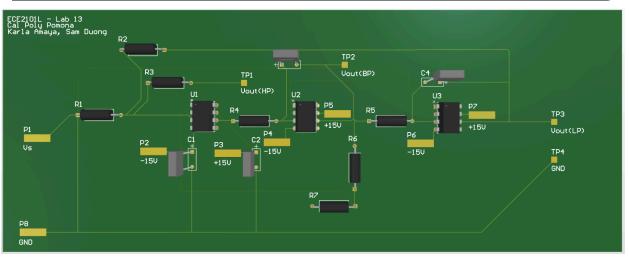
#### Part 1:



Part 2:



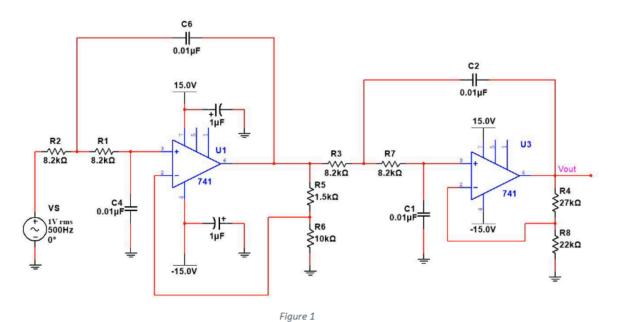




#### Lab

#### Key Objectives:

- 1-Build and test a Butter worth low pass active filter for a specific frequency and order.
- 2-Simulate the circuit
- 3-Design a PCB for the circuit.



#### **Materials Required:**

- 1. Four 0.01  $\mu$ F, four 1.0  $\mu$ F
- 2. one 1.5 k $\Omega$ , four 8.2 k $\Omega$ , two 10 k $\Omega$ , one 22 k $\Omega$ , one 27 k $\Omega$
- 3. Two LM741 C op-amps
- 4. Breadboard and all other connecting test leads, alligator clip leads, and etc.
- 5. Function Generator
- 6. Oscilloscope
- 7. Power Supply

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Tuble 1									
Component	Listed Value	A1	Measure B1	ed values A2	B2				
Ra1,RB1,Ra2,RB2	8.2 kΩ	8.19kΩ	8.16kΩ	8.19kΩ	8.19kΩ				
CA1,CA2,CB1,CB2	0.01 μF	0.01μF	0.01μF	0.01μF	0.01μF				
Ri1	10 kΩ	9.89 kΩ							
Rf1	1.5 kΩ	1.49 kΩ							

Ri2	22 kΩ	21.65 kΩ		
Rf2	27 kΩ	27.25 kΩ		

Table 2

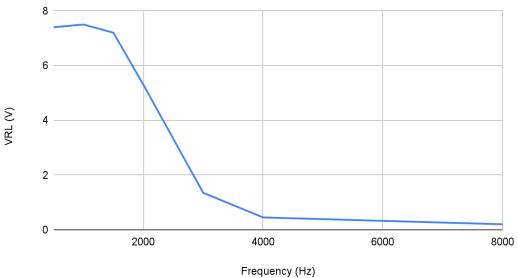
Frequency	VRL
500 Hz	7.4V
1000 Hz	7.5V
1500 Hz	7.2V
2000 Hz	5.3V
3000 Hz	1.35V
4000 Hz	450mV
8000 Hz	20mV

### **Graphs of Findings:**

Plot 1:

Figure 2

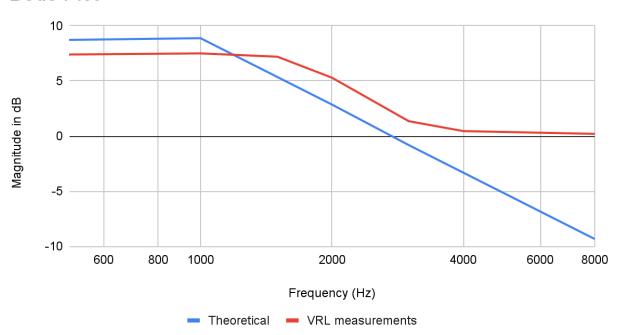
Frequency vs. VRL



Plot 2:

Figure 3

#### **Bode Plot**



#### Analysis:

- The cutoff frequency for the filter appears to be  $\sim$ 1300 Hz as shown by the drop in voltage on the graph.
- The measured voltage gain is 7.4 V
- The voltage gain should be 7.4 V, our data is correct.
- At 20,000 Hz we would predict that the voltage would be very close to 0 V.
- The actual roll off rate of this filter is 86.334 dB/Decade.

#### Part 2- State Variable Filter (Simulation)

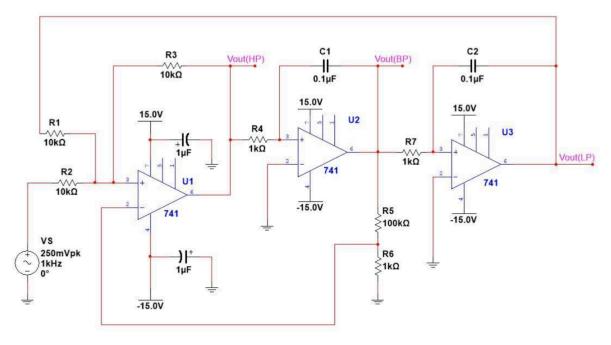


Figure 4

#### Theoretical and Measured Values

Quantity	Computed	Measured
Center Frequency, f0	1591.55 Hz	1586 Hz
Vpp (Center)		16.772 V
Upper cutoff, fcu		1610 Hz
Lower cutoff, fc1		1563 Hz
Bandwidth, BW	47.27	47 Hz
Q	33.66	33.72

Voltages at varying frequencies:

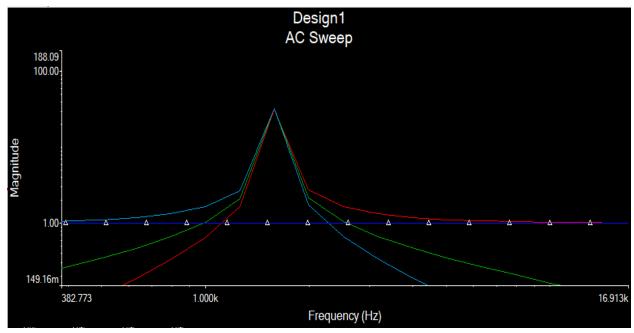


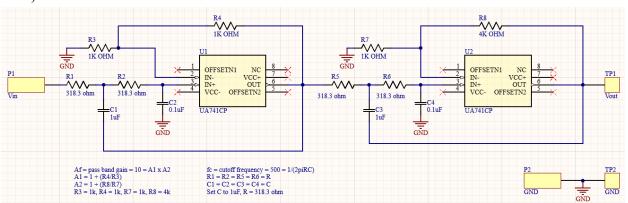
Figure 5

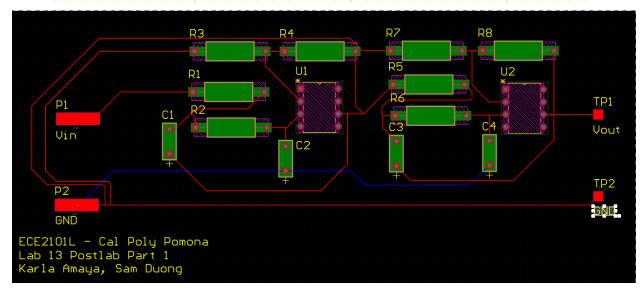
#### **Conclusion:**

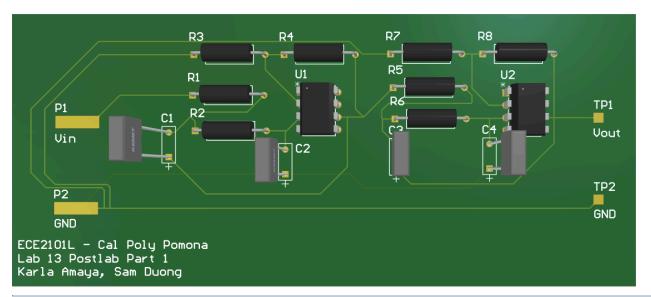
For this lab, we got to study the response of a four pole low pass filter. We did so by creating this filter with two op-amps and a Load Resistor from which we could study voltage changes. When examining the voltage across the load resistor at various frequencies, we found that the filter we created mimicked that of an actual low pass filter when it came to its frequency response. For part two of the lab, we constructed a state-variable band-pass filter. With this filter, the center frequency was found by tuning the input frequency until the bandpass output was at its highest voltage. For our experiment, this came out to 1586 Hz which is extremely close to the calculated 1591 Hz. In addition to yielding the expected center frequency, the gain and bandwidth of our experimental filter came out to be nearly identical to what our theoretical values were. The cause for such accuracy would be due to the fact that our circuit was created through a simulation rather than in physical life. Finding the center frequency and bandwidth of this filter allowed us to understand this filter's functionality in comparison to our prior lab's butterworth filter.

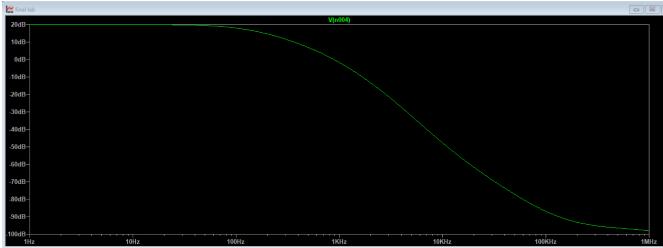
#### Postlab:

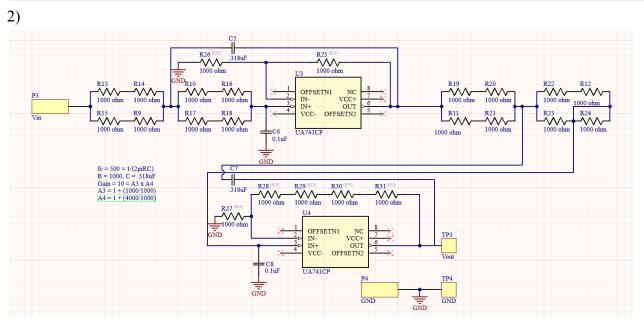
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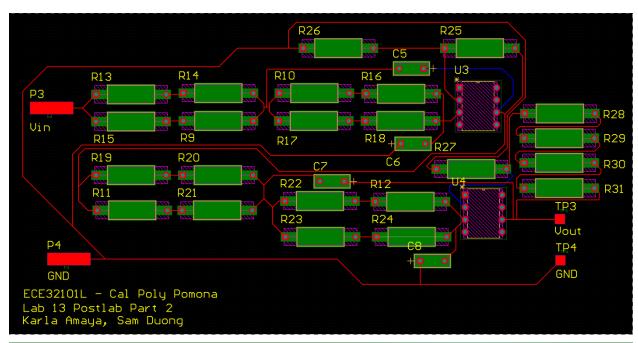


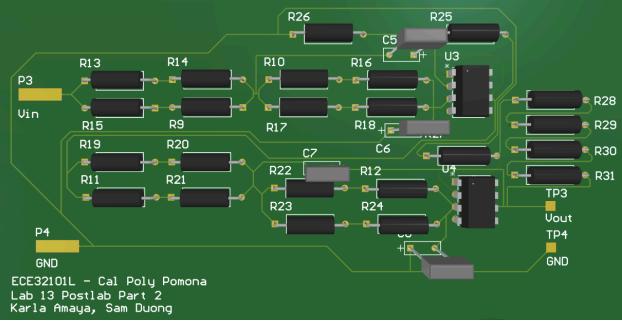


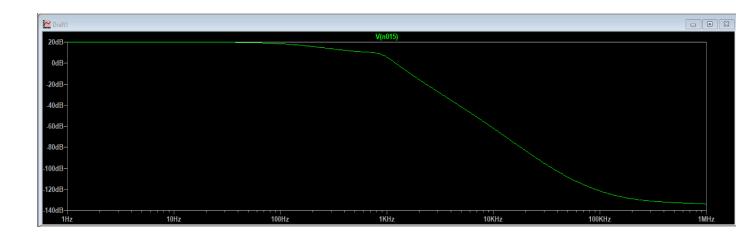




fc = 500 = 1/(2piRC) R = 1000, C = .318uF Gain = 10 = A3 x A4 A3 = 1 + (1000/1000) A4 = 1 + (4000/1000)







#### **Conclusion:**

For this lab, we got to study the response of a four pole low pass filter. We did so by creating this filter with two op-amps and a Load Resistor from which we could study voltage changes. When examining the voltage across the load resistor at various frequencies, we found that the filter we created mimicked that of an actual low pass filter when it came to its frequency response. For part two of the lab, we constructed a state-variable band-pass filter. With this filter, the center frequency was found by tuning the input frequency until the bandpass output was at its highest voltage. For our experiment, this came out to 1586 Hz which is extremely close to the calculated 1591 Hz. In addition to yielding the expected center frequency, the gain and bandwidth of our experimental filter came out to be nearly identical to what our theoretical values were. The cause for such accuracy would be due to the fact that our circuit was created through a simulation rather than in physical life. Finding the center frequency and bandwidth of this filter allowed us to understand this filter's functionality in comparison to our prior lab's butterworth filter.