IPC2BITS.__unnamed__.__unnamed__

+ T3IP0
+ T3IP1
+ T3IP2
+ __pad0__
+ SPI1EIP0
+ SPI1EIP1
+ SPI1EIP2
+ __pad1__
+ SPI1IP0
+ SPI1IP1
+ SPI1IP2
+ __pad2__
+ U1RXIP0
+ U1RXIP1
+ U1RXIP2