FCLCON1BITS.__unnamed__.__unnamed__

+ FLTMOD0
+ FLTMOD1
+ __pad0__
+ FLTSRC0
+ FLTSRC1
+ FLTSRC2
+ FLTSRC3
+ FLTSRC4
+ __pad1__
+ CLSRC0
+ CLSRC1
+ CLSRC2
+ CLSRC3
+ CLSRC4