DCTL NOR: (Positive Logic) nsisters are in cut off, so of spind verdes sol = 1/9 Vcc & S Ca to saturation cincust can supply vol Q3 92 transister is in saturation and the BNOR ad adams Fan-out

Configuration similar to RTL except base resistors are omitted when $VV_i = 0.2V$, all input transistors are in cut off, so the output $V_0 = V_{cc}$ which tries to take g_q & g_s to saturation.

Thus, the circuit can supply voltage to two transistors. Thus V_0 is clamped at

Pg - 153 to 155

 $V_0 = V_{BE(sat)} = 0.8 = V(1)$

When $\exists V_i = V(1)$, the converse ponding transistor is in saturation and the circuit connects to ground. Thus, $V_0 = V_{CE}(sat) = 0.2 = V(0)$

Hence, the cht works like a NOR gate.