

#### AHSANULLAH UNIVERSITY OF SCIENCE AND TECHNOLOGY

Department of Computer Science and Engineering

#### DIGITAL LOGIC DESIGN LAB CSE 2106

Experiment No 07

(a) Design a combinational logic circuit to Experiment Name:

convert 84-2-1 code to 2421 code.

(b) Design a combinational logic circuit to

convert the 5 bit BCD to Binarry equivalent.

#### Submitted by

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### Experiment Name:

a) Design a combinational logic circuit to convert the 84-2-1 code to 2421 code.

### Objective:

8 4 -2 -1 and 2421 codes are weighted code. They are also self complementing code because the sum of all of its weight is equal to 9. i.e. (8+4-2-1=9) and (2+4+2+1=9). So conducting binary equivalent of decimal of numbers, the numbers code and it's 9's complement code should have complementary relationship. Forz example, in 84-2-1 system, Decimal 0 = 0000 (in 84-2-1) system and 9s complement of (0=9) then 9 (in 84-2-1) should have 1111. Hence, 0 and its 9's complement i.e. 9 maintain theirz self - complement relationship which is similar to 2421 system. The objective of this experiment in to design a circuit which can convert from 84-2-1 to 2421 code.

### Treuth Table:

									and the same of th
Minterem	Decimal	I	input (8	4-2-1	)		output (	2421)	
Designation	Values	A	В	c	Д	W	×	Y	Z
0	0	0	0	0	0	D	0	0	0
7	1	0	1	1	1	0	0	0	1
6	2	0	1	1	0	0	0	1	0
5	_3	0	1	0	1	0	0	1	1
4	4	0	1	0	0	0	1	0	0
11	5	1	0	1	1	0	1	0	1
10	6	1	0	_1	0	0	1	1	0
9	7	1	0	0	1	0	1	1	1
8	8	1	0	0	0	1	1	1	0
15	9	1	1	1	1	1	1	1	1
1	à .	1	1	J					

$$W = \pm (8,15)$$

$$X = \pm (4,11,10,9,8,15)$$

$$Y = \pm (6,5,10,9,8,15)$$

$$Z = \pm (7,5,11,9,15)$$

$$d = \pm (1,2,3,12,13,14)$$

# Function Simplification:

$$W = \pm (8,15)$$

$$d = \pm (1,2,3,12,13,14)$$

CD AB	<u>cā</u>	<u></u>	CID.	сp
ĀĒ		×	×	X
AB				
A-B	×	×	1	X
₽ <u>B</u>	1			

$$W = AB + A\bar{C}\bar{D}$$
$$= A (B + \bar{C}\bar{D})$$

$$X = \angle (4,11,10,9,8,15)$$

$$d = \angle (1,2,3,12,13,14)$$

CD AB	ōō	co co		c <u>ī</u>
ĀĒ		×	*	×
ĀB	1			
AB	X	×	1	X
AB	1	1	1	1

$$Y = 2 (6,5,10,9,8,15)$$

$$d = 2 (1,2,3,12,13,14)$$

CD AB	<u>co</u>	Ĉр	ca	СФ
ĀĒ		×	×	×
ĀB		1		1
AB	×	X	1	X
AB	1	1		1

$$Y = AB + A\overline{D} + \overline{C}D + C\overline{D}$$
$$= A(B+\overline{D}) + (C+\overline{D})$$

$$Z = 2(7,5,11,9,15)$$

$$d = 2(1,2,3,12,13,14)$$

CD AB	<u>cā</u>	_CD	cD .	c <u>o</u>
ĀĒ		×	X	×
Āß		1	1	
AB	×	X	1	X
AB		1	1	

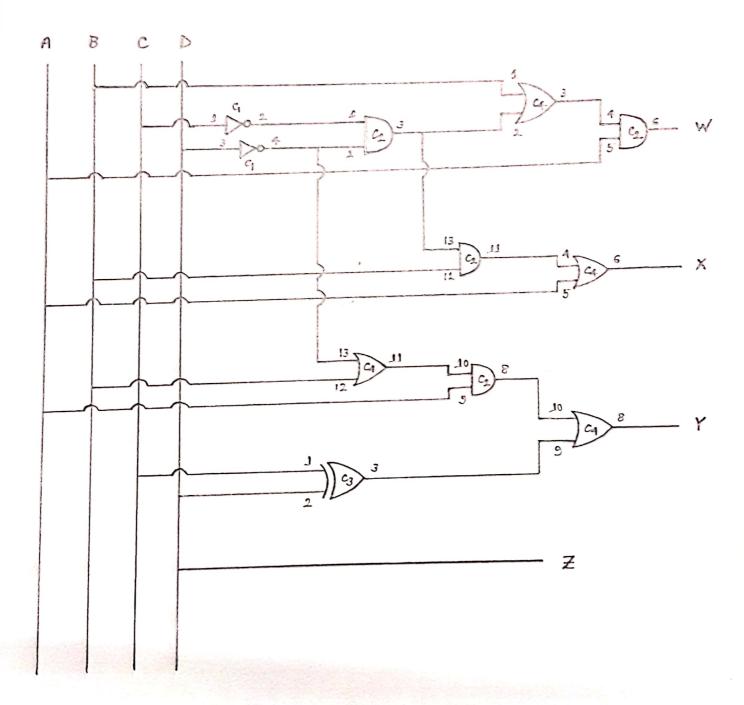
# circuit Diagrzam:

$$W = A (B + \overline{C}\overline{D})$$

$$X = A + B\bar{C}\bar{D}$$

$$Y = A(B+\overline{D}) + (C \oplus D)$$

$$Z = D$$



# IC Requirements:

- 1.  $c_1 \Rightarrow 7404 \text{ (NOT Gate)} 1 \text{ piece}$
- 2.  $c_1 \Rightarrow 7408$  (AND Gote) 1 piece
- 3.  $c_3 \Rightarrow 7486$  (XOR Gode) 1 piece
- 4.  $C_4 \Rightarrow 7432$  (OR Gote) 1 piece

## Conclusion:

In this experiment, we designed a combinational logic circuit which will convert 84-2-1 code to 2921 code. and implement it. Forz simplification of the equation we have used K-map and reducing the actual formula. If we get the correct output according to input, then our experiments was successfully completed.

b) Design a combinational logic circuit to convert the.

5 bit BCD to binarry equivalent.

# Objective:

BCD (Binarry Goded Decimal) is a system of representing numbers in which each decimal digit is represented by 4 bits. On the others hand, binory numbers is a 2 based number system. For 0 to 9, Binarry values for BCD inputs one some. But then from 10 to 15 (decimal Value) there no output of binarry. Then from 16 to 25 (BCD value) the binarry value is 6 less than the equivalent Decimal of the BCD input. Thus this process is repeated. The objective of this experiment is to design a 5 bit BCD to binorry code conventer circuit.

Trouth	Table	:
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Decimal		Inpl	of (BC	(۵			Output	(Binar	(Y)	
Values	A	В	c	۵	Ε	Fs	FA	F3	F2	亙
0	0	0	0	0	0	0	0	0	0	0
1	0	0	0	0	1	0	0	0	0	1
2	0	0	0	1	0	0	0	0	1	0
3	0	0	0	1	1	0	0	0	J	1
4	0	0	1	0	0	0	0	1	0	0
5	0	0	1	0	1	0	0	1	0	1
6	0	0	1	1	0	0	0	1	1	0
7	0	0	1	1	1	0	0	4	1	1
8	0	1	0	0	0	0	1	0	0	0
9	0	1	0	0	1	0	1	0	0	1
_10	0	1	0	1	0	×	×	×	×	×
_11	0	1	. 0	1	1	×	×	×	X	×
12	0	1	1	0	0	×	X	X	X	×
13	0	1	1	0	1	*	x	×	X	X
	0	1	1	1	0	×	X	X	X	X
15	0	1	1	1	1	×	X	×	X	X
16	1	0	0	0	0	0	1	0	1	0
_17	1	0	0	0	1	0	1	0	1	1
18	1	0	0	1	0	0	1	נ	a	0
1.9	1	0	0	1	1	0	1	L	0	1
20	1	0	1	0	0	0	1	1	J	0
21	1	0	1	0	1	0	1	1		1
22	1	0	1	1	0	1	0	0	0	0
2.3	1	0	1	1	1	1	0	0	0	1
24	1	1	0	0	0	1	0	0	1	0
25	1	1	0	0	1	1	٥	0	1	1
26	1	1	0	1	0	×	X	X	X	>
27	1	1	0	1	1	X	X	X	×	2
28	1	1	1	0	0	×	×	X	X	2
29	1	1	J	0	1	*	X	X	χ	)
30	1	1	1	1	0	×	X	X	X	>
31	1	1	1	ر	11	×	×	X	×	1

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# Function simplification:

$$F_5 = 2 (22, 23, 24, 25)$$

$$d = 2 (10, 11, 12, 13, 14, 15, 26, 27, 28, 29, 30, 31)$$

CDE	<u>CDE</u>	ĈĎΕ	<b>c</b> o∈	CDE	CDE	CDE	cōE	CDE
ĀĒ					A STATE OF THE STA			
ĀB	And the second s		×	×	×	X	×	X
AB	1	1	X	×	X	×	X	X
PB					1	1		

: simplified function, 
$$F_5 = AB + ACD$$

$$= A (B+CD)$$

$$F_4 = £ (8,9,16,17,18,19,20,21)$$

$$d = £ (10,11,12,13,14,15,26,27,28,29,30,31)$$

COE	COE	COE	COE	CPĒ	CDE	CDE	CDE
	,						
1	1	×	X	X	X	X	X
		×	X	×	×	×	×
IA	1	1	1			1	1
	COE 1						

: Simplified function, 
$$\overline{A} = \overline{AB} + \overline{ABC} + \overline{ABD}$$

$$= \overline{AB} (\overline{C} + \overline{D}) + \overline{AB}$$

$$F_3 = 2(4,5,6,7,18,19,20,21)$$

$$J = 2(10,11,12,13,14,15,26,27,28,29,30,31)$$

CDE	CDE	COE	COĒ	COĒ	CDE	cīn∈	cDE
				1	1	1	1
		×	×	×	×	×	X
		×	×	X	×	×	×
		1	1			J	1
	CDE	CDE CDE	×	××	x x [1 x	$\begin{array}{c ccccccccccccccccccccccccccccccccccc$	

: simplified function, 
$$F_3 = A\overline{C}D + \overline{A}C + C\overline{D}$$
  
=  $A\overline{C}D + C(\overline{A} + \overline{D})$ 

$$J = \angle (10, 11, 12, 13, 14, 15, 26, 27, 28, 29, 30, 31)$$

AB CDE	ΞĒ	ζŌΕ	_co∈	CDE	COĒ	CDE	ŒΕ	cō€
ĀĒ			1	1	1	1		
ĀB			×	X	X	×	×	×
AB	1	1	Х	X	×	X	X	×
AĒ	1	L					1	1

: simplified function, 
$$F_2 = \overline{AD} + \overline{AD}$$
  
=  $A \oplus D$ 

$$H = \{ (1,3,5,7,9,17,19,21,23,25)$$

$$J = \{ (10,11,12,13,14,15,26,27,28,23,30,31) \}$$

COE	COĒ	CDE	CDE	COĒ	COĒ	CDE	CŌE	cōĒ
ĀĒ		1	1			1	1	
ĀB		J	×	×	. ×	×	×	X
AB		1	×	×	×	X	X	X
AB		1	1			1	1	

simplified Function, Fi = CE+CE = E

# Minimized Expressions:

$$F_5 = A(B+CD)$$

$$F_A = A\overline{B}(\overline{c}+\overline{D})+\overline{A}B$$

$$F_3 = A\overline{C}O + C(\overline{A} + \overline{D})$$

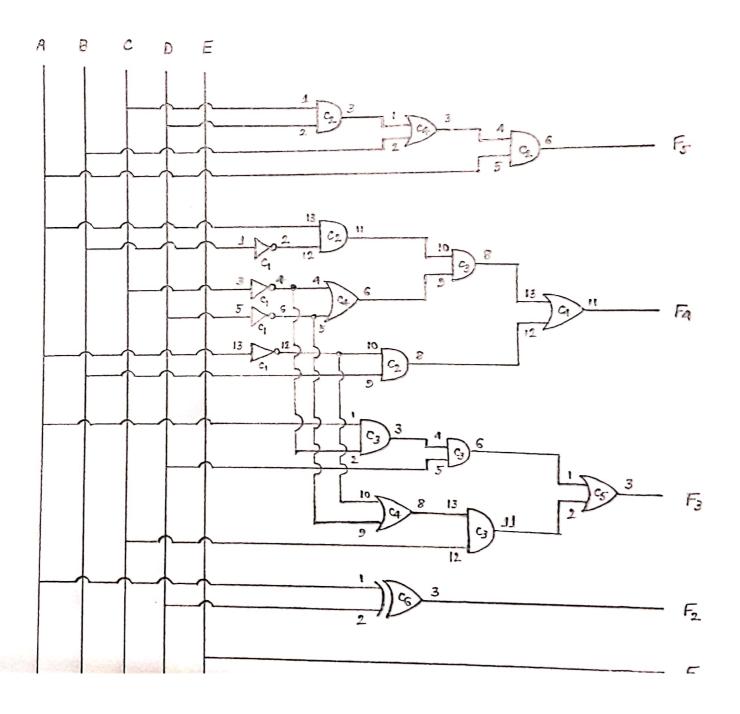
$$F_A = E$$

## circuit Diagram:

$$F_{\overline{5}} = A(B+CD)$$
 $F_{\overline{3}} = A\overline{C}D + C(\overline{A}+\overline{D})$ 
 $F_{\overline{3}} = E$ 

$$F_{\overline{3}} = A\overline{B}(\overline{C}+\overline{D}) + \overline{A}B$$

$$F_{\overline{2}} = A\oplus D$$



## IC Requirements:

2. 
$$c_2 = c_3 \Rightarrow 7408$$
 (AND Grate) — 2 pieces

3. 
$$C_4 = C_5 \Rightarrow 7432$$
 (or Gate) — 2 pieces

### Conclusion:

In this experiment, we have constructed a circuit which converts 5 bit BCD to binary equivalent. We have correfully implemented the circuit and used K-Map forz simplifying the equations. If we find desired outputs from the experiment according to the truth table, then our experiment was successfully completed.