



## **Ahsanullah University of Science & Technology**

### **Department of Computer Science & Engineering**

**Course No** : CSE3108  
**Course Title** : Microprocessors Sessional  
**Assignment No** : 04

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#### **Submitted By-**

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Question :

C4 - R1 - R3

Answer :

DSEG    SEGMENT    'DATA'

DSEG    ENDS

SSEG    SEGMENT    STACK    'STACK'

         DW            100h        DUP(?)

SSEG    ENDS

CSEG    SEGMENT    'CODE'

START    PROC        FAR

         PUSH    DS

         MOV     AX, 0

         PUSH    AX

                 MOV    AX, DSEG

                 MOV    DS, AX

                 MOV    ES, AX

                 MOV    DX, 2004h

                 MOV    AL, 0FFh        ; C4

                 OUT    DX, AL



MOV CX, 0100H

L1: LOOP L1

MOV DX, 2000h ; R1

MOV AL, 00000010b

OUT DX, AL

MOV DX, 2001h

MOV AL, 00000010b

OUT DX, AL

MOV DX, 2002h

MOV AL, 00000010b

OUT DX, AL

MOV DX, 2003h

MOV AL, 00000010b

OUT DX, AL

MOV CX, 0100H

L2: LOOP L2

MOV DX, 2000h ; R3

MOV AL, 00001010b

OUT DX, AL

MOV DX, 2001h

MOV AL, 00001010b

OUT DX, AL

MOV DX, 2002h

MOV AL, 00001010b

OUT DX, AL

MOV DX, 2003h

MOV AL, 00001010b

OUT DX, AL

RET

START ENDP

CSEG ENDS

END START