

Ahsanullah University of Science & Technology

Department of Computer Science & Engineering LAB REPORT

Course No : EEE-2142

Course Title : Electronics Device & Circuits Lab

Experiment No : 06

Experiment Name: The BJT Biasing Circuits

Submitted By-

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Group No : 06

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Nome of the Expertiment: The BUT Biasing Circuits.

Objective: study of the BJT Biasing circuits.

Equipments And Components:

Serial No.	Component Details	Specification	Quantity	
1.	NPH Transistorz C828, BD135		1 piece each	
2.	Resistora	470 n, 560 n, 220 Kn	1 piece each	
3.	РОТ	10 K.D.	1 unit	
4.	Trainerz Board		1 wit	
5.	DC Powerz Supply		1 unit	
6.	Digital Multimeterz		1 unit	
7.	chords and wire		as required	

Experimental setup:

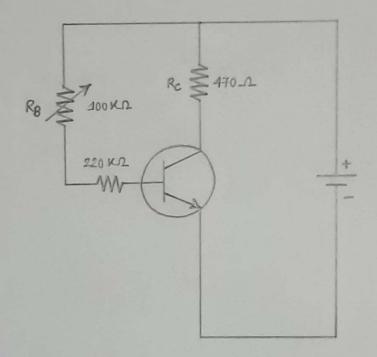


Figure: Fixed Bias Circuit

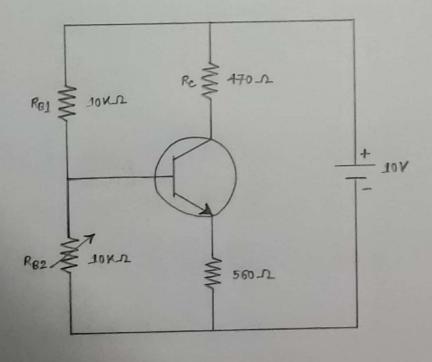


Figure: self Bias Cincuit

Data Sheet:

Table - 1: Data for Fixed Bias Circuit

Transistor	Re (A)	Ve (volt)	$I_{c} = V_{c}/R_{c}$ (mA)	Ve E (volt)	a-point
C828	466	4.34	9, 313	5.05	(5.05, 10.44)
6D135	466	4.16	8.927	5.24	(5.24, 10)

Table - 2: Data for Self Bias Circuit

Transistor	Re (-12)	Vc (volt)	$I_c = V_c / Rc$ (mA)	VeE (volf)	Q-point
c828	466	2.2	4.72	4.97	(4.97, 4.8)
вы35	466	2.23	4.78	4.91	(4.91, 5.16)

Calculation:

Ic fore fixed - bias cirrcuit,

$$T_C = \frac{V_{CC}}{R_C} = \frac{10V}{466} = 21.45 \text{ mA}$$

Ic for self-bias circuit,

$$T_{c} = \frac{V_{cc}}{R_{c} + 560} = \frac{10}{460 + 560} = 9.74 \text{ mA}$$

Report:

1) Which circuit shows better stability? Explain in the context of the results obtained in the laboratory.

Answerz:

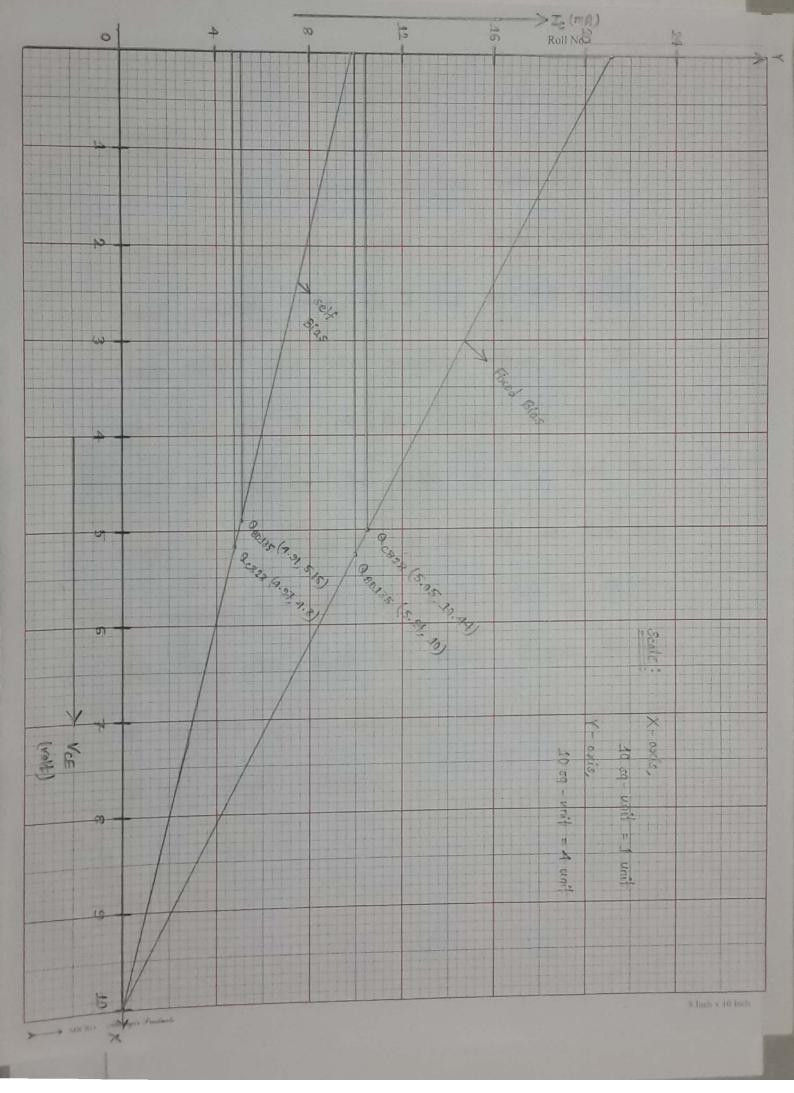
The self bias circuit shows better stability. this experiment, two biasing circuits were used to study the BJT biasing circuits. They were: 1) Fixed Bias Cirrcuit & 2 Jeff Bias Cirrcuit. In fixed bias circuit, the values of Ic, Vc & VCE has been different transistors (C828 & BD135). different for two But their resistor Re was same. The difference is much morre variable because BJT is temperature dependent and for this reason the circuit show more vorciability. On the others hand, in self bias circuit, the value of Ic, Vc & VCE were almost some for different transistors. For all of this

meason we can say that the self bias eincuit shows more stability.

2 Draw the DC load line forz both the circuits and show the a-point.

Answerz :

The DC load line for both the circuits and the Q-points are shown in the attached graph.



Discussion:

In this experiment, we have implemented the BJT biasing circuits. Firstly, we should connect the circuit carrefully. Then, VRC & VCE for both transister (0828, BD135) were taken correfully. After that, force both fixed bias & self bias circuits the value were taken. By observing the graph we have found the a-point for the circuits. At the end, we have come to conclusion that the self bias circuit is stable than the fixed blas circuit because the values of Ier Ve & VeE in self bias circuit were almost some fore two different transistore.