



Ahsanullah University of Science and Technology (AUST)
Department of Computer Science and Engineering

LAB REPORT

Course No. : CSE2210

Course Title: Digital Electronics and Pulse Techniques Lab

Experiment Number: 03

Name of the Experiment: Study of a TTL NAND Gate with totem-pole output.

Submitted By:

180204142

S. M Tasnimul Hasan