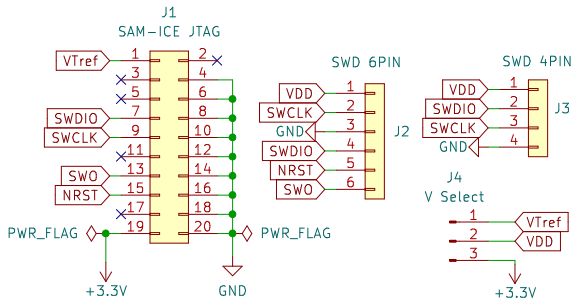


VTref	1	●	●	2	NC
Not used	3	●	●	4	GND
Not used	5	●	●	6	GND
SWDIO	7	●	●	8	GND
SWCLK	9	●	●	10	GND
Not used	11	●	●	12	GND
SWO	13	●	●	14	GND*
RESET	15	●	●	16	GND*
Not used	17	●	●	18	GND*
5V-Supply	19	●	●	20	GND*

Table 5. Debug connector CN4 (SWD)		
Pin	CN4	Designation
1	VDD_TARET	VDD Target
2	SWCLK	SWD clock
3	GND	ground
4	SWDIO	SWD data input/output
5	NRST	Reset target STMP
6	SWO	Reserved

Figure 9. Using ST-LINKV2-1 to program the STM32 on an external application



Samiul

Sheet: /  
File: JTAG\_to\_SWD.kicad\_sch

Title: JTAG to SWD

Size: A4 Date: 2023-10-16

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