Micro instruction OP code	T-state	Cp	Lp	Ep	Lm	Ce	Li	Ei	La	Ea	Sa	Sb	S1	So	Cin	Eu	Lt	Lb	Eb	Lc	Ec	Lo	Hex code
All	T_1	0	0	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	300000
	T_2	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	800000
	T_3	0	0	0	0	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0C0000
0EH MVI B,byte	T_4	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	1	0	0	0	0	020080
	T_5	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	T_6	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
14H SBI byte	T_4	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	020500
	T_5	0	0	0	0	0	0	0	0	0	0	1	0	0	1	0	0	0	0	0	0	0	0102.0E
	T_6	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
2H DCR A	T_4	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	1	0	0	0	0	0	011200
	T_5	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	T_6	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
3H XRA B	T_4	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	1	0	0	0	0	0	000140
	T_5	0	0	0	0	0	0	0	1	0	0	1	1	0	0	1	0	0	0	0	0	0	013200
	T_6	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0
4H ORA C	T_4	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	1	000110
	T_5	0	0	0	0	0	0	0	1	0	0	1	0	1	0	1	0	0	0	0	0	0	011A00
	T_6	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

		CP LP LP LM Ce LpEiLe Eq Sq S₁ S₁ So ciEnLe LqEqELe Le Lco					Hc.A
5H ANA B	T4	000 0	0000 00d0		0000	0000	00d4u
	T5	0000	0000 1010		0000	0000	
	T6	0	0 0		0	0	0
6H XRI byte	T4	000 0	000 10	000 1	0000	0000	029100
	T5	0 0.0 0	000 1	00 1,1	0000	0000	0l3200
	T6	0	0	0	0	0	0
7H CMA	T4	0000 0	000 1	0010 1010	0000	0000	012900
	T5	0	0 1	0	0	0	0
	T6	0	0	0	0	0	0
8H DIV2	T4	0 0000	0000	11 00	0000	0000	b0c000
	T5	0 0	0 0	0 0	0	0	0
	T6	0	0	0	0	0	0
9H LDC=00	T4	000 1	0010 0000	0000	0000	120000
	T5	0000	1000 0000	0010	0000	080020
	T6	0	0	0	0	0
AH mov n,B	T4	00 00 0	0001 0000	0100 0000	0000	
	T5	000 00	0000 0000	0 0	0,0
	T6	0	0	0	0

Op Code	Micro Instr	Tstate	Micro operation	Active	RoN
	All	T_1	MAR = PC	Ep,Lm	300000
		T_2	PC = PC+1	Cp	800000
		T_3	IR = RAM[MAR]	CE,Lp	0C0000
OH	MVI byte	T_4	B= IR	Ep,Lc	020020
		T_5	—	—	
		T_6	—		
1H	SBI byte	T_4	Temp=IR	Ei,Lt	020100
		T_5	A =ALU(A-temp)	Eu,Lq	010200
		T_6	—	—	—
2H	DCR A	T_4	A= ALU(A+1)	Eu,Lq,S_1	011200
		T_5	—	—	=
		T_6	—	—	=
3H	XRA B	T_4	Temp = B	Ec,Lt	000110
		T_5	A = ALU	S_2,S_1,S_0, Eu,Lá	013A00
		T_6	—		—
4H	ORA e	T_4	temp = c	Eg,Lt	000110
		T_5	A = ALU	S_1,S_0,Eu, Lq	011A00
		T_6	—	—	-0
5H	ANA B	T_4	Temp = B	Lk, EB	000140
		T_5	A = ALU	S_2, Eu, Lo	0|2200
		T_6	—	—	0

6H	XRI byte	T4 T5 T6	temp = IR A = ALU —	Lt, Ep S1,So , Eu,Lg —	090110 011A00 000000
7H	CMA	T4 T5 T6	A = ALU(A') — —	S2,So, Eu,Lg 0 0	012A00 H0 0
8H	DIV 2	T4 T5 T6	A = SHLA — —	Eg , Sa — —	00C000 H7 —
9H	LDC add	T4 T5 T6	MAR = IR C = RAM —	Lm, Ei CE, Lg —	120000 090000 —
AH	mov A,B	T4 T5 T6	A = B — —	La, Eb 0 0	000090 000000 0
BH	mov C,B	T4 T5 T6	C = B — —	Lc, Eb 0 0	010010 000000 000000
CH	Jmp	T4 T5 T6	PC = IR — —	Ep, Lp — —	420000 000000 000001

DH	OUT	T_4 T_5 T_6	OUT=A —	La, Ea 0	product 0
EH	NOP	T_4 T_5 T_6	none	none	—
FH	HLT	T_3	none	none	—