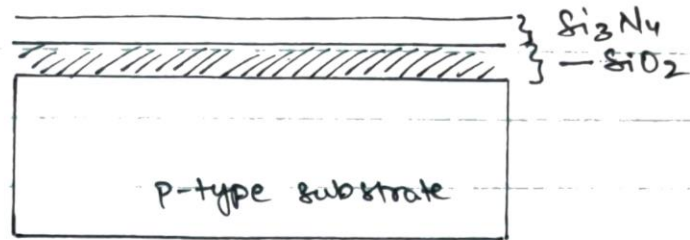


PROCESS PROJECT
INTEGRATED CIRCUIT TECHNOLOGY.

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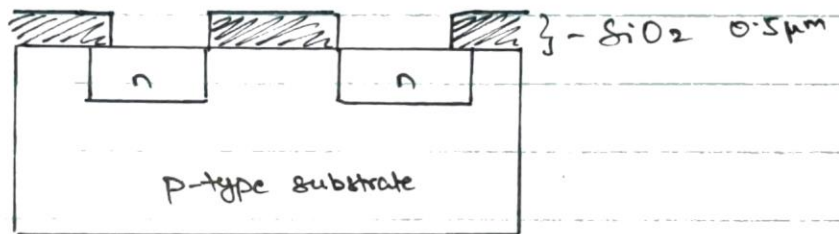
MOSFET STEPS LAYOUT

Step-①



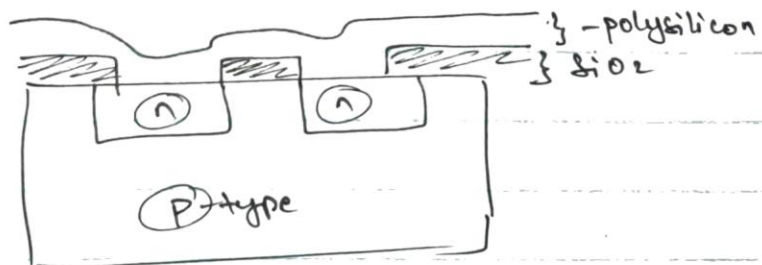
(a)

Step-②

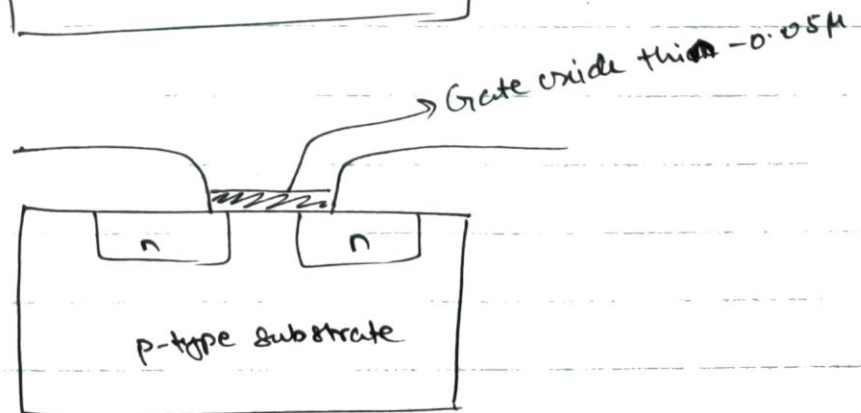


(b)

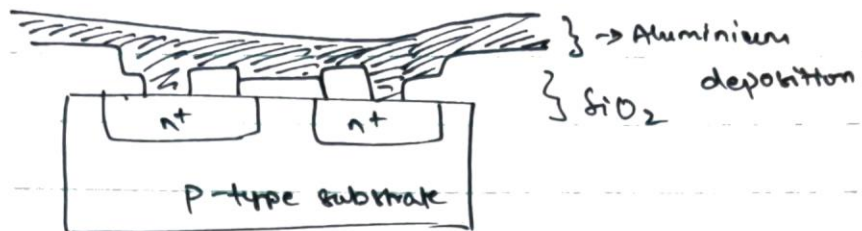
Step-③



Step-④



Step - (5)



Step - (6)

