A register as part of the 32x32 bit Paplinski's "WORD SERIAL MULTIPLIER"

A register in particular is a sequential and works when the clock is high and the register works in such a way that when op <= 0 data will not be loaded when '1' shifted to the next bit and load again... and go The below mentioned waveform represents the basic functionality of a register which is used as a memory unit as part of a storage space and transfer data for immediate use by the CPU i.e memory register

