ALU - Arithmetic and Logic Unit designed as part of the Paplinski's "WORD SERIAL MULTIPLIER"

The function of the ALV is to perform operations like addition and subraction, The F - output from the ALV is loaded into the A-register through arithmetic shift right operation

Least significant bit f0 is shifted into the Q register, The inputs to the ALV A and B are taken from the D register and A register respectively, again the output from the ALV with F = F/2 is performed and fed

	TimeA - 60ns	_									
	TimeA	50ns	10001101	10000101	1	X 20001202		268439809	X 268435713	1	V
		40ns	01100111	011000111	п	01100111		17826065	17825809	п	
	•	30ns	11100101	01000001	2	(10100100		286261505	(16777217	2	1
		20ns	101	1001		1102		473	1249		44.00
		10ns	11111111 01000101	111101111	3	11111111 12100102		2863311	2863270 🕨 🗡 286261249	3	
Baseline = 0			00000000	000000000	٥	000000000		°	٥	°	<u> </u>
			*h10001101	*h10000101	'h1	'h20001202		.d268439809	.d268435713	TP.	***********
Cursor = 60ns	Baseline = 0	Cursor-Baseline = 60ns	1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	+ Brb	T D Opto	+ F. th	1	+ P	+	+ P opto	- E