padmanabhunil - U88648766 || USF EE

Dapath has connections to all the registers and it is connected to the Control unit

It has the inputs - qq, dd and output is aq, it is connected with the control unit with the zI and op connections as the internal signals

Base-line = 0	TimeA = 1600ms	0 100ns 200ns 300ns 400ns 500ns 500ns 700ns 700ns 700ns 100ns 1100ns 1200ns 1300ns 1300ns 1300ns 1300ns		5355555 (0000000 )	(0000000 )	(n) (15 )(s)		000000000000000000000000000000000000000
Cursor = 1600ns	Baseline = 0	Cursor-Baseline = 1600ns	0	*h00000000	000000004.	, 169	0	00000000
		Curi	CIK_th	T dd th		⊕ op_tb	zI_tb	aq th