

Samruddhi Joshi

Wakad, Pune, Maharashtra – 411057
+91-9271007802
samruddhi2003joshi@gmail.com
linkedin.com/in/samruddhi-joshi
github.com/samruddhi427

PROFESSIONAL SUMMARY

Motivated and detail-oriented **Electrical and Computer Engineering** student with strong fundamentals in **power systems, PLC/SCADA, MATLAB Simulink, and project management**. Experienced in designing automation systems, transformer load-sharing circuits, and electrical simulations. Skilled in CAD, PCB design, and electrical tools with practical exposure to industrial workflows. Eager to contribute to large-scale **EPC projects in water treatment and process engineering** with strong problem-solving ability, adaptability, and teamwork.

EDUCATION

MIT World Peace University, Pune	2022–2026 (Expected)
B.Tech. Electrical and Computer Engineering	CGPA: 6.8 (6th Sem)
CBSE Class 12	2022, 63%
CBSE Class 10	2020, 93%

TECHNICAL SKILLS

- **Core Electrical:** Power Systems, Electrical Machines, Control Systems, Protection Systems
- **Automation & Tools:** PLC, SCADA, MATLAB Simulink, PCB Design, AutoCAD
- **Programming & Software:** C/C++, Python (for simulations), SQL (for data handling)
- **Project Management Tools:** MS Project, Git, GitHub (for collaboration)

PROJECTS

Load Sharing of Transformers with Automatic Cutoff System

- Developed IoT-enabled system with CT and temperature sensors for predictive fault detection.
- Applied system integration principles ensuring reliable power distribution.

PLC Automation Projects (Traffic Light, Bottle Filling)

- Designed ladder logic programs in PLC and verified automation workflows for safety and efficiency.

High-Performance Predictive Analytics for Energy Grids

- Implemented forecasting algorithms in MATLAB/Python for demand prediction.
- Designed anomaly detection dashboards ensuring computational efficiency.

Mesh Generation and Simulation (Academic Project)

- Applied computational geometry and C++ data structures for handling geometric meshes.

CERTIFICATIONS

- EV Technology – IIT Jodhpur
- NPTEL – CMOS Digital & VLSI Design
- Skill Lync Training in Electrical Systems
- Data Structures and Algorithms Certification
- Full Stack Development Badge

WORK HISTORY

Computer Society of India (CSI), MIT-WPU Student Chapter (07/2023–07/2025)

- Organized workshops on automation, simulations, and electrical project design.
- Collaborated with peers on technical projects emphasizing industrial applications.