SITCON'21

FLYNN'S CLASSIC TAXONOMY

SISD

Single Instruction
Single Data

SIMD

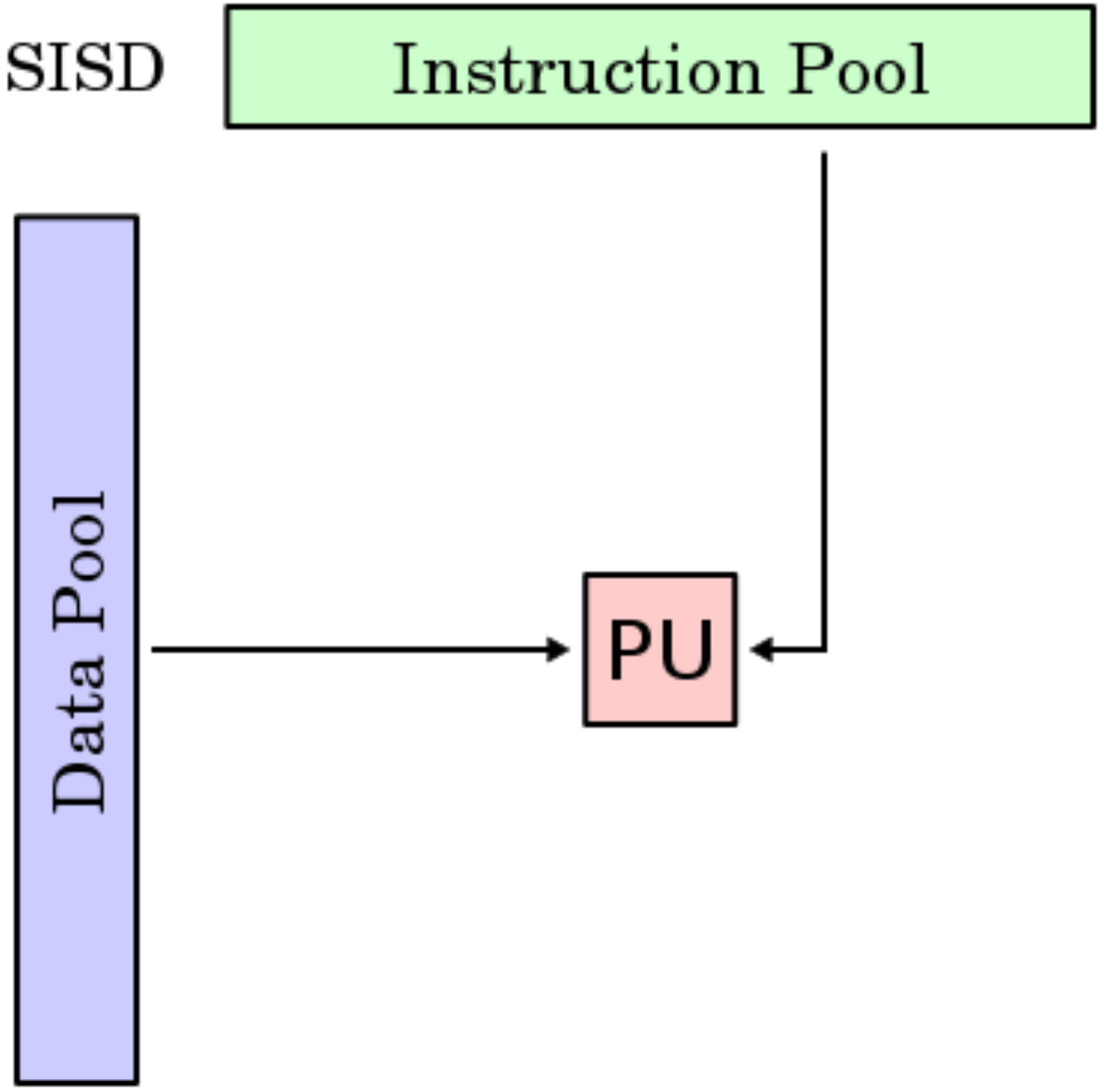
Single Instruction
Multiple Data

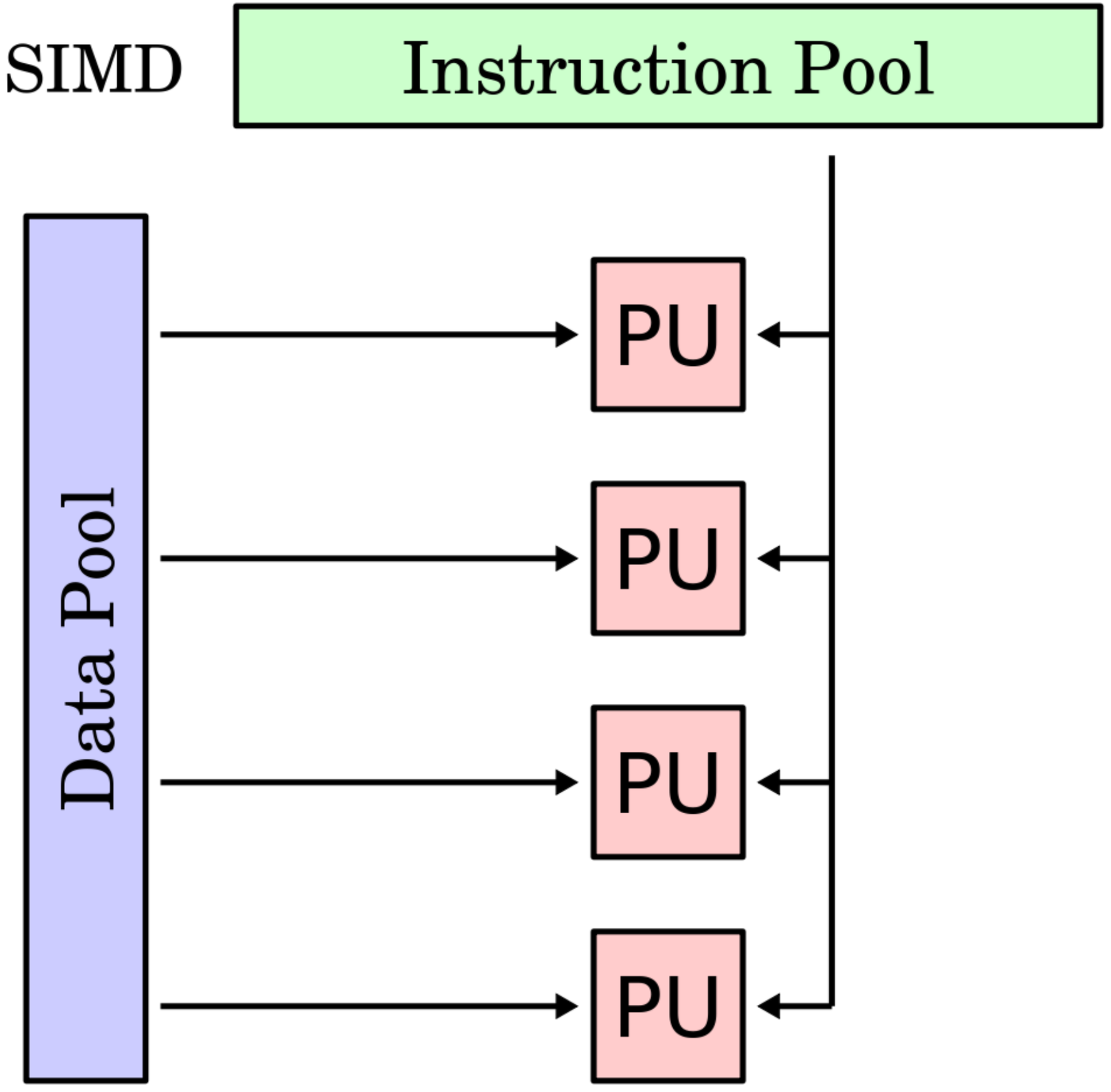
SIMD

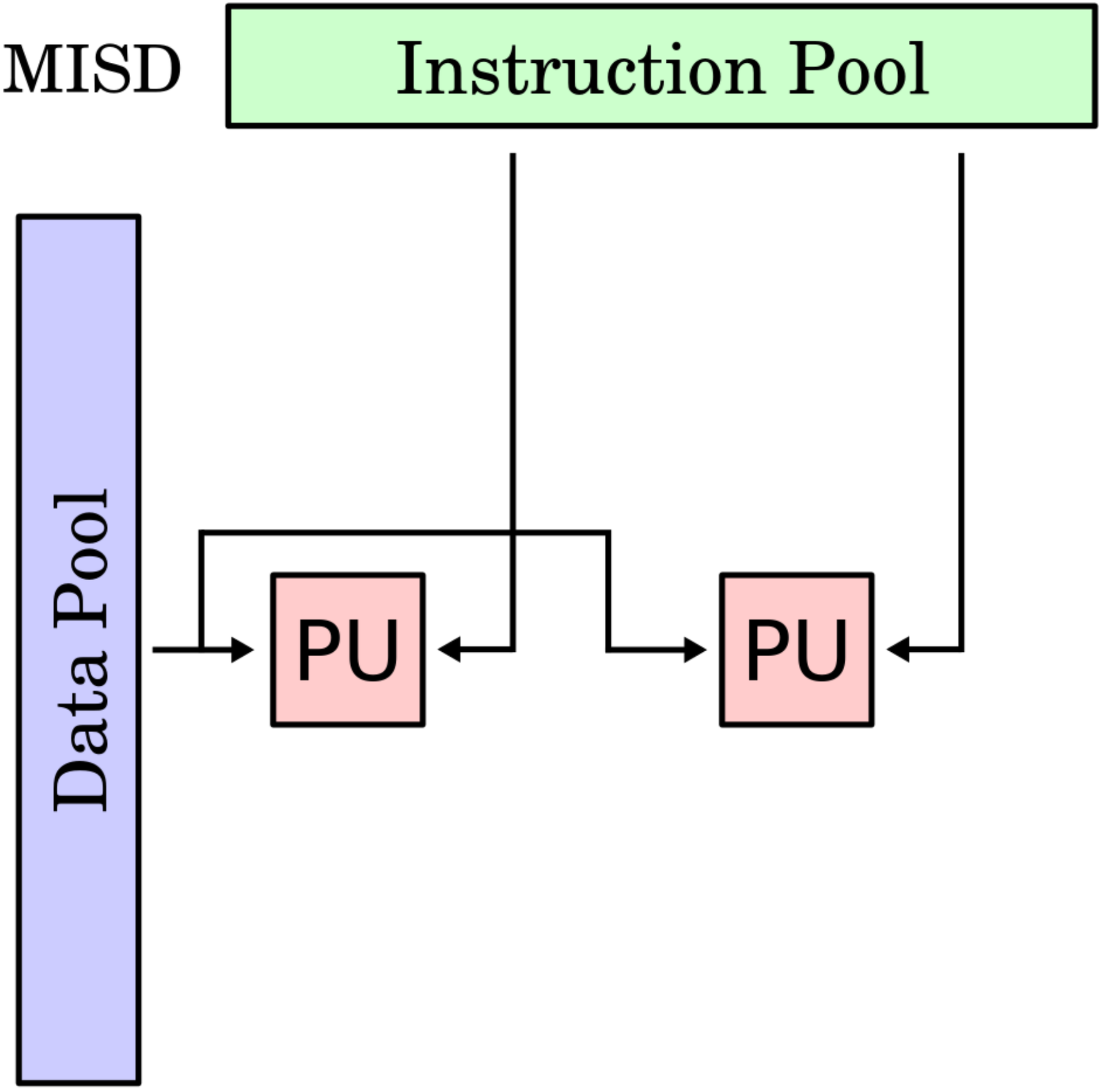
Single Instruction
Multiple Data

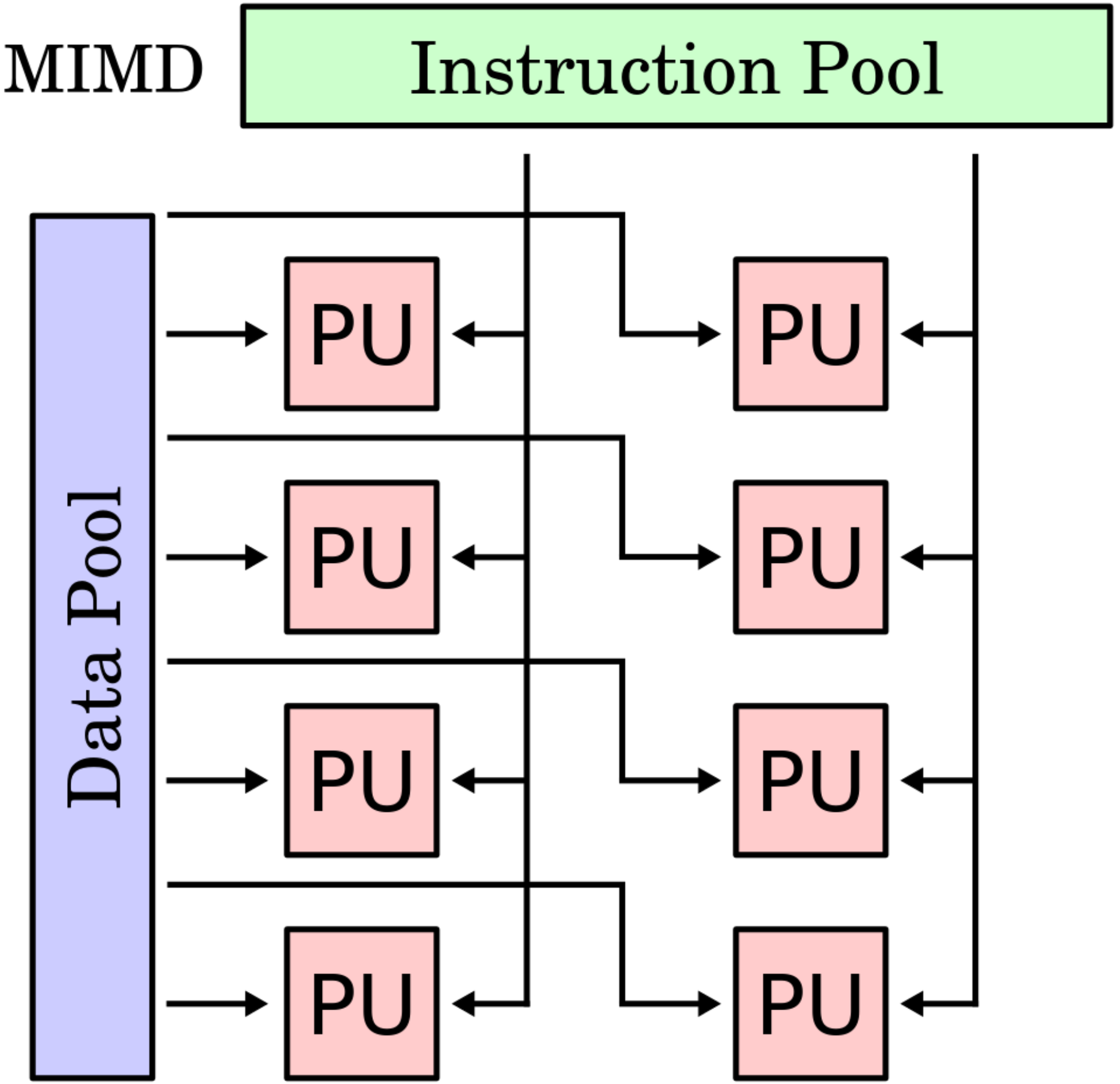
MIMD

Multiple Instruction
Multiple Data


SISD
Instruction Pool
Data Pool
PU


SIMD
Instruction Pool
Data Pool
PU
PU
PU
PU


MISD
Instruction Pool
Data Pool
PU
PU


MIMD
Instruction Pool
Data Pool
PU
PU
PU
PU
PU
PU
PU
PU

CPU

GPU

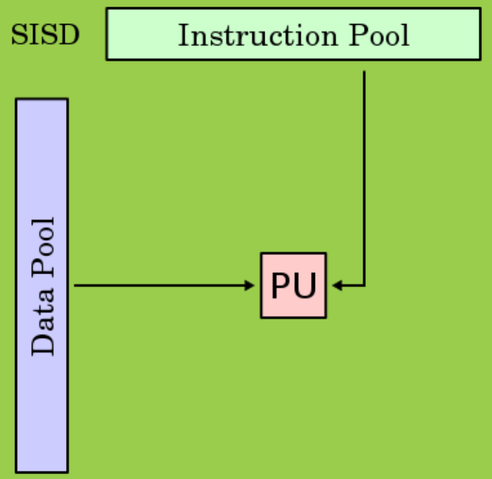
Vector Processor

FLYNN'S CLASSIC TAXONOMY

SISD

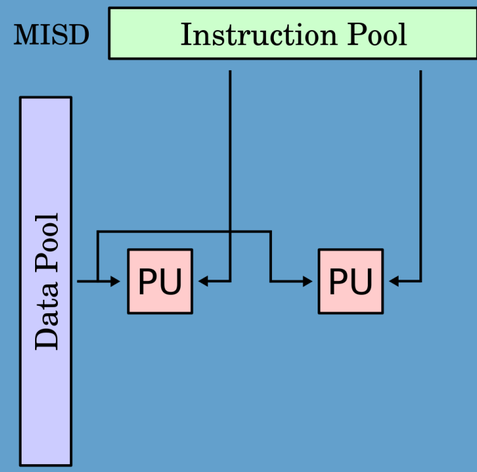
Single Instruction
Single Data

CPU



SIMD

Single Instruction
Multiple Data

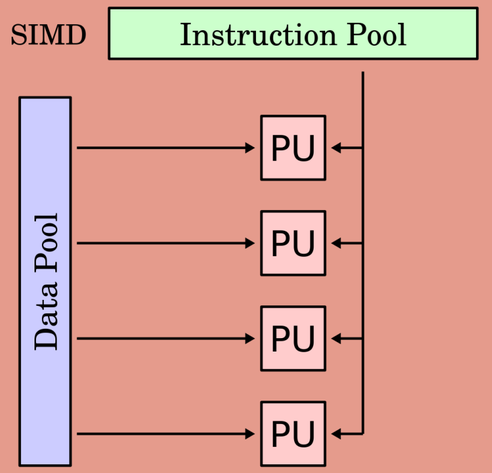


SIMD

Single Instruction
Multiple Data

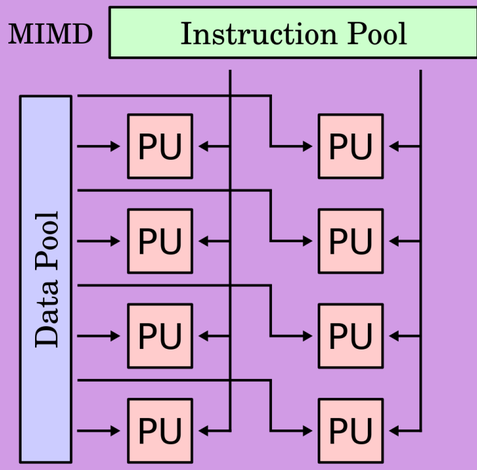
GPU

Vector Processor



MIMD

Multiple Instruction
Multiple Data



SITCON'21

EXECUTION MODEL

Software

Hardware