

# CS 6290 Project 3: Cache Coherence

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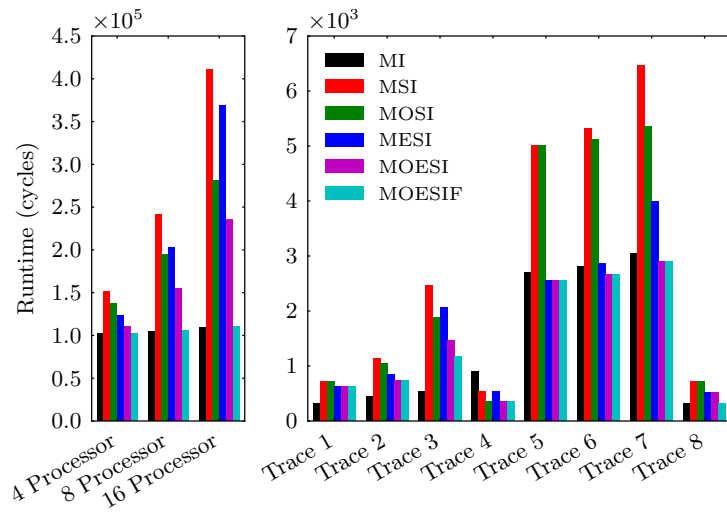


Figure 1: Runtime (in clock cycles) of the various trace simulations using each cache coherency protocol. The longer validation runs are on the left plot, and the short traces are on the right.

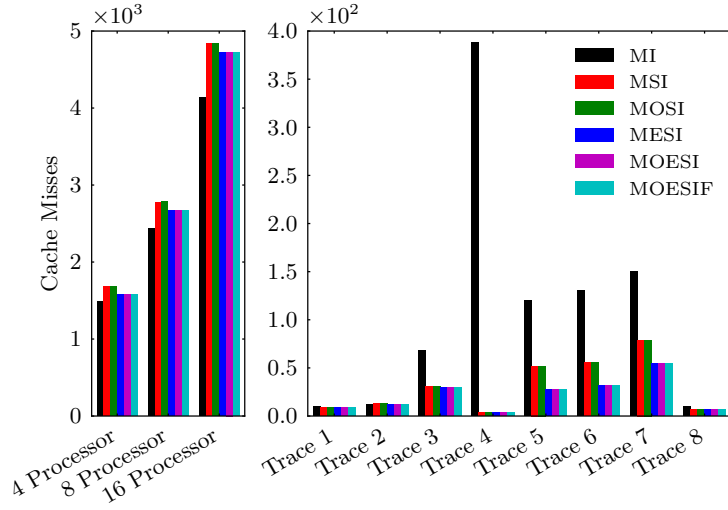


Figure 2: Number of cache misses during simulation of the various traces using each cache coherence protocol.

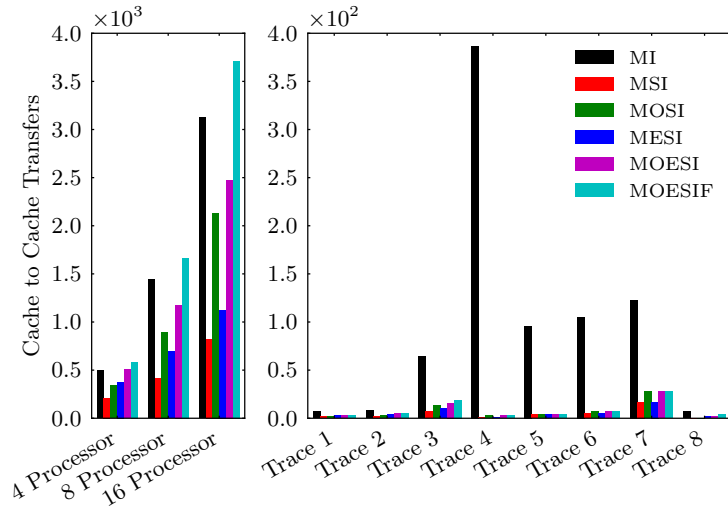


Figure 3: Number of cache-to-cache transfers during simulation of the various traces using each cache coherence protocol.

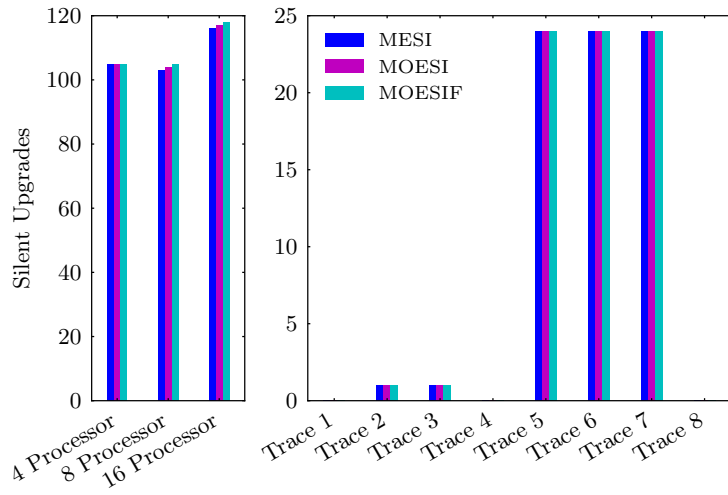


Figure 4: Number of silent upgrades to the modified state from the exclusive state during simulation of the various traces using each cache coherency protocol that contains an exclusive state.