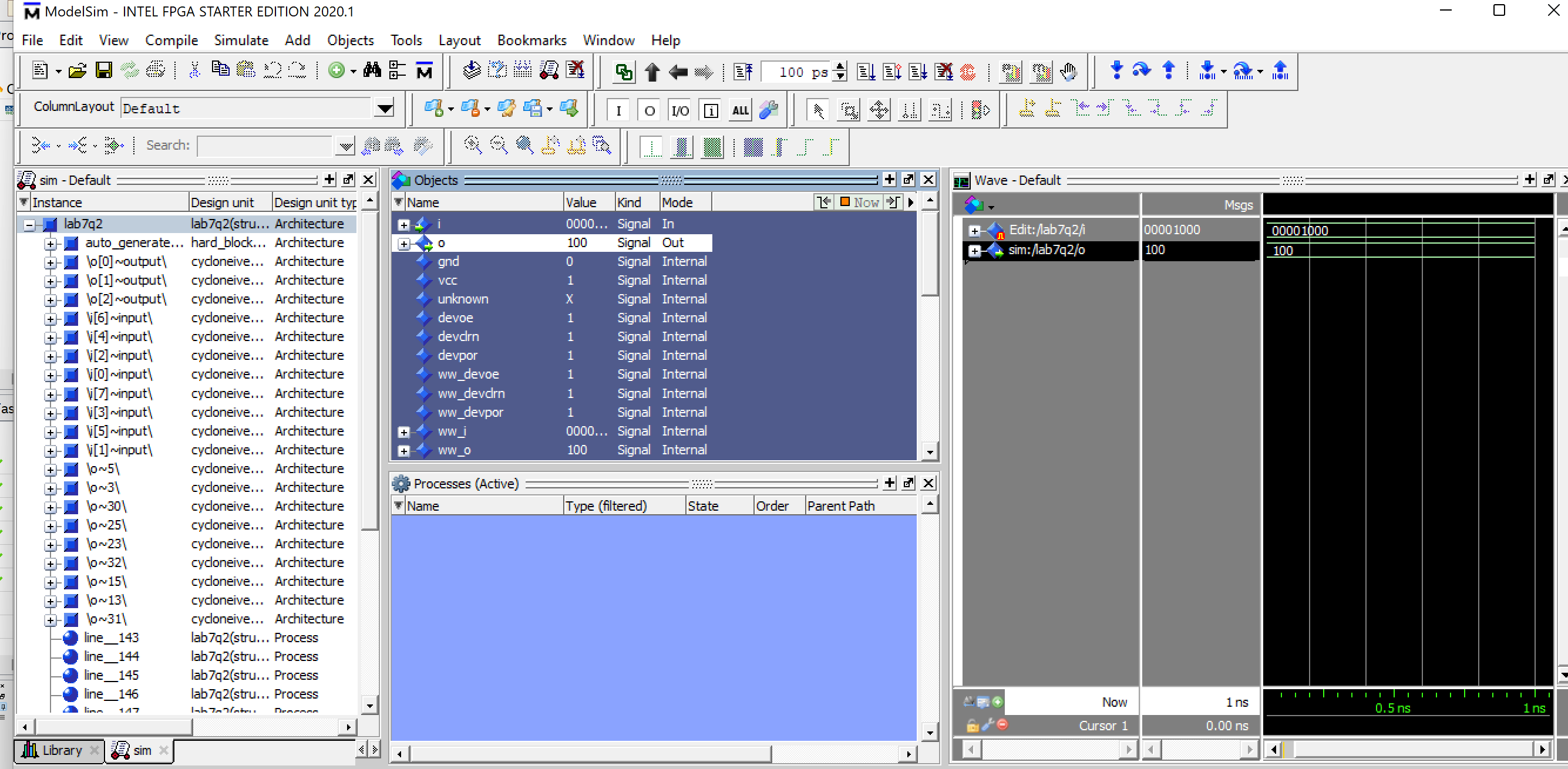
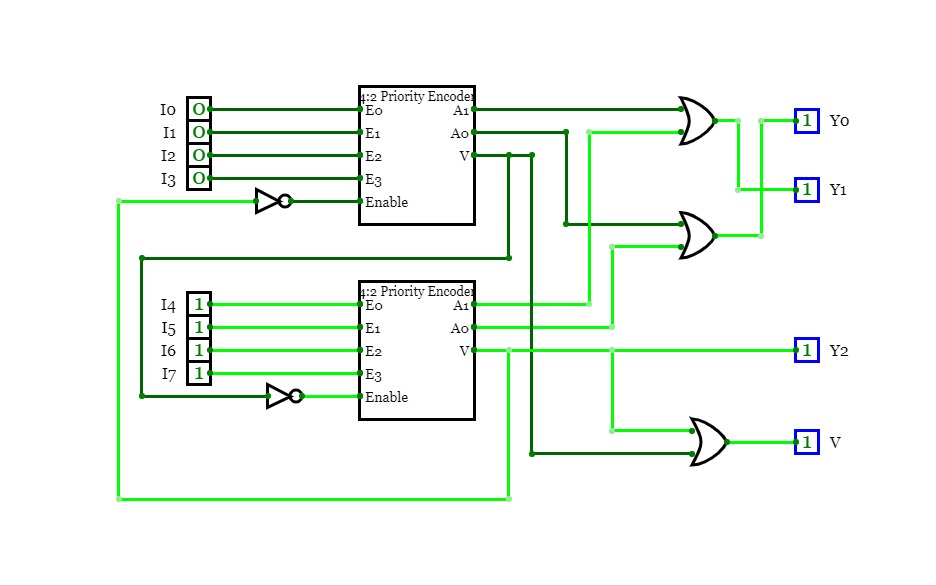
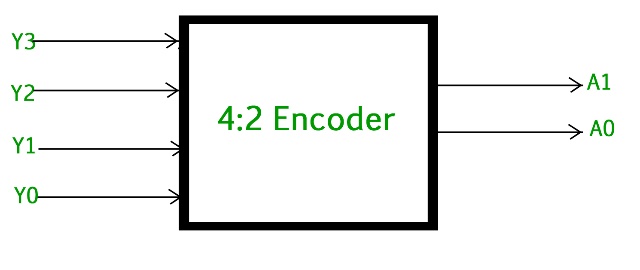


Model sim of 4:2 encoder



Model Sim simulation showing the waves of a 8:3 encoder using two 4:2 decoders.



Graphical user interface, text

Description automatically generatedGraphical user interface, table

Description automatically generated with medium confidence

Diagram of a 4:2 encoder and a 8:3 encoder using two 4:2 encoders with VHDL code representing each.

A close-up of a circuit board

Description automatically generated with medium confidence

Project uploaded to FPGA board representing i(3) being enabled which shows 011.