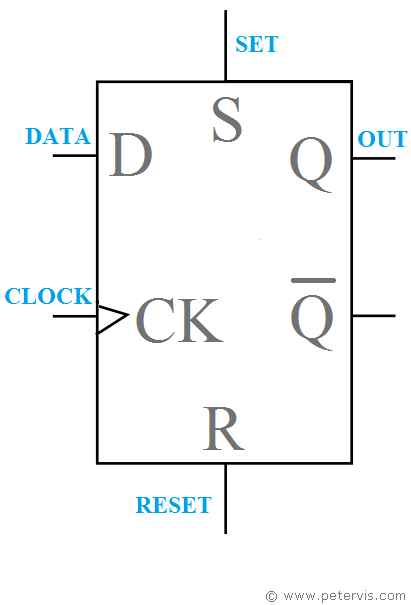


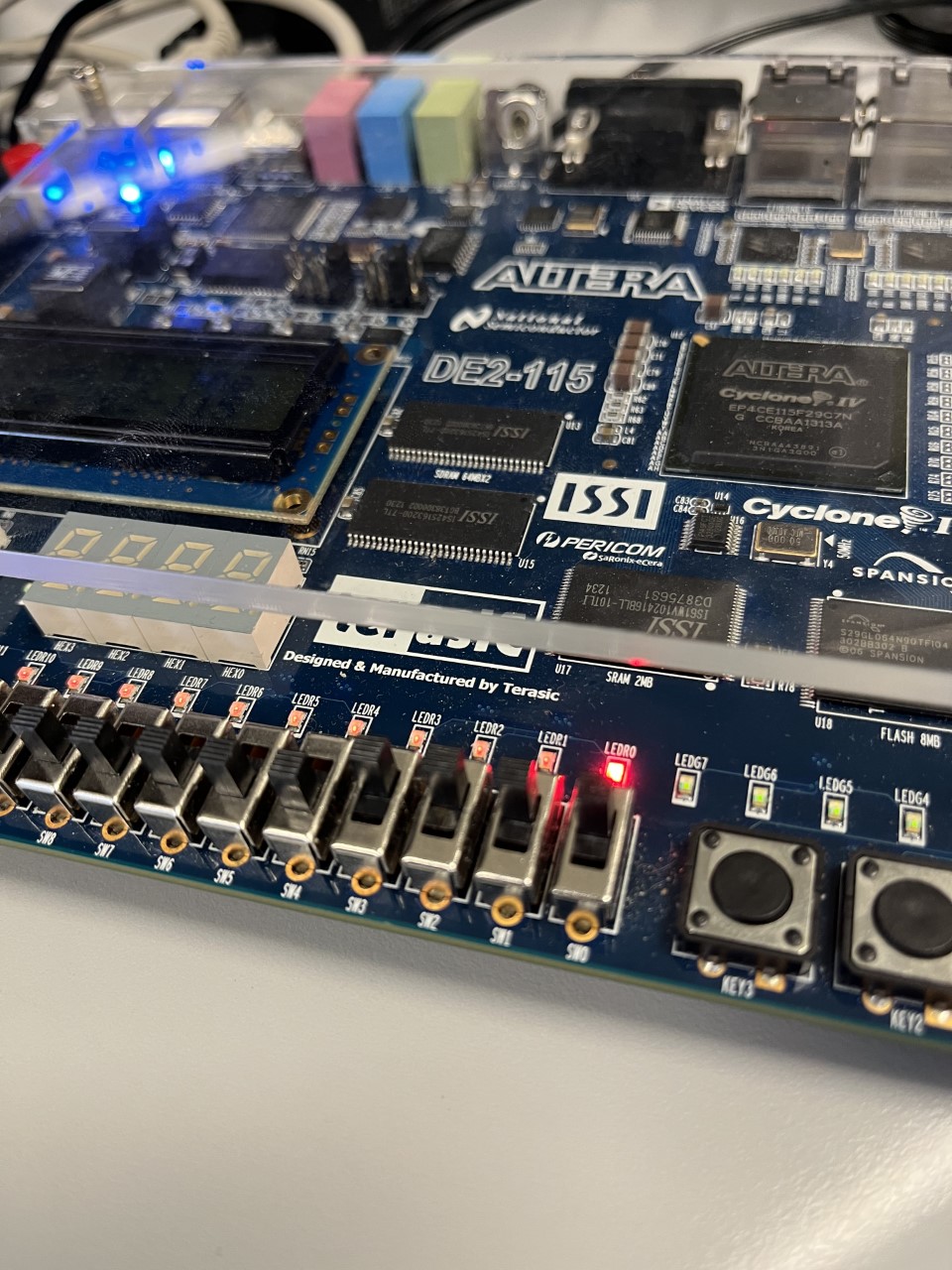
Modelsim of D flipflop



Graphical user interface, text, application

Description automatically generated

Diagram of a D flip flop with VHDL code representing it



Project uploaded to FPGA board with D and clock enabled