Table 15. Alternate functions

							200		io. Alternate idirenoils								
		AF0	AF1	AF2	AF3	AF4	AF5	AF6	AF7	AF8	AF9	AF10	AF11	AF12	AF13	AF14	AF15
Port		SYS_AF	TIM2/TIM15/ TIM16/TIM17/ EVENT	TIM1/TIM3/ TIM15/ TIM16	TSC	I2C1/TIM1	SPI1/Infrared	TIM1/Infrared	USART1/USA RT2/USART3/ GPCOMP6	GPCOMP2/ GPCOMP4/ GPCOMP6	CAN/TIM1/ TIM15	TIM2/TIM3 /TIM17	TIM1	TIM1	OPAMP2	ı	EVENT
	PA0	1	TIM2_CH1/TI M2_ETR	ı	TSC_61_101	I	1		USART2_CTS	ı		ı	1	1	,	ı	EVENTOUT
	PA1	ı	TIM2_CH2	-	TSC_G1_102	ı	I	1	USART2_RTS _DE	1	TIM15_CH1N	ı	ı	1	I	ı	EVENTOUT
	PA2	-	TIM2_CH3		TSC_G1_103	-	1	-	USART2_TX	COMP2_OUT	TIM15_CH1	_	-	1		ı	EVENTOUT
	PA3	1	TIM2_CH4	ı	TSC_G1_I04	1	1	ī	USART2_RX	1	TIM15_CH2	1	ı	1		ı	EVENTOUT
	PA4	1	ı	тімз_сн2	TSC_G2_IO1	-	SPI1_NSS	ı	USART2_CK	1		į	1	ī	•	ı	EVENTOUT
	PA5	ı	TIM2_CH1/TI M2_ETR	-	TSC_62_102	Į	SPI1_SCK			ı	-	ı	ı	1	ļ	ı	EVENTOUT
	PA6	-	TIM16_CH1	TIM3_CH1	TSC_G2_I03	-	SPI1_MISO	TIM1_BKIN		1	i	-	1	-		ı	EVENTOUT
Port A	PA7	1	TIM17_CH1	TIM3_CH2	TSC_G2_I04	-	SPI1_MOSI	TIM1_CH1N		1		-	1	-		1	EVENTOUT
	PA8	MCO	Ť	-	-	-	Ť	тім1_сн1	USART1_CK	-	1	1	-	=		^	EVENTOUT
	PA9	1	-	-	TSC_64_101	-	Ť	тім1_сн2	USART1_TX	-	TIM15_BKIN	TIM2_CH3	-	=		1	EVENTOUT
	PA10	-	TIM17_BKIN	-	TSC_64_102	=	=	тім1_снз	USART1_RX	COMP6_OUT	j	TIM2_CH4	-	=		1	EVENTOUT
	PA11	1	ı	1	-	-	ì	TIM1_CH1N	USART1_CTS	-	CAN_RX	į	TIM1_CH4	TIM1_BKIN2		ı	EVENTOUT
	PA12	ı	TIM16_CH1	1	ī	į	ij	TIM1_CH2N	USART1_RTS _DE	COMP2_OUT	CAN_TX	ı	TIM1_ETR			1	EVENTOUT
	PA13	JTMS/SWDAT	TIM16_CH1N	_	TSC_64_103	-	IR_OUT	-	USART3_CTS	-		-	-	-	-	-	EVENTOUT
	PA14	JTCK/SWCLK	ı	ı	TSC_G4_104	I2C1_SDA	1	TIM1_BKIN	USART2_TX	1		1	1	1	1	1	EVENTOUT
	PA15	IOTU	TIM2_CH1/ TIM2_ETR	-	TSC_SYNC	I2C1_SCL	SPI1_NSS	-	USART2_RX	•	TIM1_BKIN	ı	1	-		1	EVENTOUT
	PB0	-	-	тімз_снз	TSC_G3_102	-	=	TIM1_CH2N		-		-	-	_	=	1	EVENTOUT
	PB1	ı	ř	TIM3_CH4	TSC_G3_IO3	-	È	TIM1_CH3N	·	COMP4_OUT	į	į	_	•		ij	EVENTOUT
	PB2		1	ı	TSC_G3_IO4	1	ì	-			i	ı	-	_		ı	EVENTOUT
	PB3	JTDO/TRACE SWO	TIM2_CH2	1	TSC_G5_I01	Į	SPI1_SCK	1	USART2_TX	į	ı	TIM3_ETR	ı			ı	EVENTOUT
Port B	PB4	NJTRST	TIM16_CH1	TIM3_CH1	TSC_G5_102	Ī	SPI1_MISO	-	USART2_RX	1	-	TIM17_BK IN		-		•	EVENTOUT
	PB5	1	TIM16_BKIN	тімз_сн2	1	I2C1_SMBA	SPI1_MOSI		USART2_CK	II.	-	ТІМ17_СН 1				ı	EVENTOUT
	PB6	1	TIM16_CH1N	1	TSC_G5_I03	I2C1_SCL	ì	-	USART1_TX	-		į	-			ı	EVENTOUT
	PB7	1	TIM17_CH1N	1	TSC_G5_IO4	I2C1_SDA	Ĥ	-	USART1_RX	ı		TIM3_CH4	-	_		ı	EVENTOUT
	PB8	ı	TIM16_CH1	ı	TSC_SYNC	I2C1_SCL	ı	ı	USART3_RX	1	CAN_RX	ı	-	TIM1_BKIN		1	EVENTOUT
	PB9	•	TIM17_CH1	ı	1	I2C1_SDA	1	IR_OUT	USART3_TX	COMP2_OUT	CAN_TX		_	_		1	EVENTOUT

57/

_
ntinued
ပ္ပ
functions
Alternate
15
Table

						ומאוני	able 13. Alternate junctions (continued)	וומוב וו		רכסוונווונ	ממ)						
		AF0	AF1	AF2	AF3	AF4	AF5	AF6	AF7	AF8	AF9	AF10	AF11	AF12	AF13	AF14	AF15
Port		SYS_AF	TIM2/TIM15/ TIM16/TIM17/ EVENT	TIM1/TIM3/ TIM15/ TIM16	TSC	I2C1/TIM1	SPI1/Infrared	TIM1/Infrared	USART1/USA RT2/USART3/ GPCOMP6	GPCOMP2/ GPCOMP4/ GPCOMP6	CAN/TIM1/ TIM15	TIM2/TIM3 /TIM17	TIM1	TIM1	OPAMP2	ı	EVENT
	PB10	1	тімг_снз	1	TSC_SYNC	ı	ı	-	USART3_TX	1	ı	1	ı	1			EVENTOUT
	PB11	=	TIM2_CH4	=	TSC_G6_IO1	=	-	-	USART3_RX	-	•	-	=	-		ı	EVENTOUT
	PB12	-	-	-	TSC_G6_102	Ė	-	TIM1_BKIN	USART3_CK	-	-	-	-	-		1	EVENTOUT
Port B	PB13	-	1	-	TSC_66_103	-	-	TIM1_CH1N	USART3_CTS	-	-	-	ì	-		ı	EVENTOUT
	PB14	ı	TIM15_CH1	ı	TSC_G6_104	I	-	TIM1_CH2N	USART3_RTS _DE	ı	1	1	1	ı		ı	EVENTOUT
	PB15	ı	TIM15_CH2	TIM15_CH1	ı	TIM1_CH3N	1	ı	1	1	1	1	1	ı		ı	EVENTOUT
	PC0	1	EVENTOUT	TIM1_CH1	1	-	1	-	ı	-	1	1	i	1	1	ı	ı
		-	1	-	-	ì	-	=	-	ı	-	=	Ē	_	1	1	1
	PC2	-	EVENTOUT	тім1_снз	-	Ė	-	-	-	-	-	-	-	-	1	1	-
	PC3	-	EVENTOUT	TIM1_CH4	-	ì	-	TIM1_BKIN2	-	ı	=	=	Ē	_	1	1	1
	PC4	-	EVENTOUT	TIM1_ETR	-	ì	-	=	USART1_TX	ı	-	=	Ē	_	1	1	1
	PC5	•	EVENTOUT	TIM15_BKIN	TSC_G3_I01	Ė	ı	-	USART1_RX	•	ı	•	Ĥ	į	ı	1	1
	PC6	ı	EVENTOUT	TIM3_CH1		ı	1	1	COMP6_OUT	ı	ı	ı	i	ı	ı	ı	
t	PC7	ı	EVENTOUT	тімз_сн2		ì	1	-	1	1	1	1	Î	•	ı		ı
Š	PC8	1	EVENTOUT	тімз_снз		ì	1	-	-	1	-	-	ì	1	1		1
	PC9	1	EVENTOUT	TIM3_CH4		ı	1	1	1	ı	1	1	i	1	1		
	PC10	1	EVENTOUT	ì	1	i	1	-	USART3_TX	-	ı	1	İ	-	ı	1	ľ
	PC11	•	EVENTOUT	•		Ė	ı	-	USART3_RX	-	•	•	Ĥ	į	ı	1	1
	PC12	ı	EVENTOUT	i		ì	1	-	USART3_CK	-	1	1	Î	•	ı		ı
	PC13		ı	1	ı	TIM1_CH1N	<u>.</u>	1	1	ı	1	1	i	1	1		
	PC14	1	ı	1		1	1	1	1	1	1	1	i		ı		
	PC15	1	ı	ì	1	i	1	-	1	-	1	1	İ	-	ı	1	ľ
Port D	PD2	ı	EVENTOUT	TIM3_ETR		ì	ı	-	1	1	1	1	Î	•	ı		ı
ц to	PF0	1	ı	1		1	1	TIM1_CH3N	1	1	1	1	i		ı		
5	PF1	1	1	1	1	1	ı		1		ı		1	1	1	ı	1

