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I. Shift Registers;
is a type of digi circuit using Cascade of flip flop where the output of one flip flop is connected to input of

Types of Shift Registor:

b-sevial in / Sevial out

the next.

- 2. Sevial in/ Pavallel out
- 3. Povallet in/ soviel out
- o. universal povollel in/
- -> data Storage
- 2. Timing dia avoms are
 graphical Representation
 that Show timing and
 Sequencing of disital
 Signals in a Shift
 Register