POSSESSION OF MOBILE IN EXAMINATION IS UFM PRACTICE

Name of Student			Enrolment No	Enrolment No	
D	epartment -	DENNETT HNIVI	ERSITY, GREATER NOIDA		
			Examination, July 2019		
C	OURSE CO	DDE: ECSE104L	MAX. DURATION:	TWO HOUR	
		AME: DIGITAL DESIGN			
COURSE CREDIT: <u>5</u>			MAX. MARKS: <u>5</u>	MAX. MARKS: <u>50</u>	
N	ote:		40		
• All the questions are compulsory.					
•	Please w	rite precisely and neatly. Pleas	e make clear diagram wherever requir	ed.	
	a. M	n a circuit which can convert BC Iake truth table erive equations using K-map.	D to excess-3.	(10 Marks)	
		raw the circuit diagram			
			7.0		
	2. Desig	n a 8-to-1 mux using 2-to-1 mux.		(5 Marks)	
	3. Desig	n the asynchronous counter using	T flip flop which count numbers as follow	vs: (10 Marks)	
	_	n a combined register which h I serial out (PISO) together.	ave capability of serial in serial out(SI	SO) and parallel (5 marks)	
	a) F	y the following Boolean expre =AB'C+A'BC'+AB'CD'+A'I =C'D+A'BC'D+A'BC'D+AE		(10 marks)	
	6. Multi	ple choice questions.		(5*2=10 marks)	
	i.	What is the result when a decina. 1388 b. 12166 c. 327.375 d. 1476	nal 5238 is converted to base 16?		
	il,	Convert the binary number 100 a. 12.5 b. 125 c. 90.125 d. 9.125	1.0010 to decimal. (1 Mark)		

iii. Consider the following Boolean expression for F:

$$F(P, Q, R, S) = PQ + P'QR + P'QR'S$$

The minimal sum-of-products form of F is

a.
$$PQ + QR + QS$$

b.
$$P+Q+R+S$$

c.
$$P' + Q' + R' + S'$$

d.
$$P'R + P'R'S + P$$

iv. In an SR latch made by cross-coupling two NAND gates, if both S and R inputs are set to 1, then it will result in

a.
$$Q = 0$$
, $Q' = 1$

b.
$$Q = 1, Q' = 0$$

v. Equation of carry in full adder is (a, b are inputs, and c is carry input).

a.
$$carry = ab + bc + ca$$

b.
$$carry = ab + (a \oplus b) c$$