

A
 U_J1
 J1.schdoc
 U_J2
 J2.schdoc
 U_J3
 J3.schdoc
 U_J4
 J4.schdoc



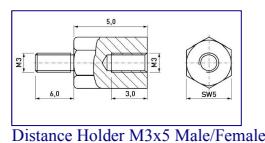
Washer M3



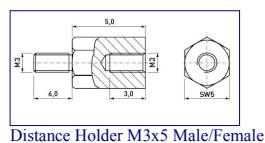
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Nut M3



Distance Holder M3x5 Male/Female



Distance Holder M3x5 Male/Female



Washer M3



Serrated Washer M3



Nut M3



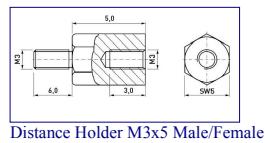
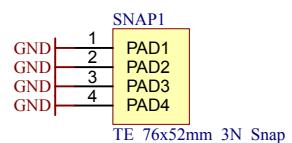
Washer M3



Serrated Washer M3



Nut M3



Distance Holder M3x5 Male/Female



Washer M3



Serrated Washer M3



Nut M3



Title: TEBF0808 - B2B-connectors

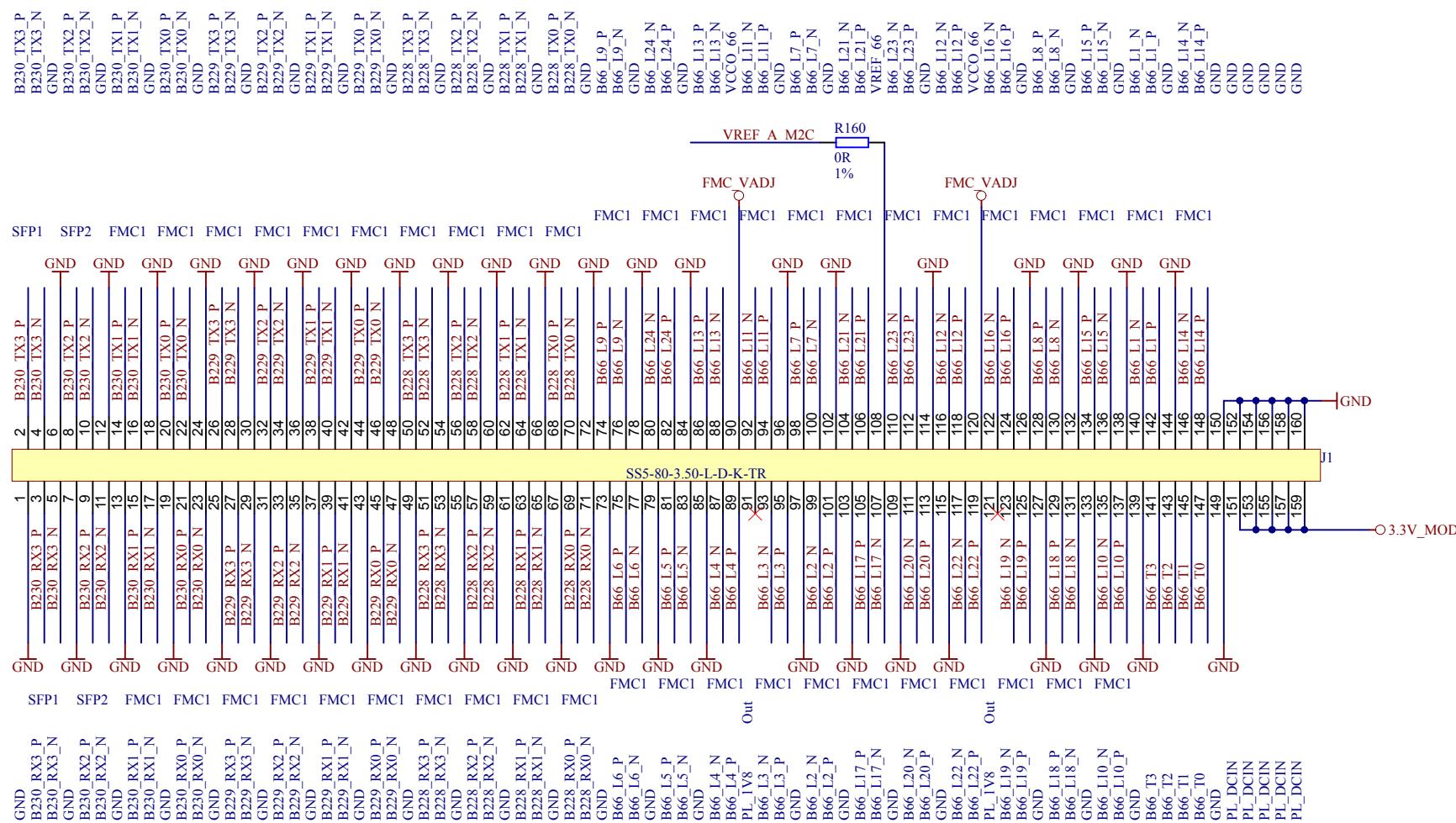
A4 | Number: TEBF0808
A

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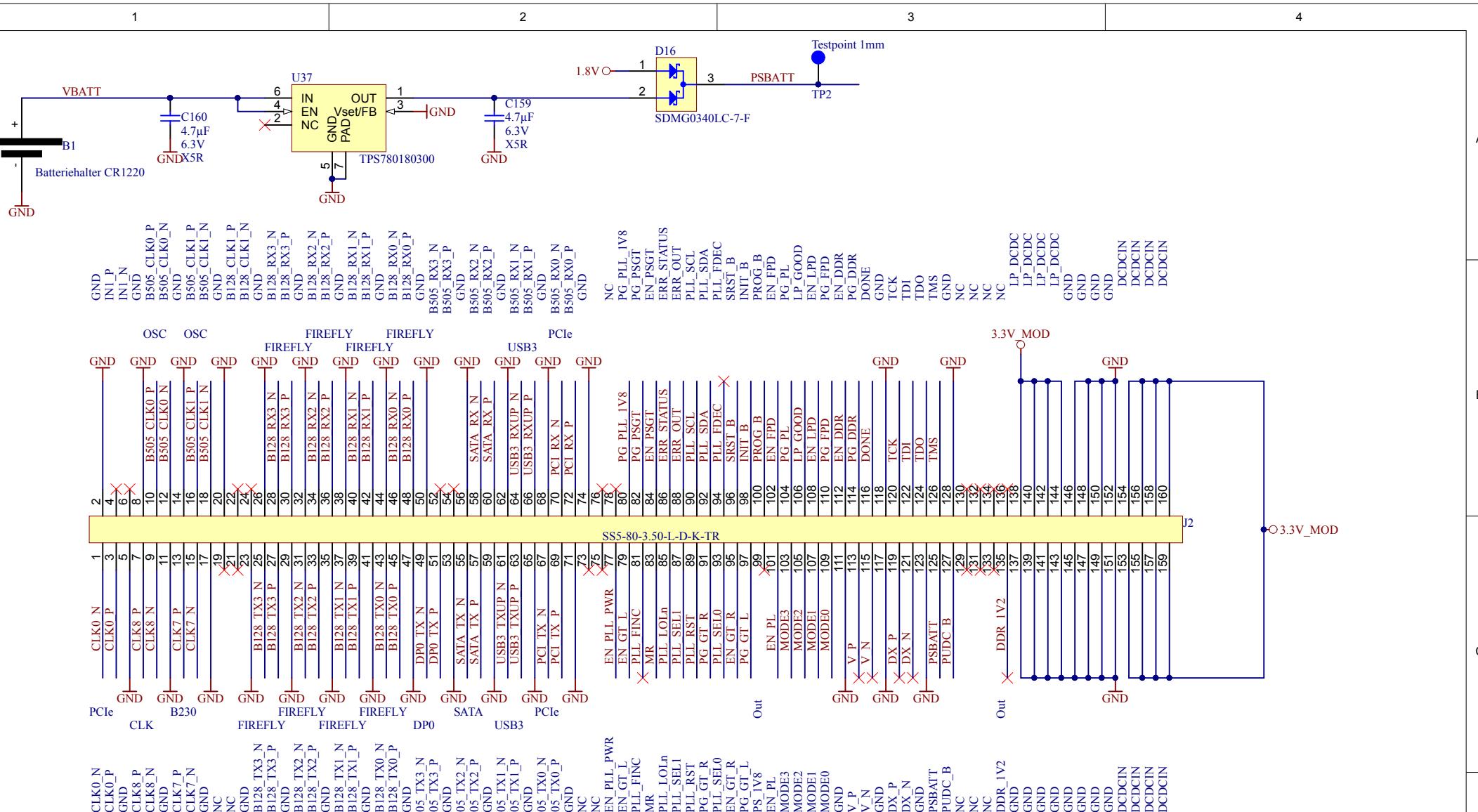
Date: 2016-09-29 Copyright: Trenz Electronic GmbH

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Filename: B2B.schdoc

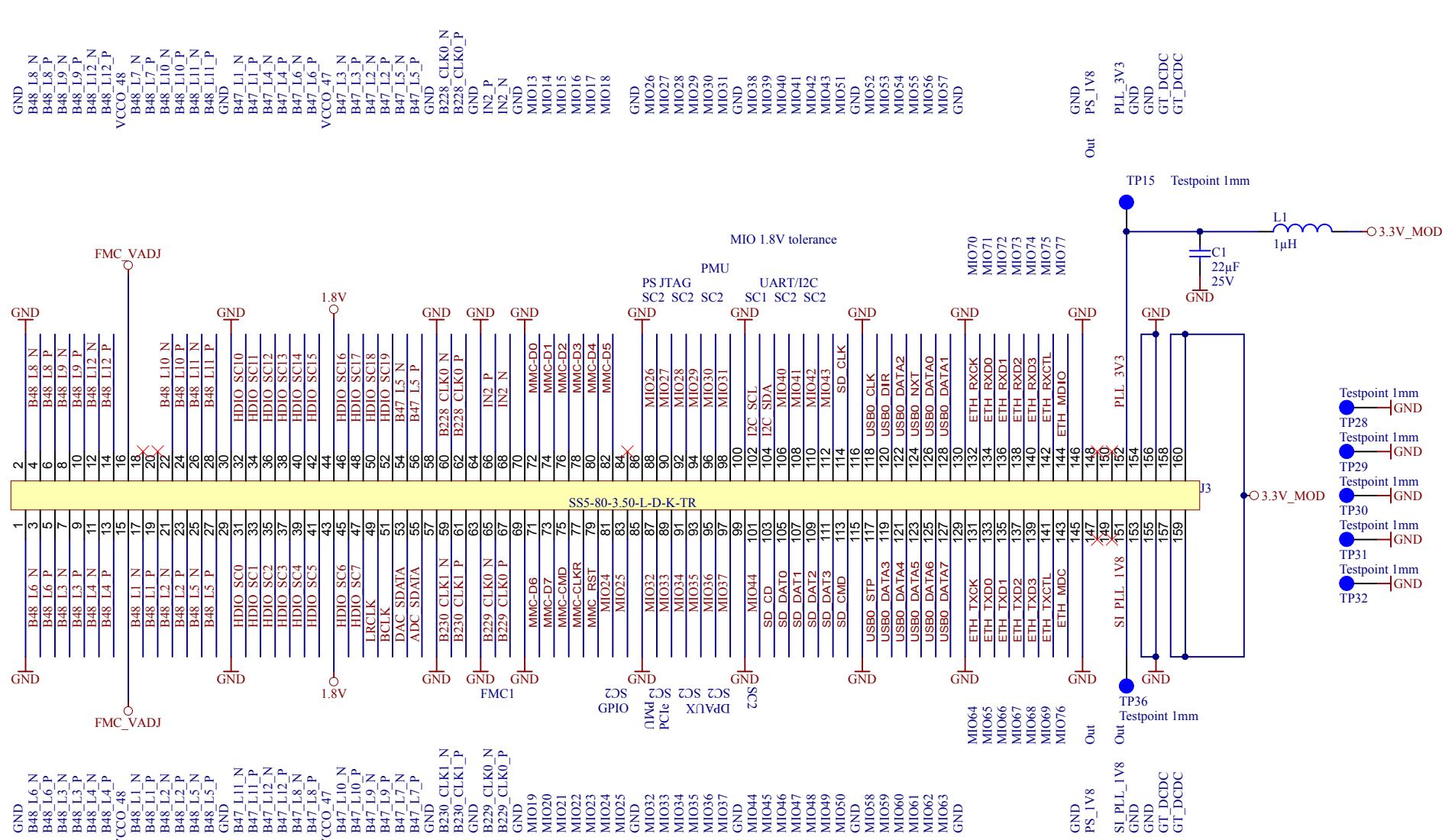


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Copyright:	Trenz Electronic
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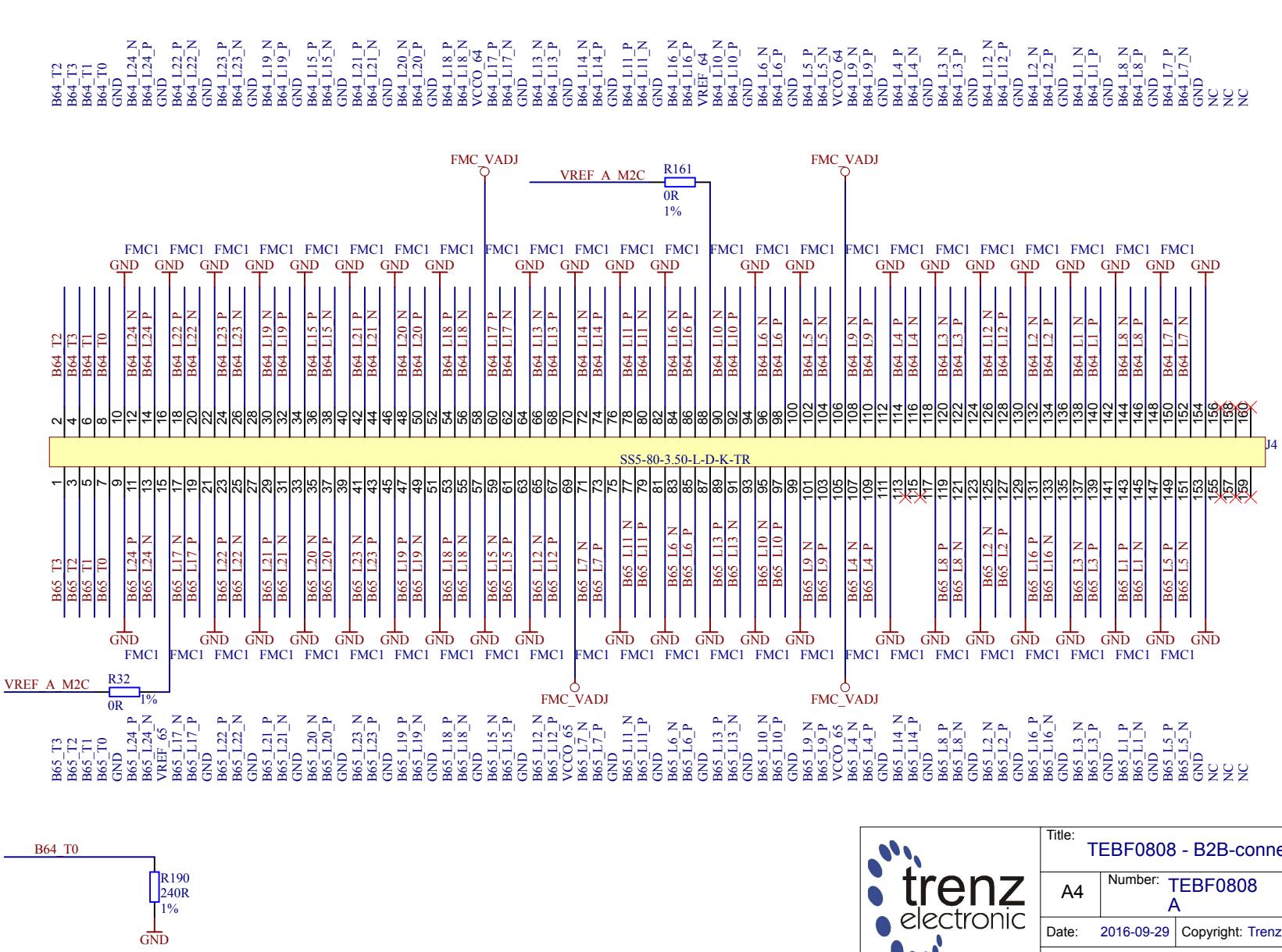


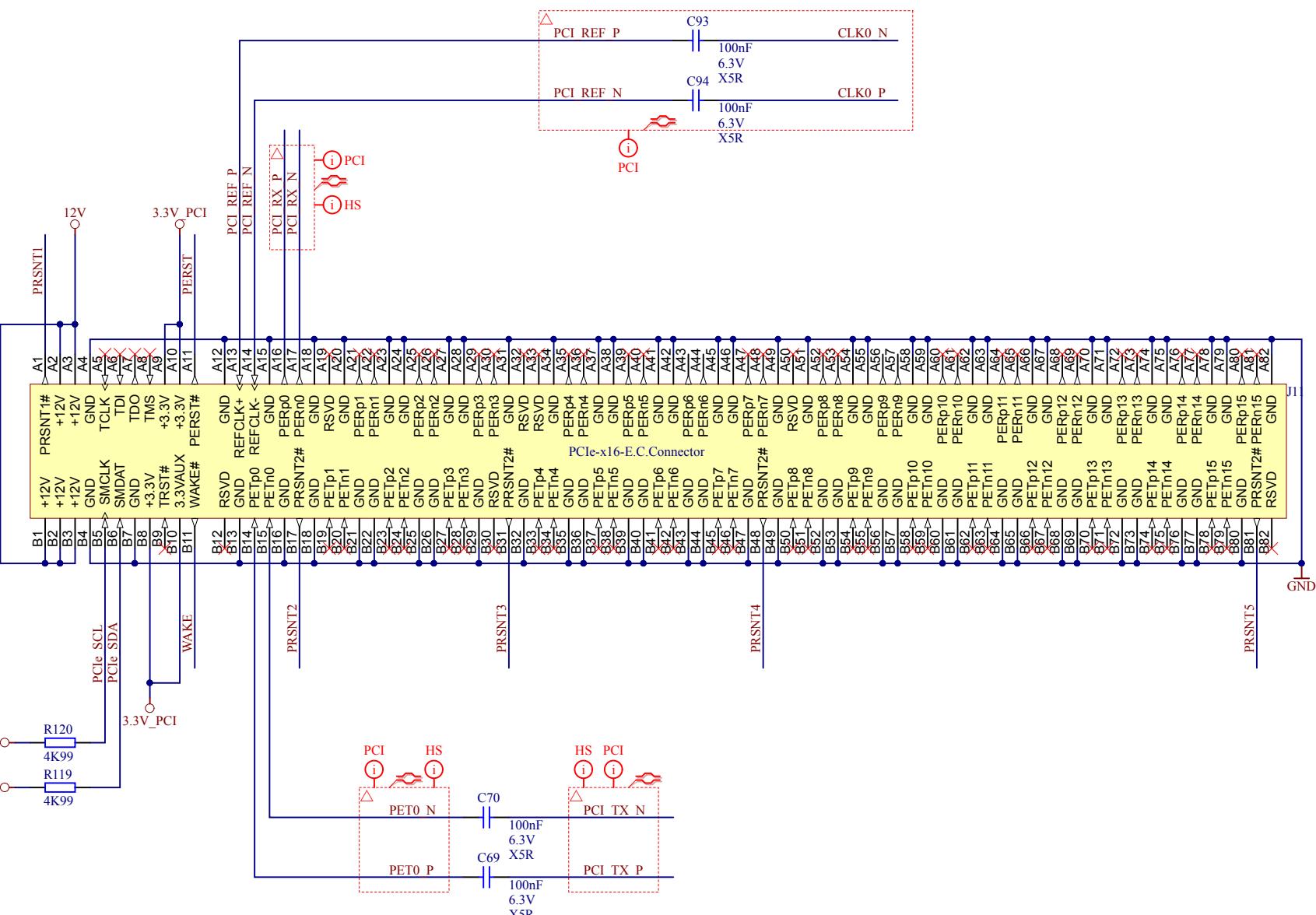
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Date: 2016-09-29	Copyright: Trenz Electronic GmbH
Filenumber:	J2.schdoc



Title: TEBF0808 - B2B-connector J3	
A4	Number: TEBF0808 A
Date: 2016-09-29	Copyright: Trenz Electronic
Filename:	J3.schdoc





Title: TEBF0808 - PCIe

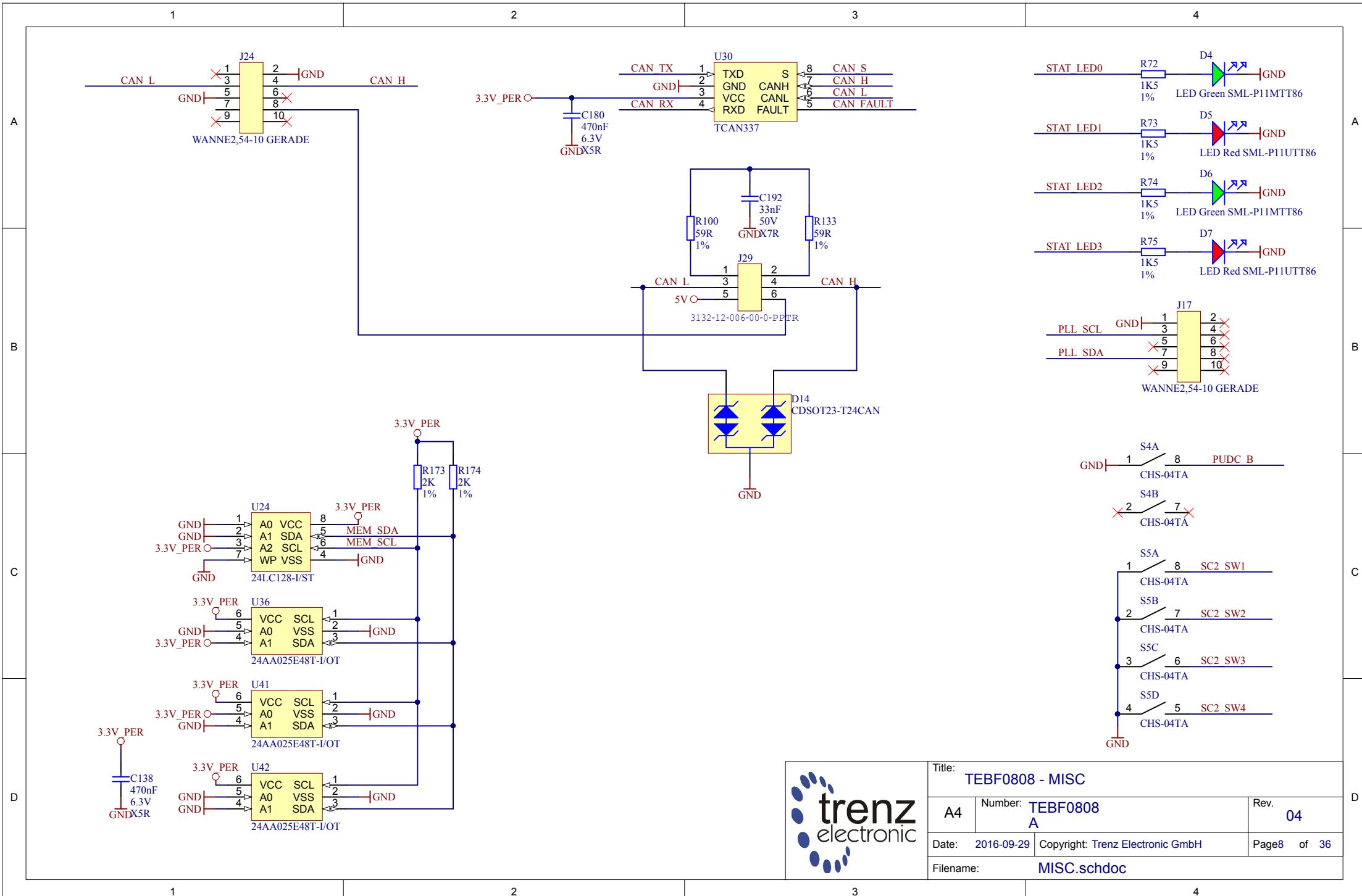
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04

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Filename: PCIe.schdoc



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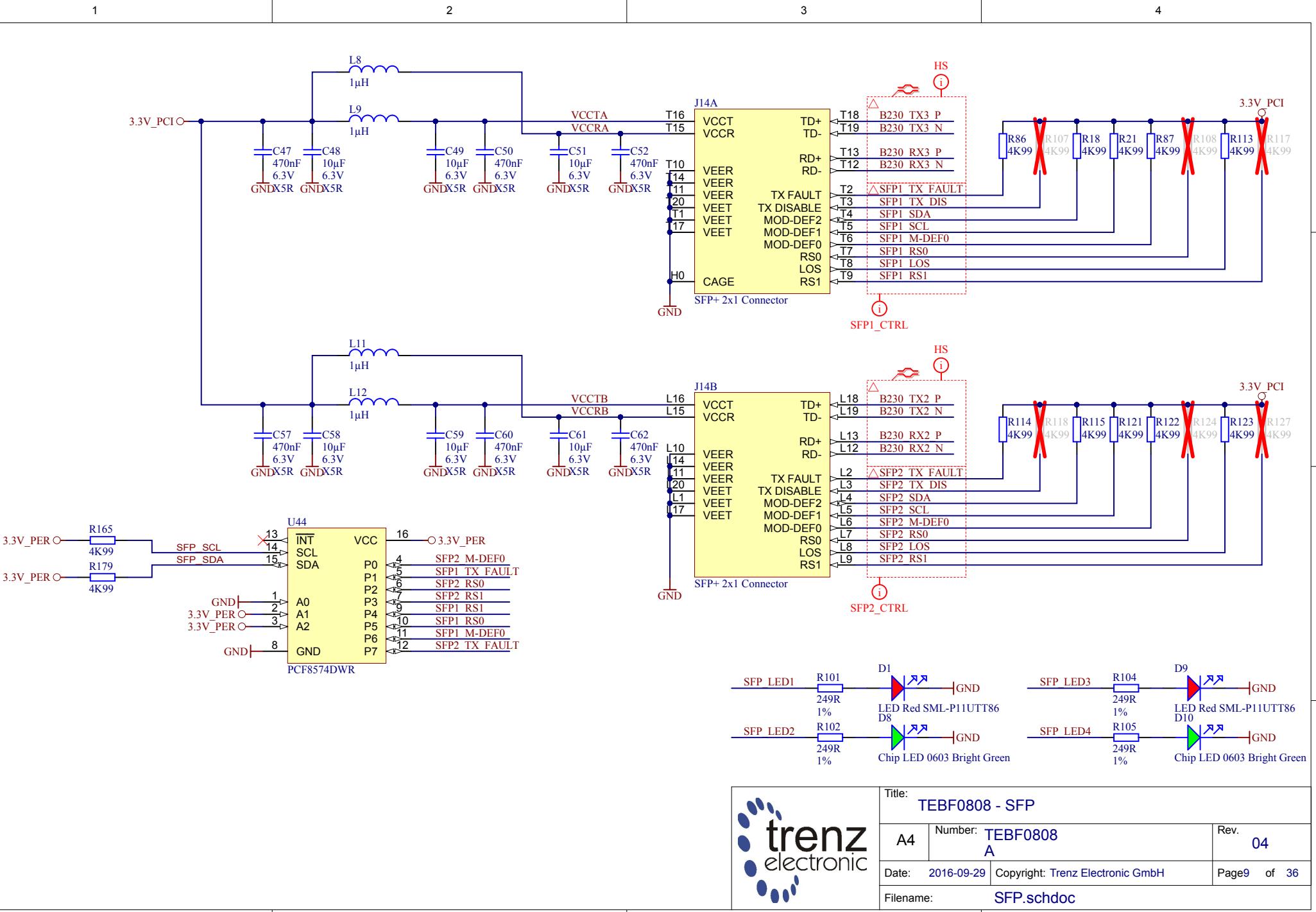
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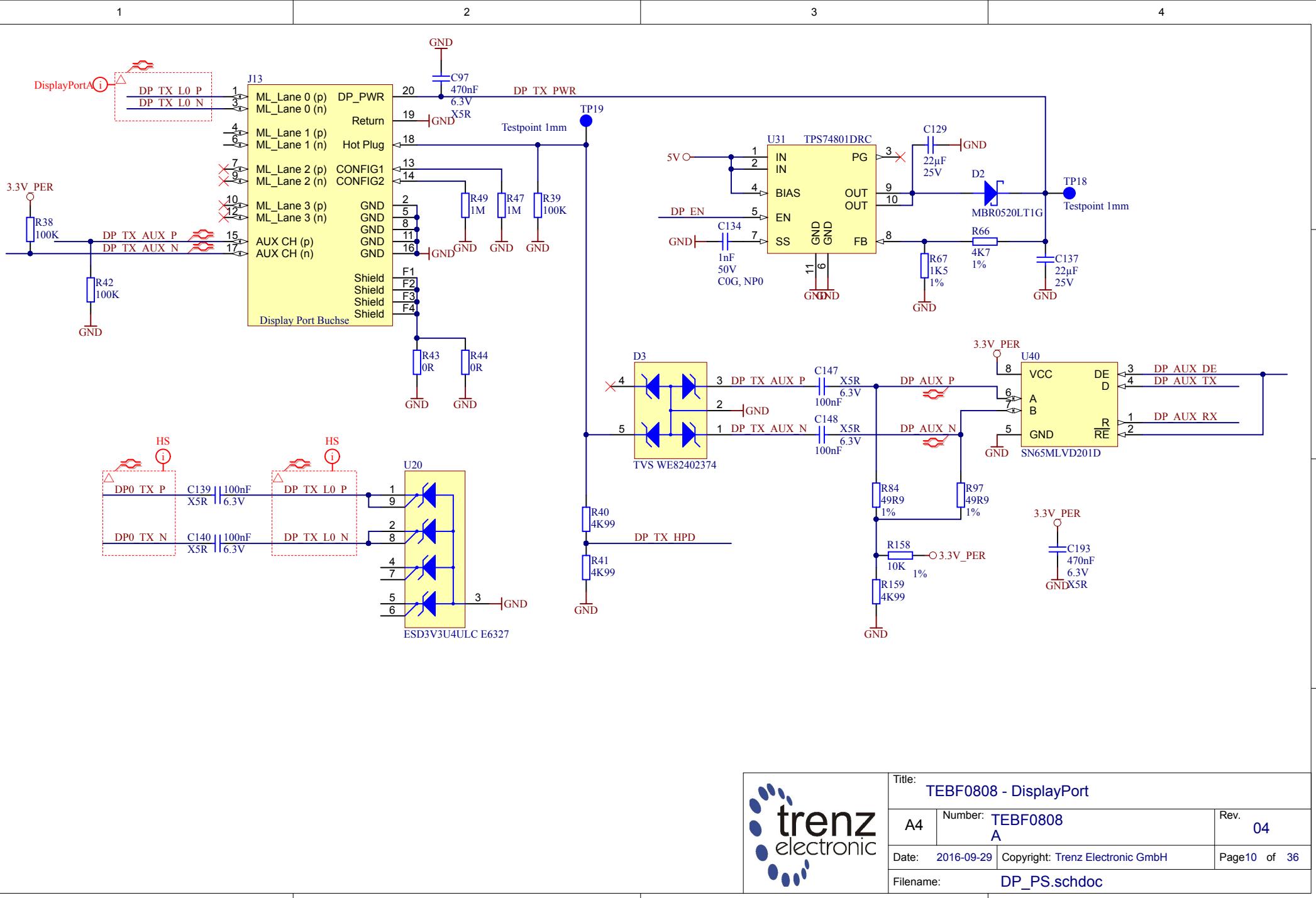
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Date: 2016-09-29 Copyright: Trenz Electronic GmbH

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Filename: MISC.schdoc





Title: TEBF0808 - DisplayPort	
A4	Number: TEBF0808 A
Date: 2016-09-29	Copyright: Trenz Electronic GmbH
Filename: DP_PS.schdoc	Rev. 04

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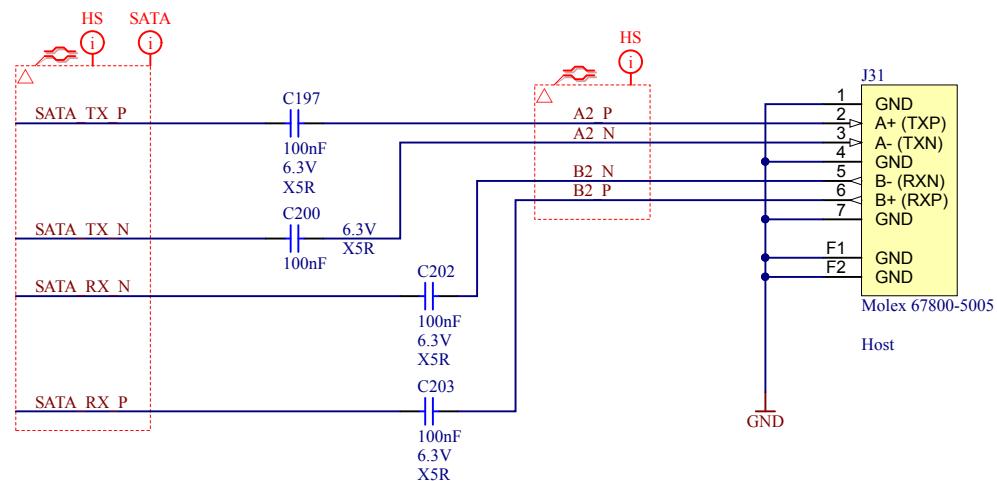
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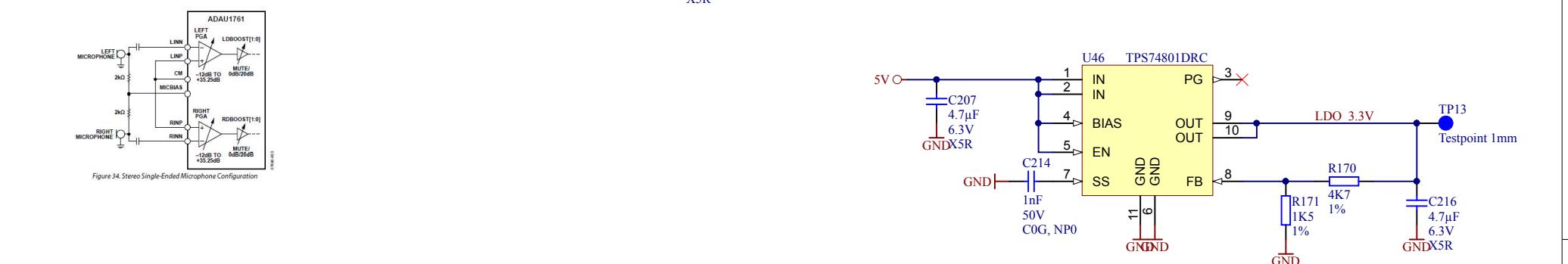
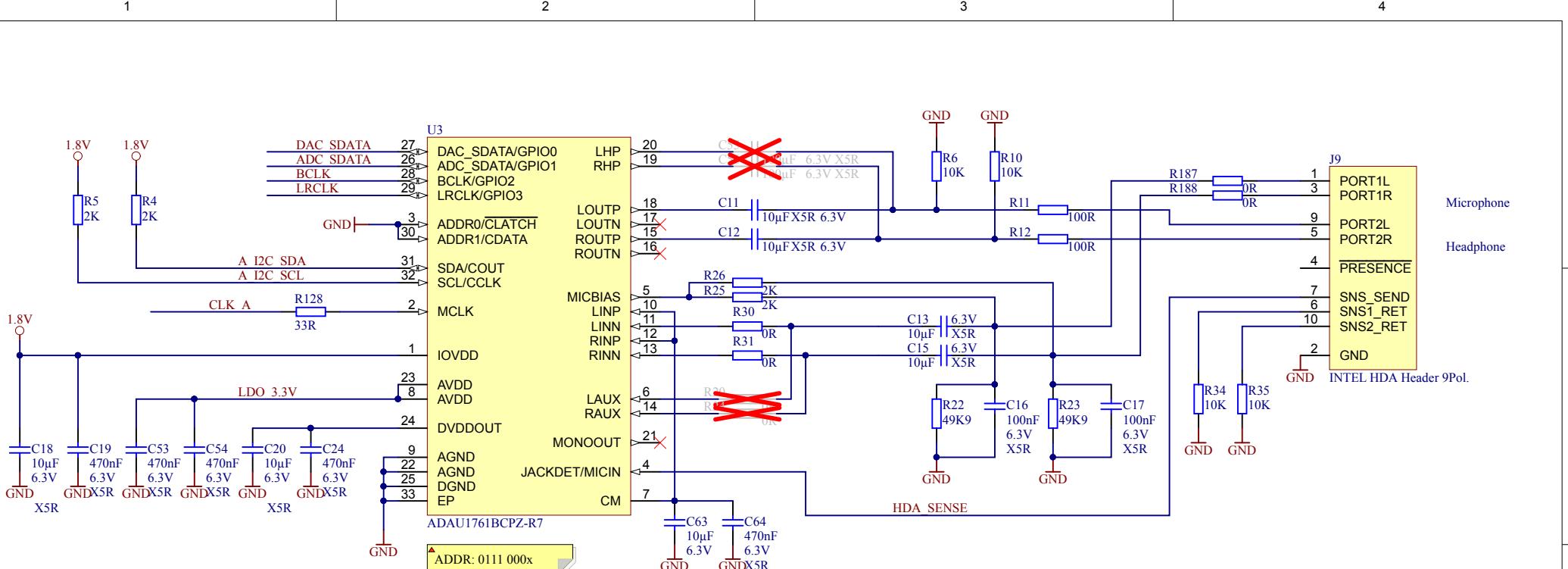
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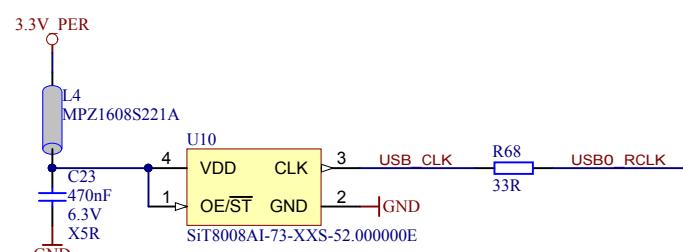
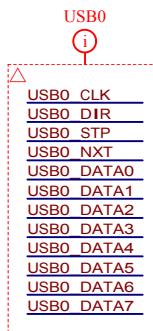
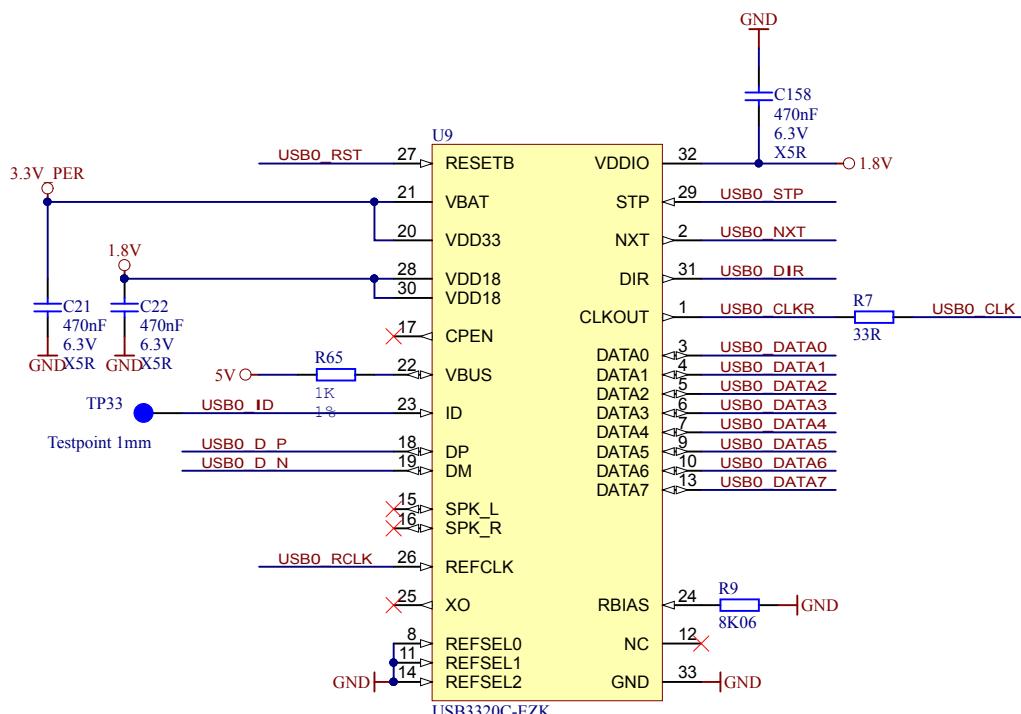


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Title: TEBF0808 - Audio		Rev. 04
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1 2 3 4



Title: TEBF0808 - USB		
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Filename: USB-PHY.SchDoc		

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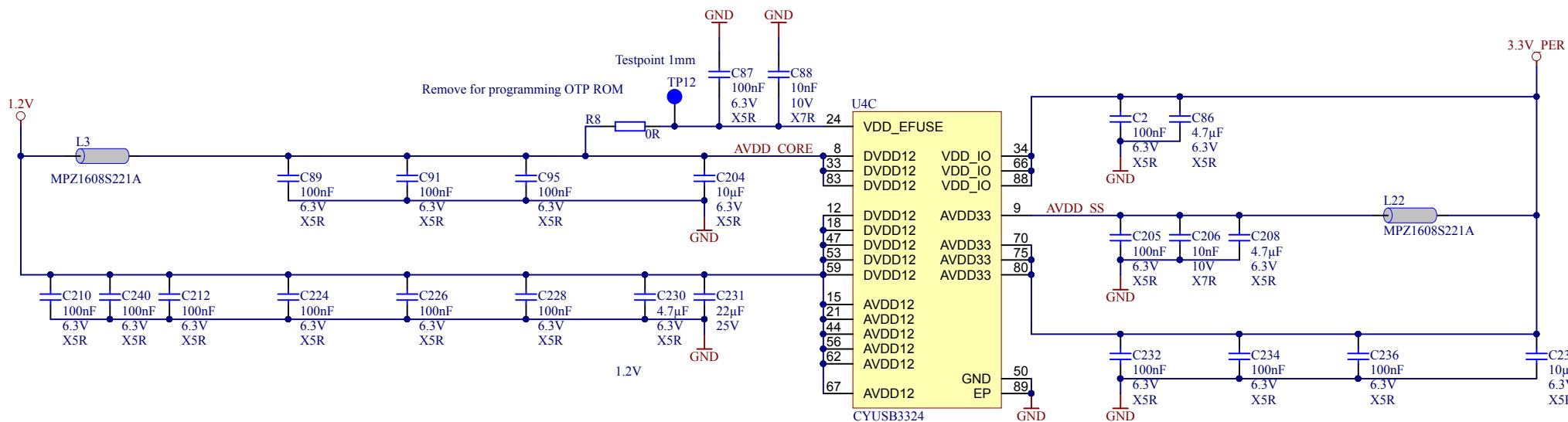
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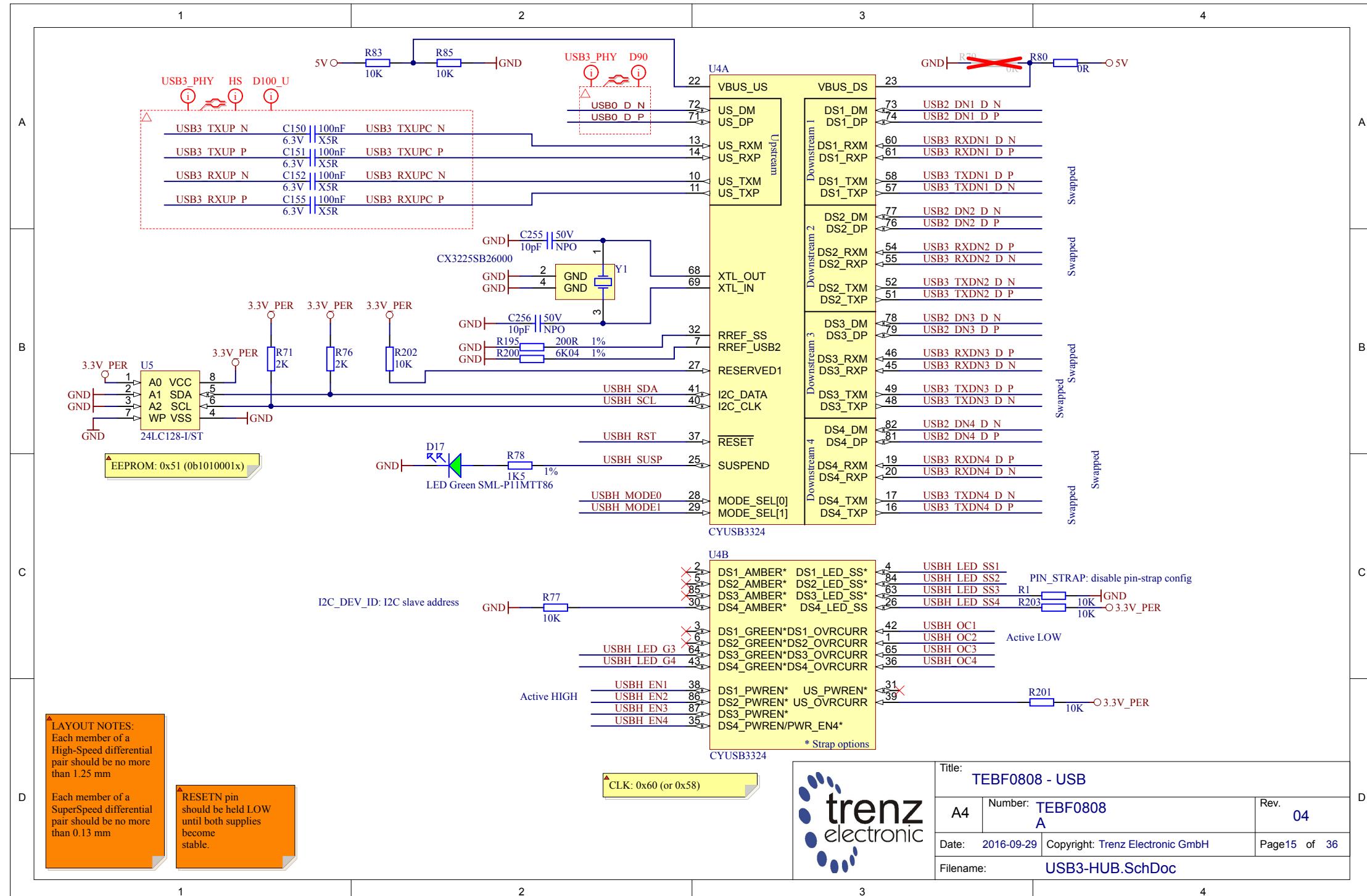
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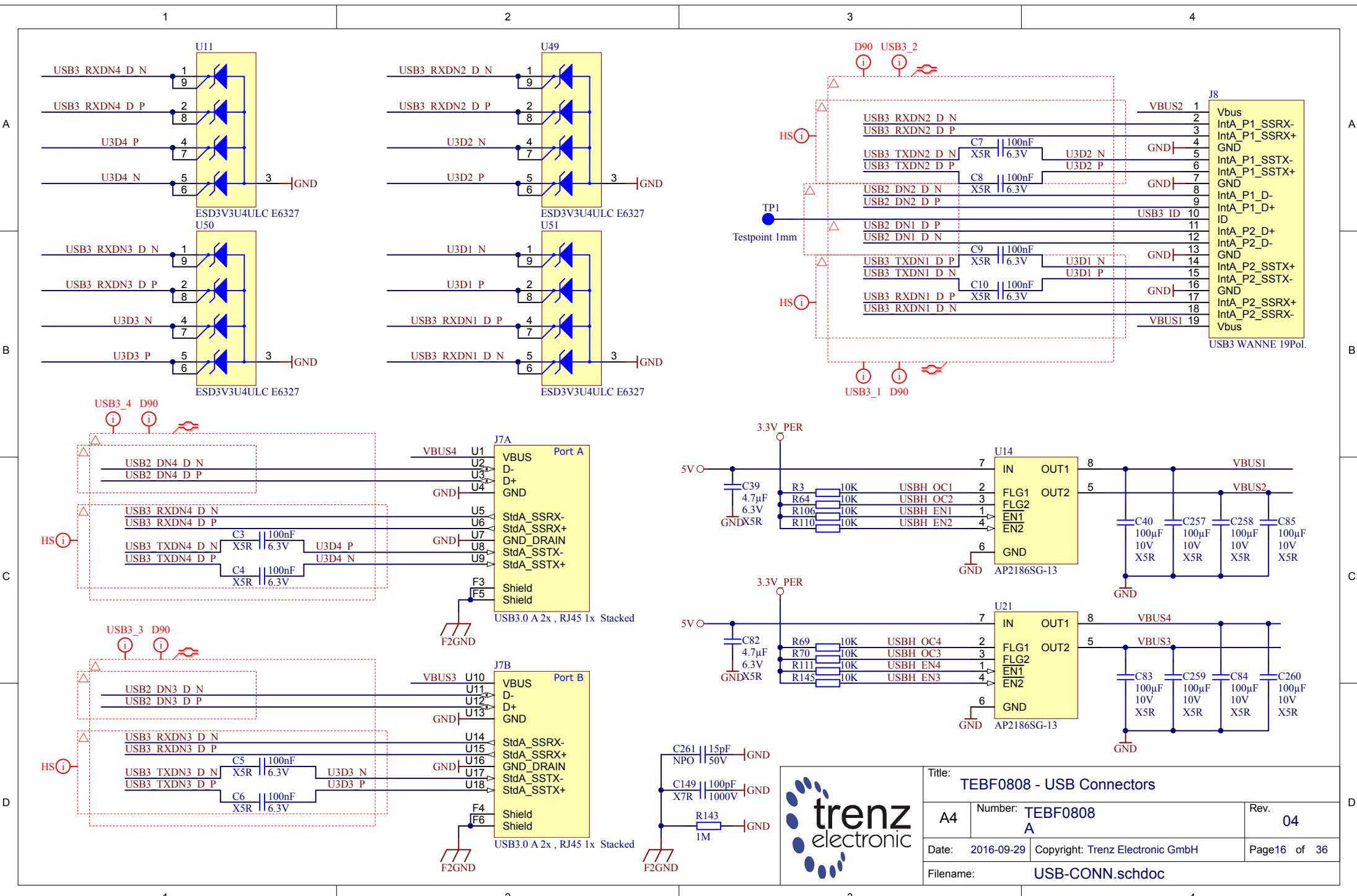
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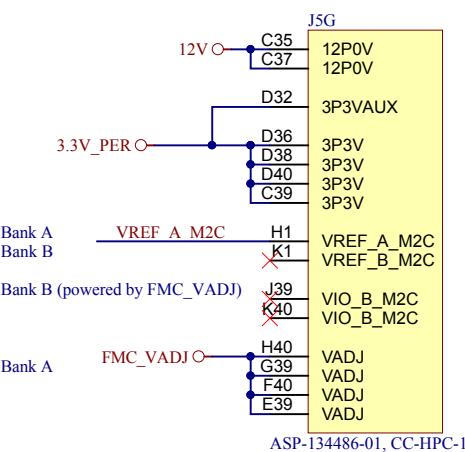


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Filename: USB.schdoc		

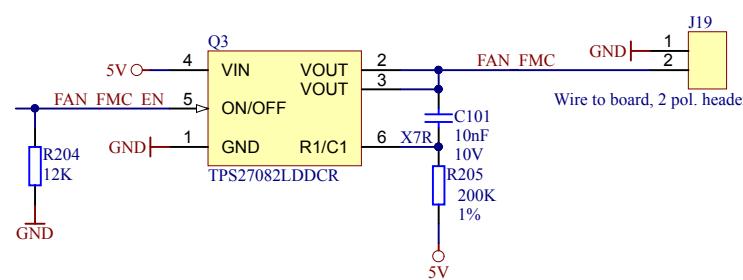




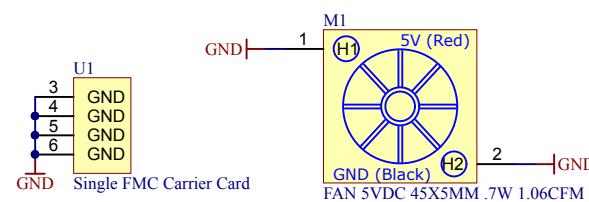
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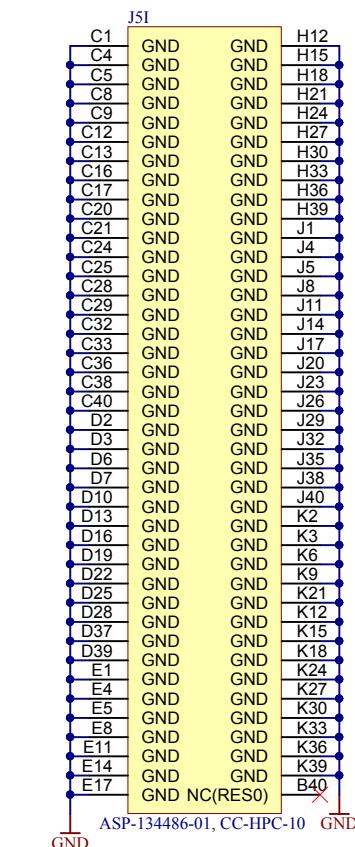
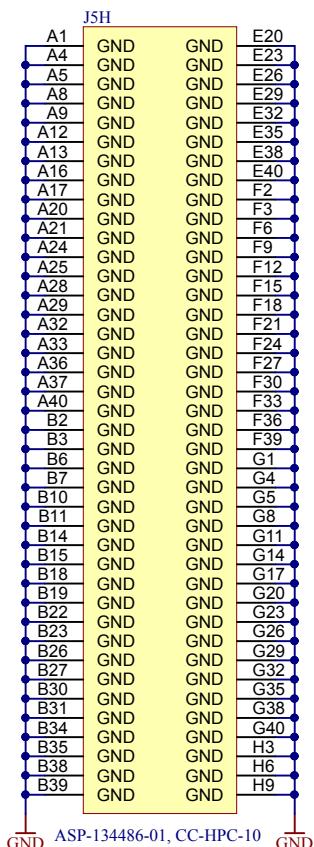
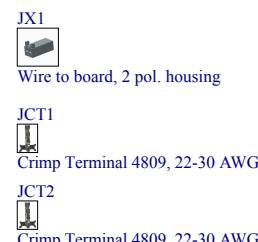
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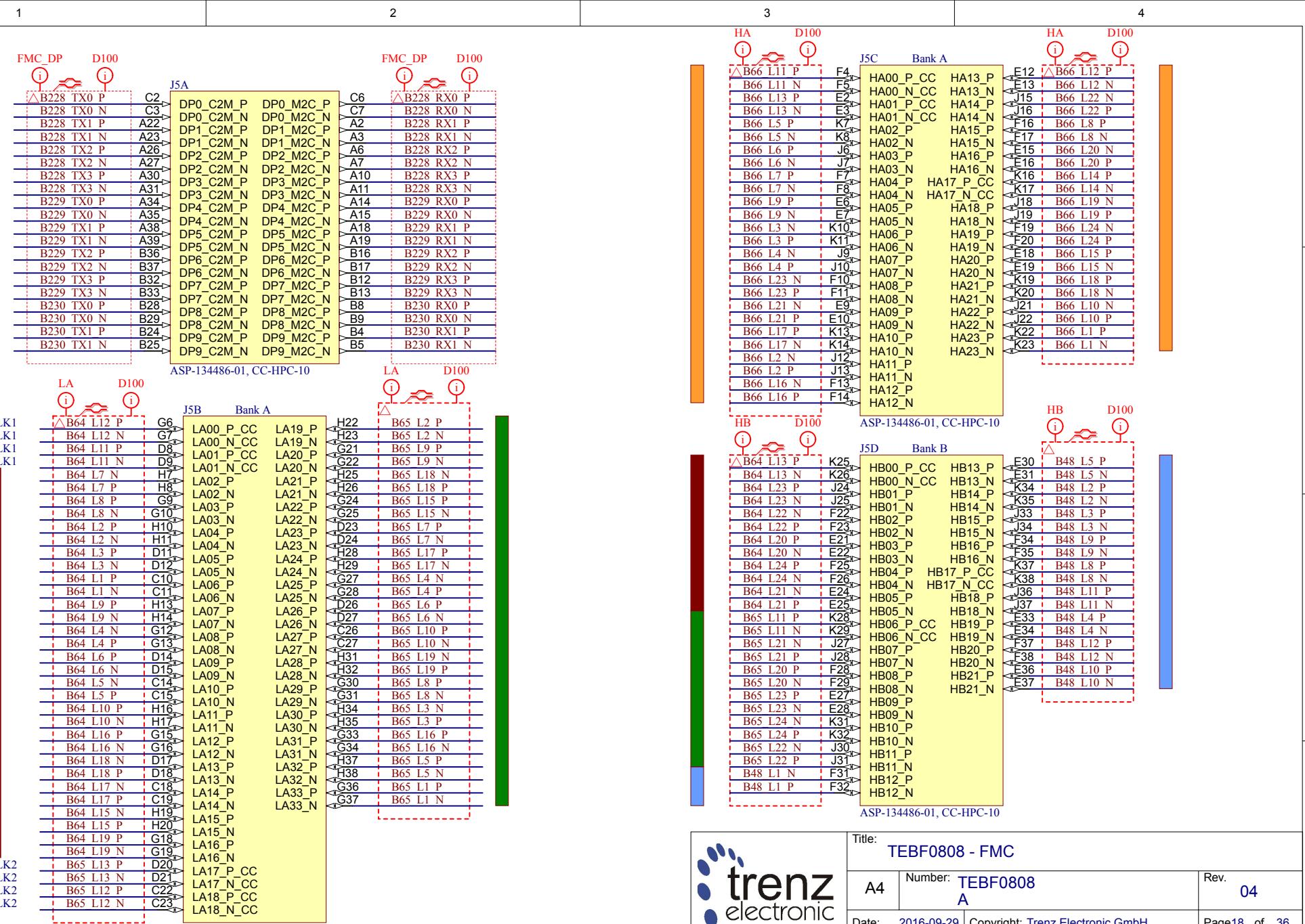
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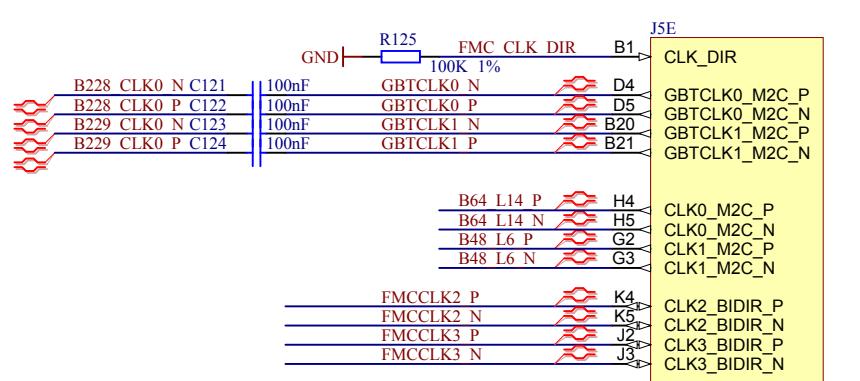
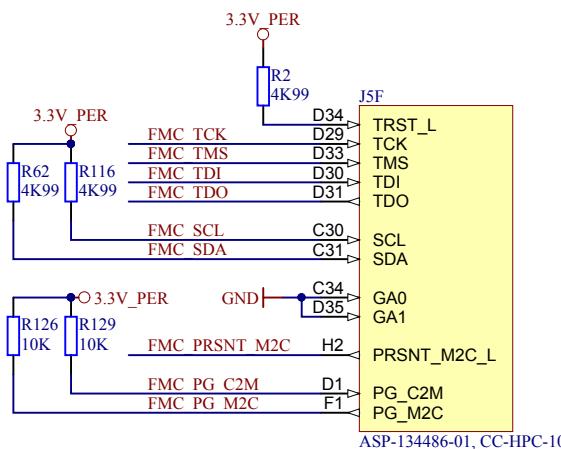
Rev. 04

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Filename: FMC PWR.SchDoc





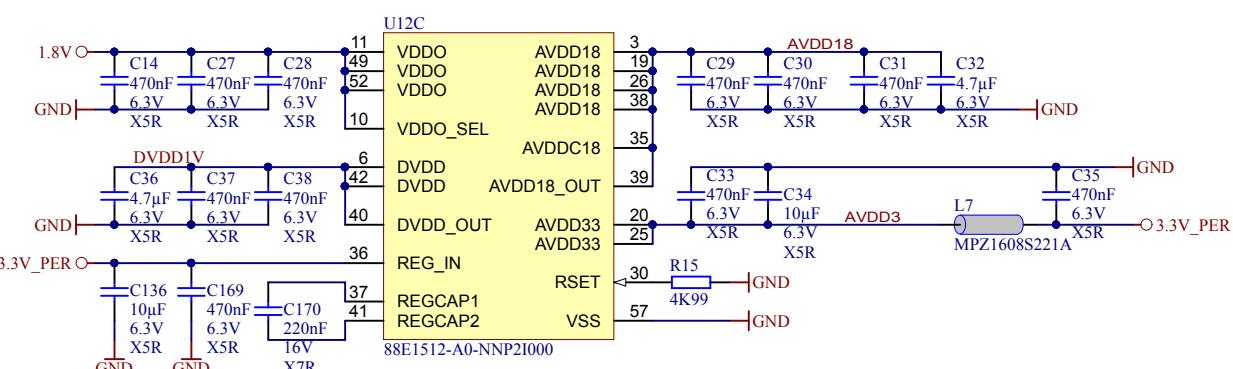
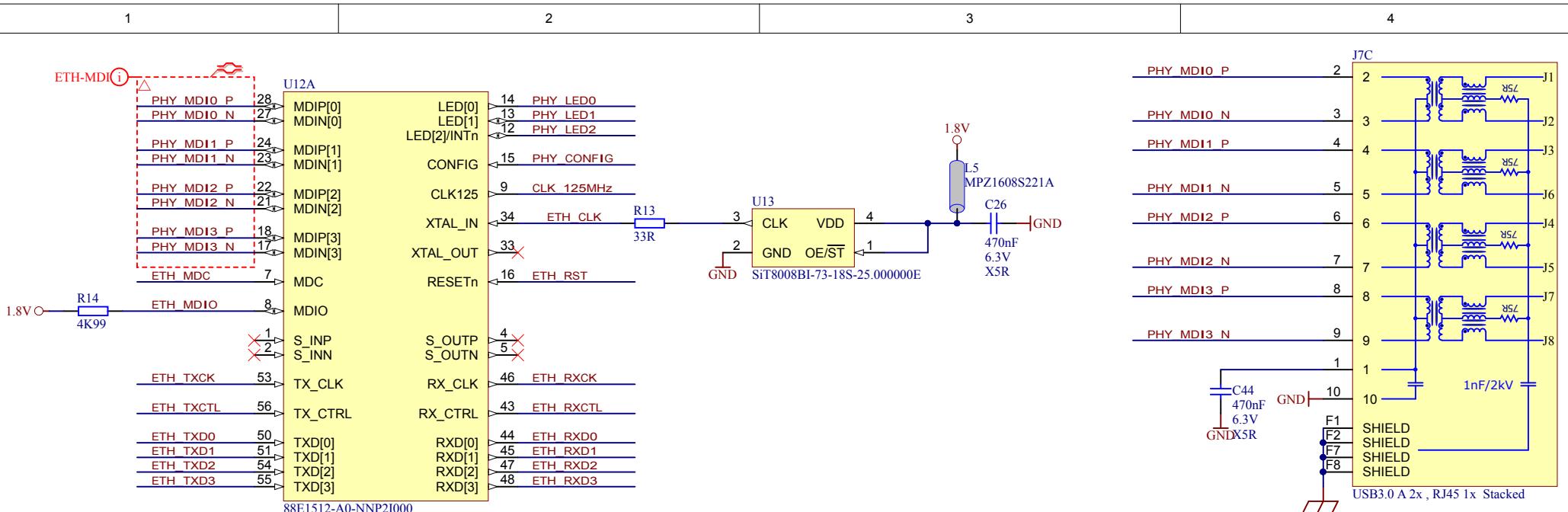
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A4 Number: TEBF0808
A Rev. 04

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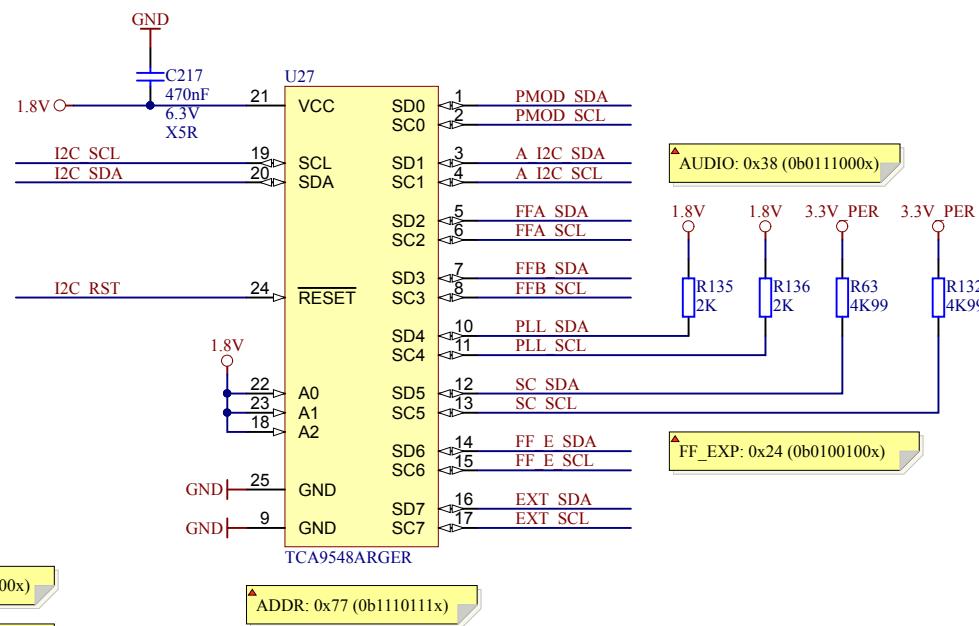
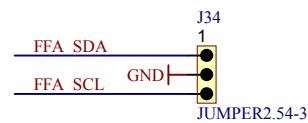
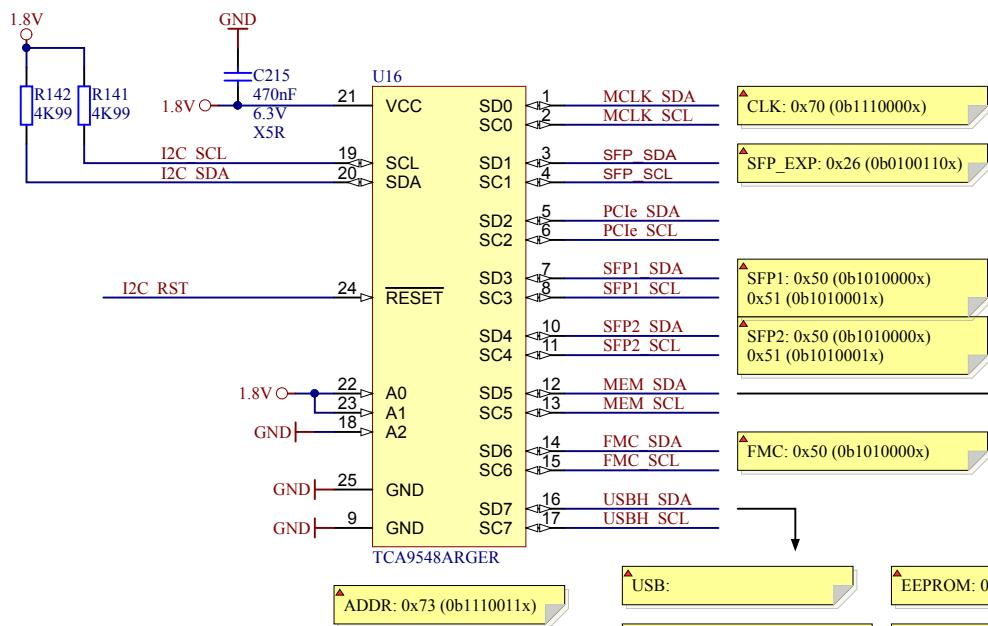
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A4	Number: TEBF0808 A	Rev. 04
Date: 2016-09-29	Copyright: Trenz Electronic GmbH	Page 20 of 36
Filename: ETH-PHY.SchDoc		

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Title: TEBF0808 - I2C System

A4 | Number: TEBF0808
A

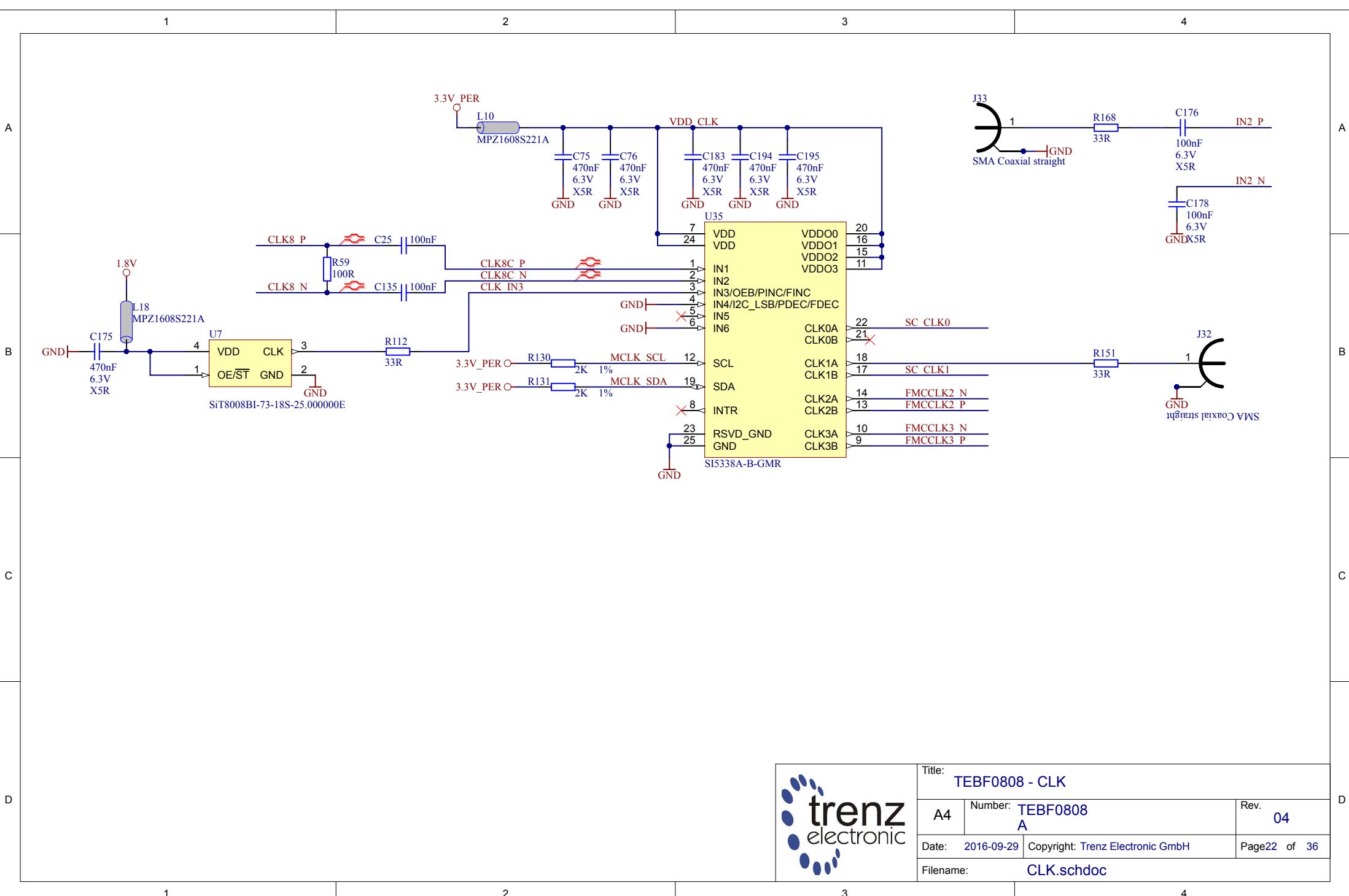
Rev. 04

Date: 2016-09-29 | Copyright: Trenz Electronic GmbH

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Filename: I2C_system.schdoc

1 2 3 4



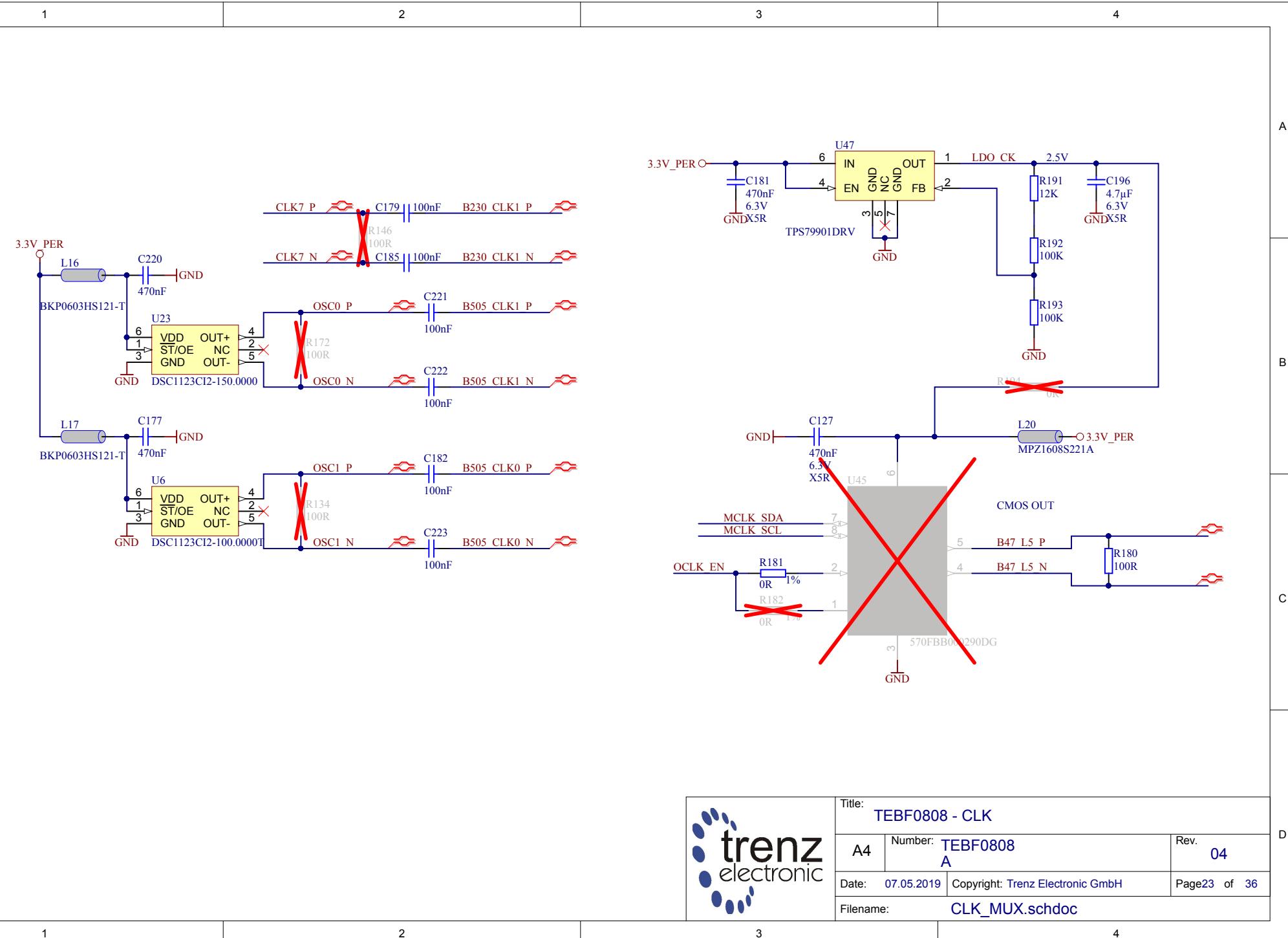
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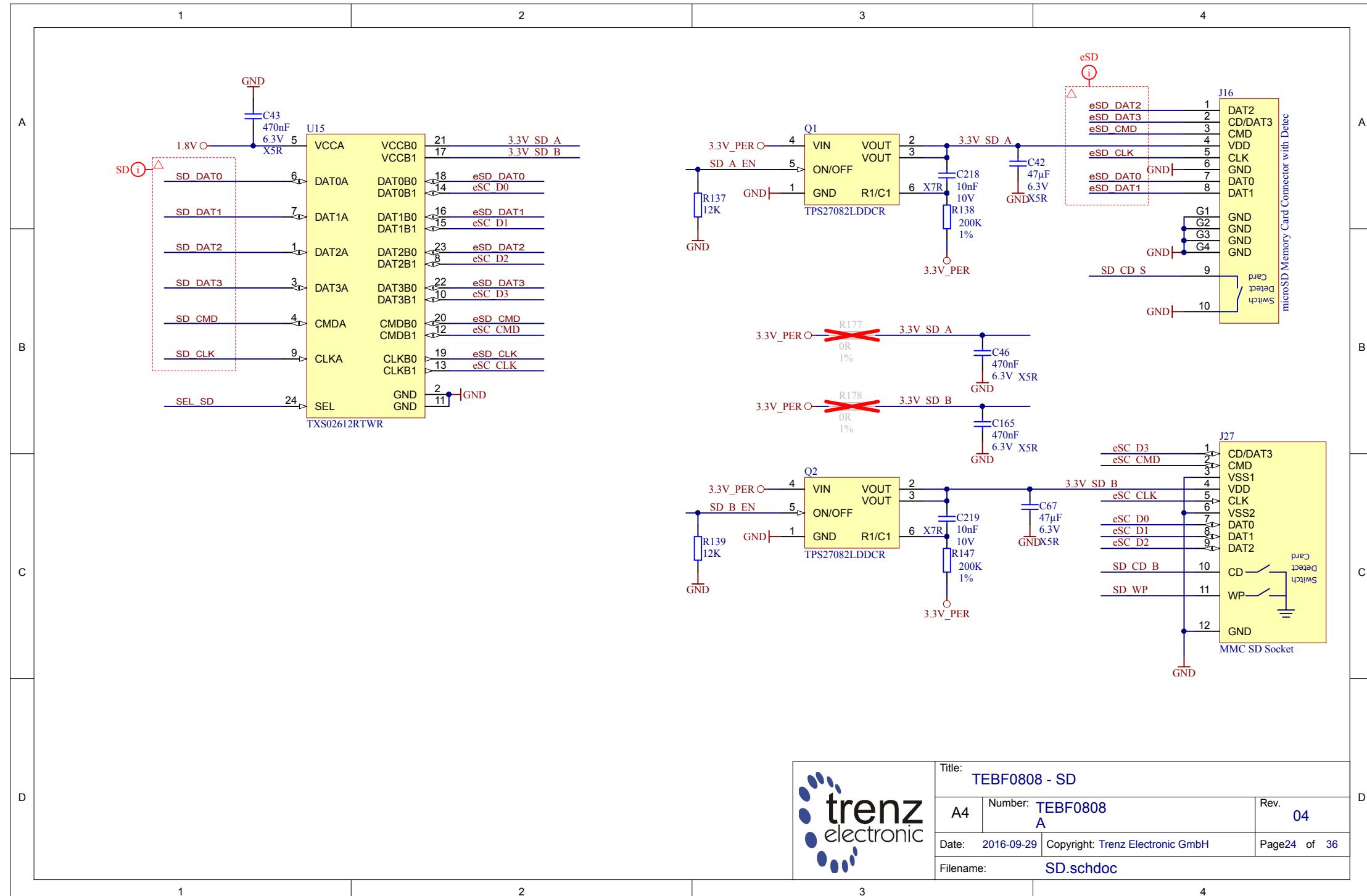
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A Rev. 04

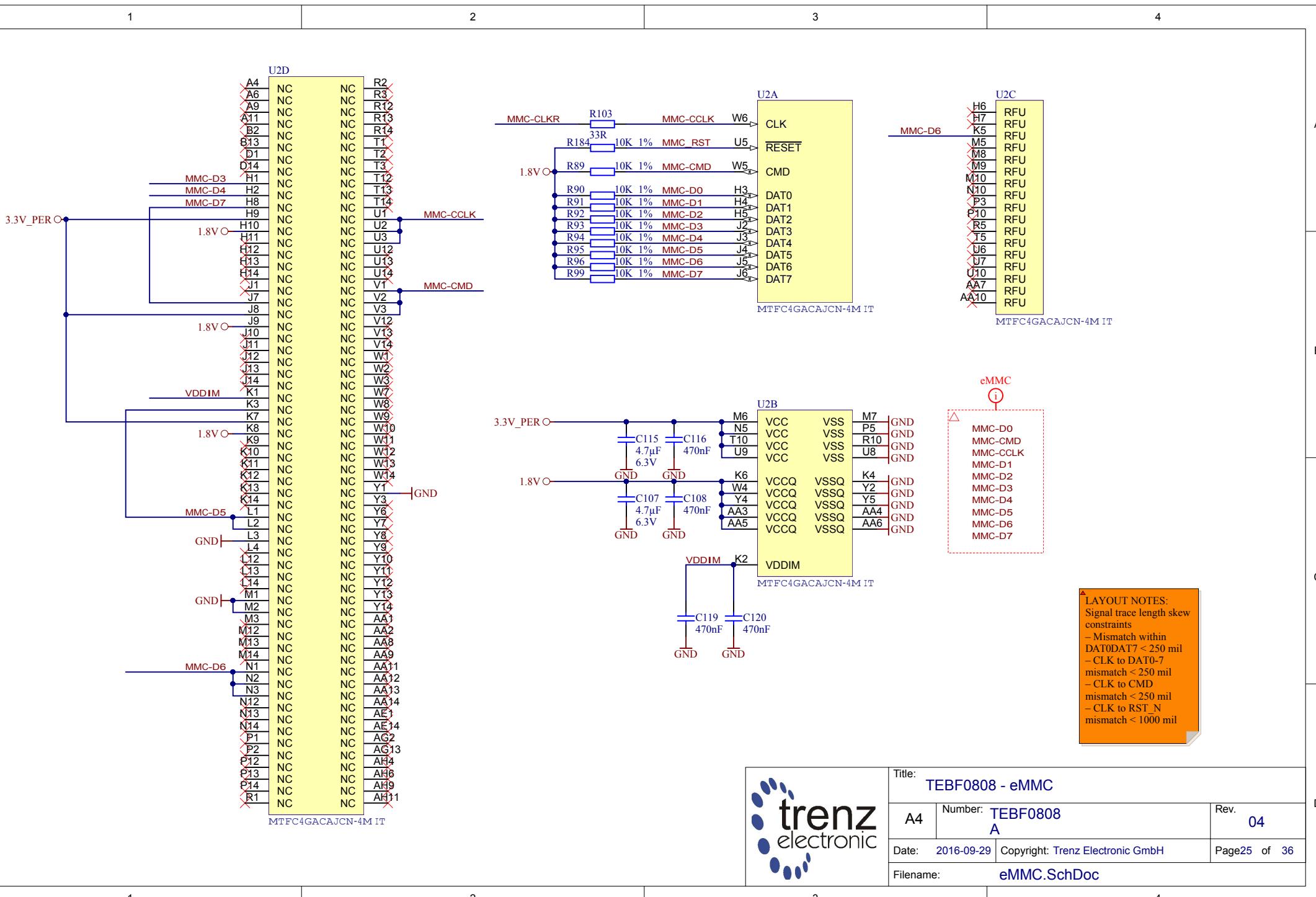
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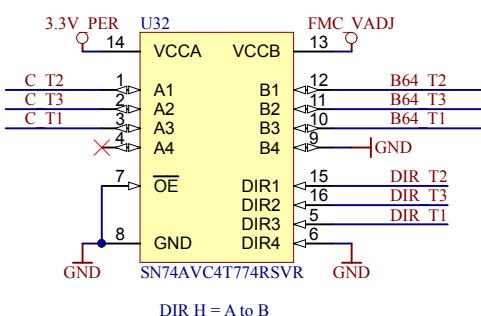
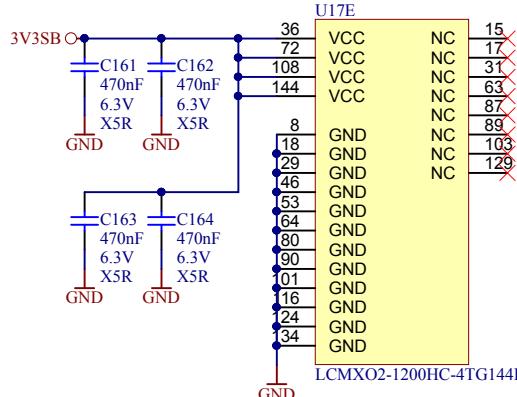
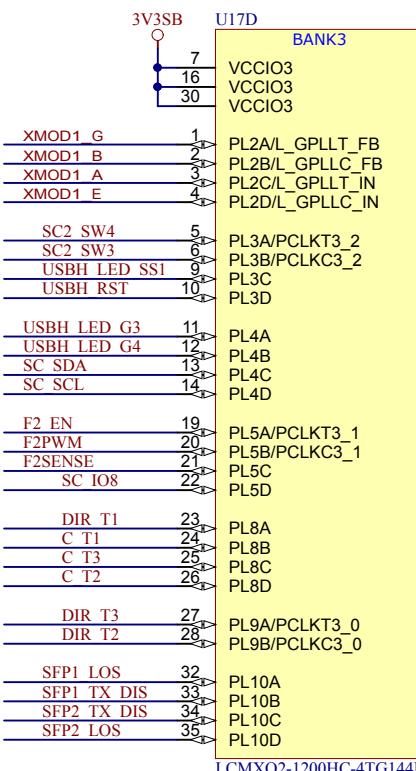
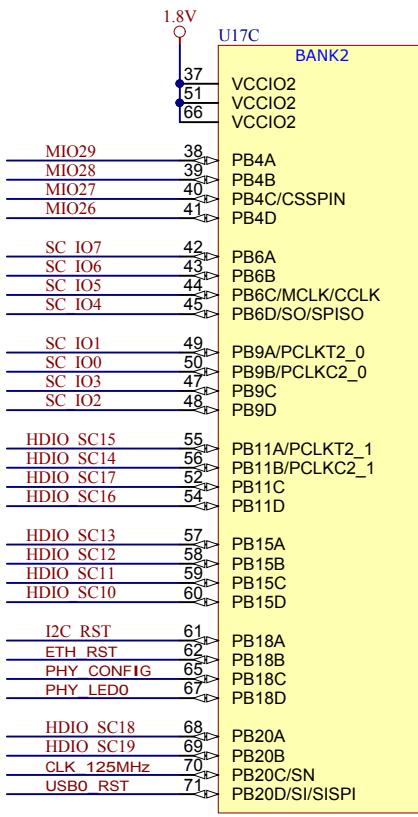
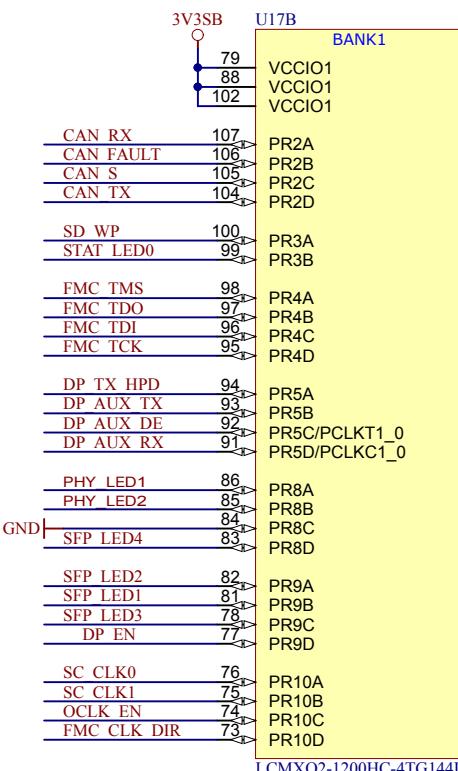
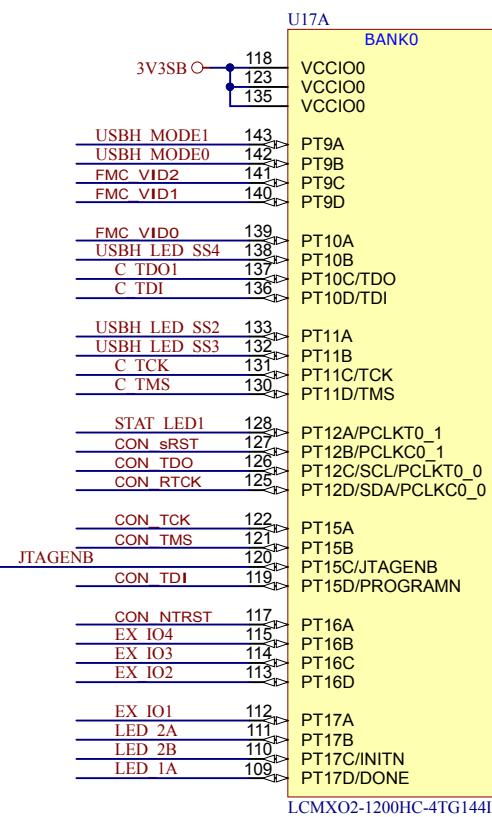
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Pin 84 PR8C "GND": REV04



Title: TEBF0808 - CPLD

A4 Number: TEBF0808

Rev. 04

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Filename: SC.schdoc

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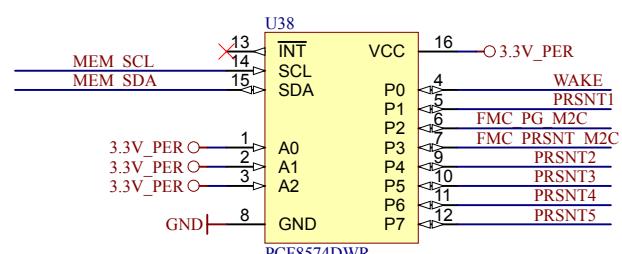
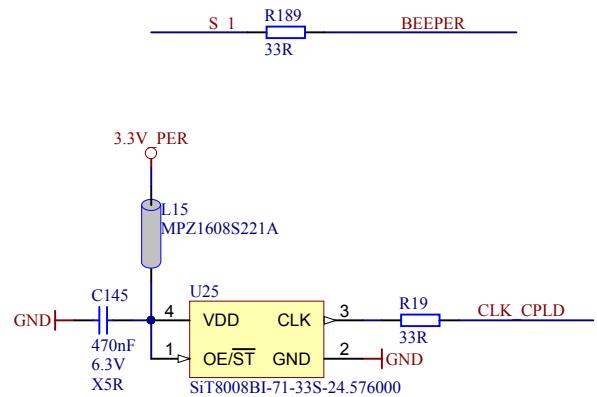
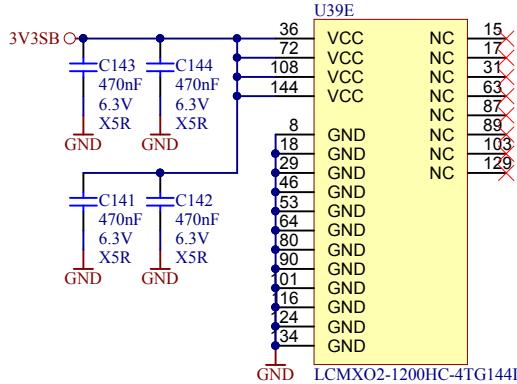
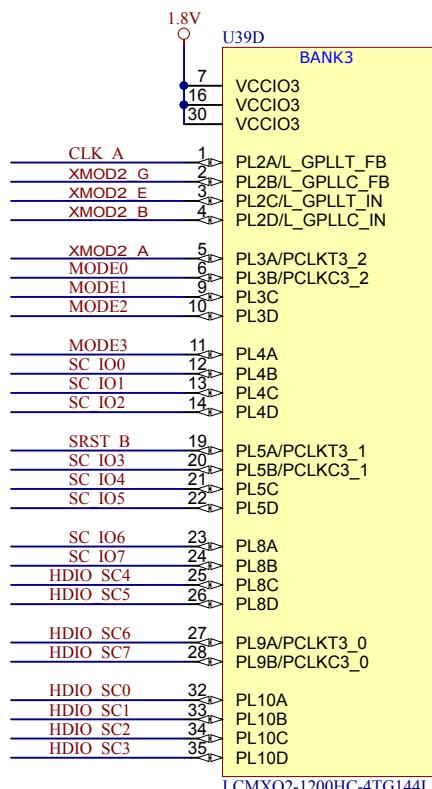
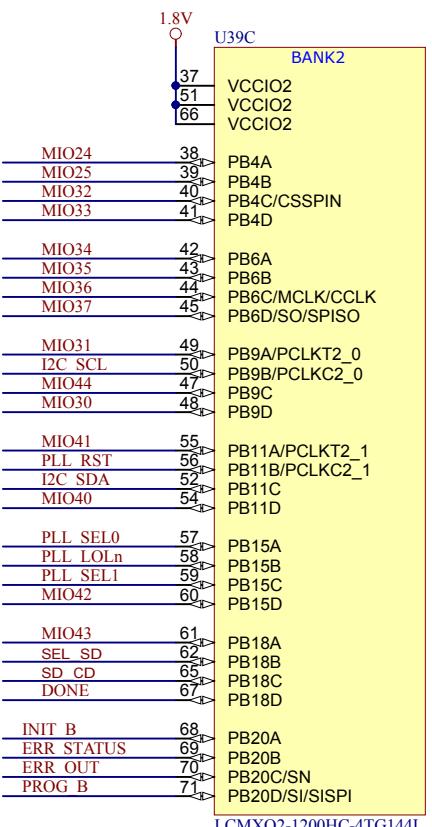
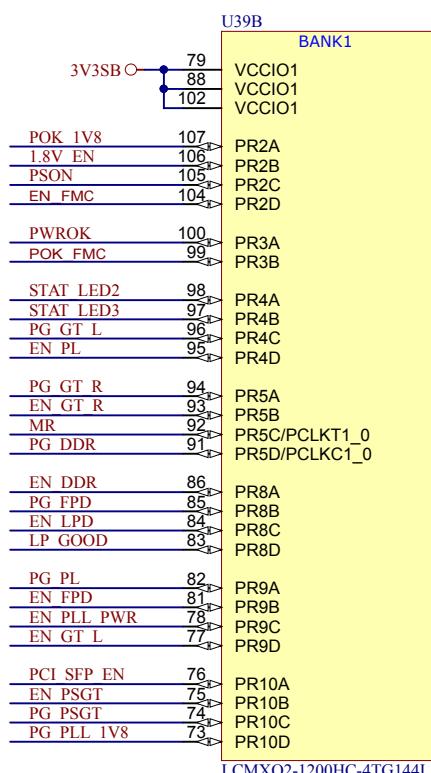
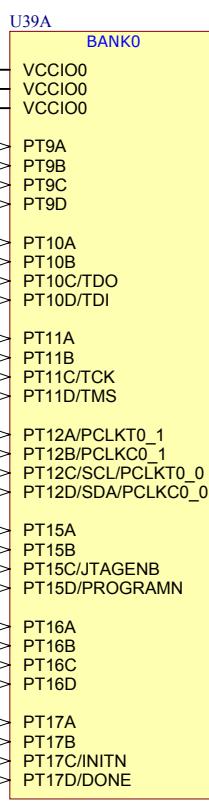
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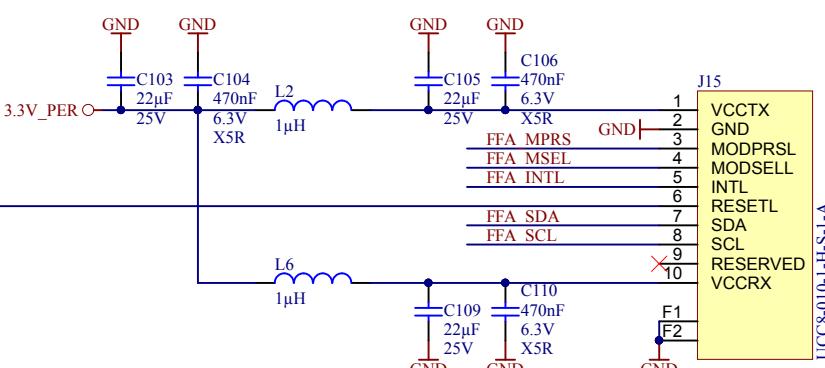
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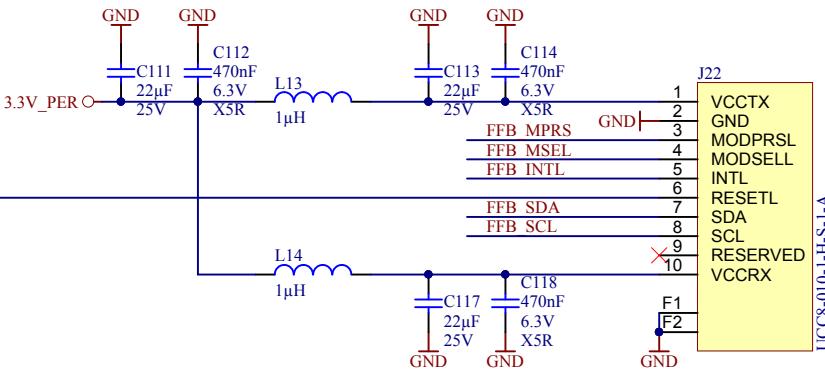
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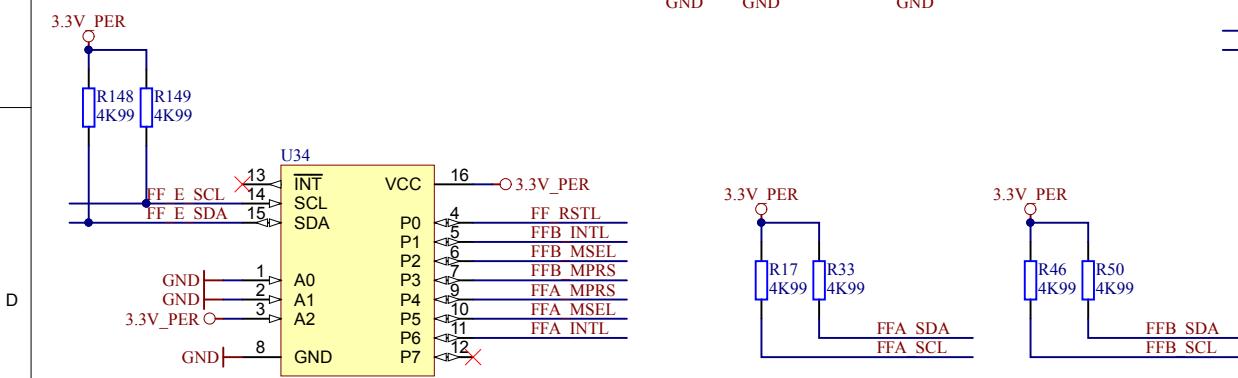
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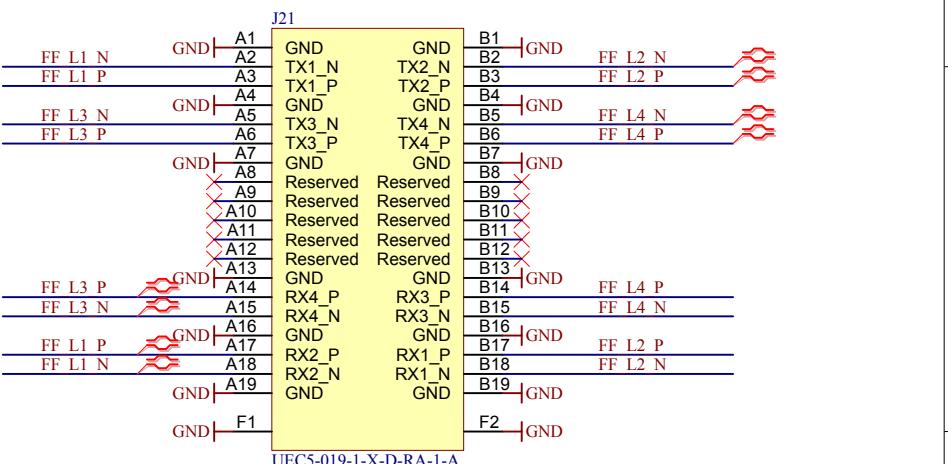
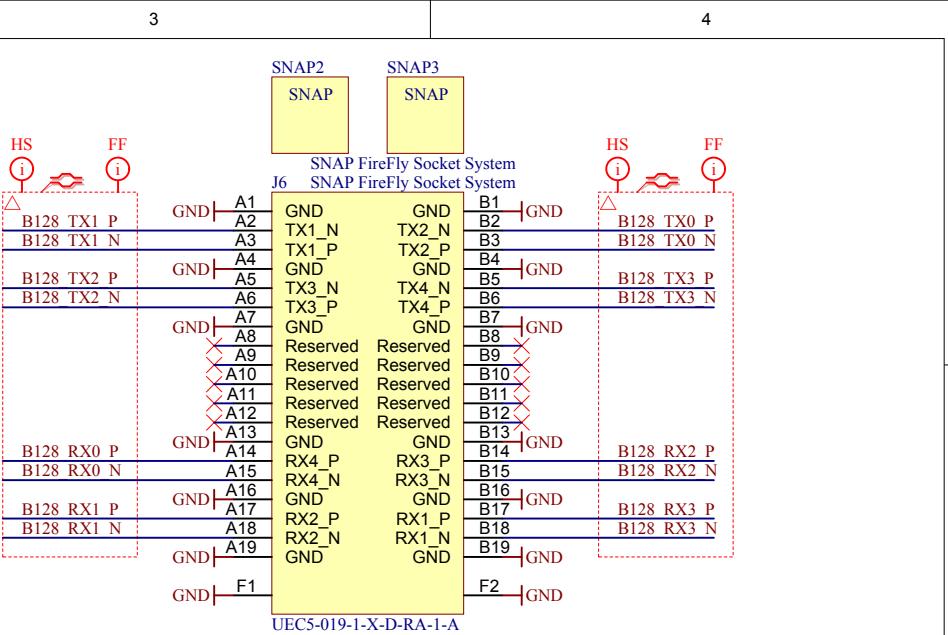
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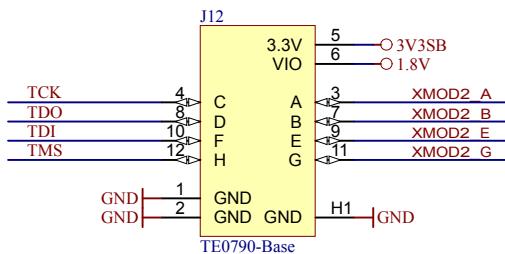


Title: TEBF0808 - FireFly		
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Filename: FireFly.SchDoc		

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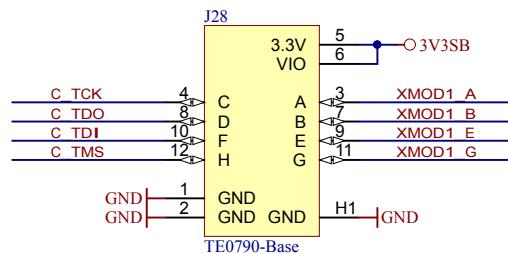
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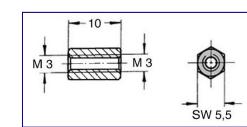
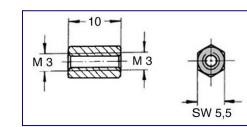
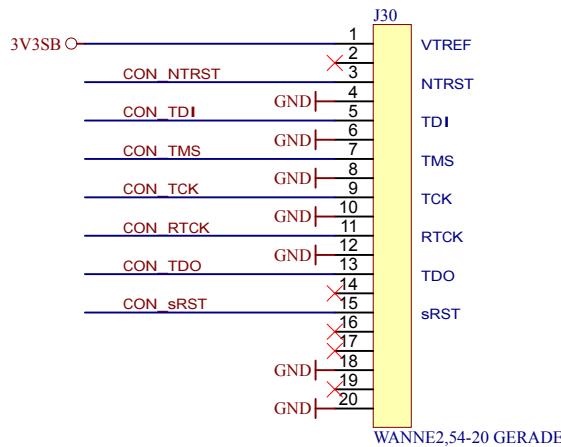
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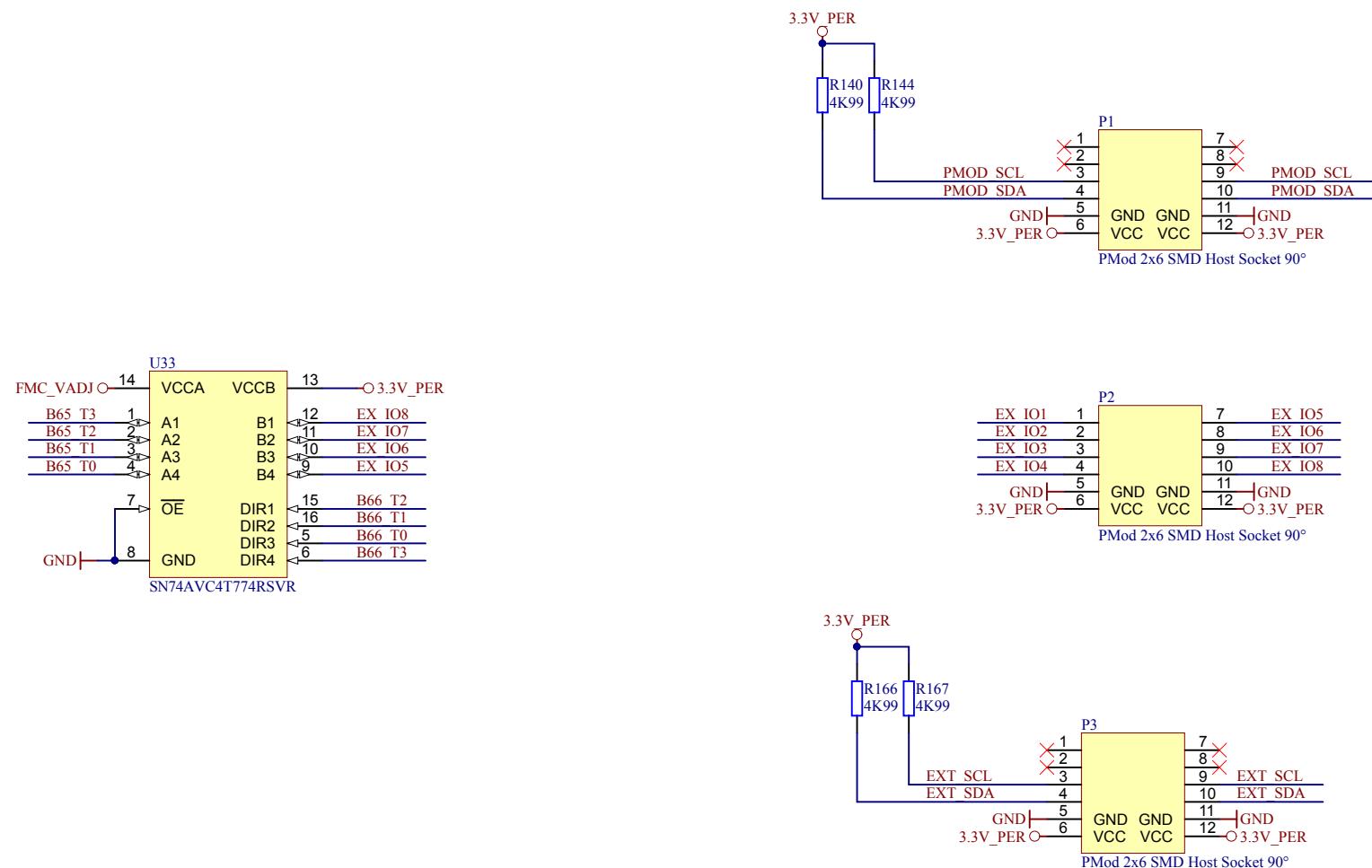
A4 | Number: TEBF0808
A

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Filename: XMOD.schdoc



Title: TEBF0808 - MISC

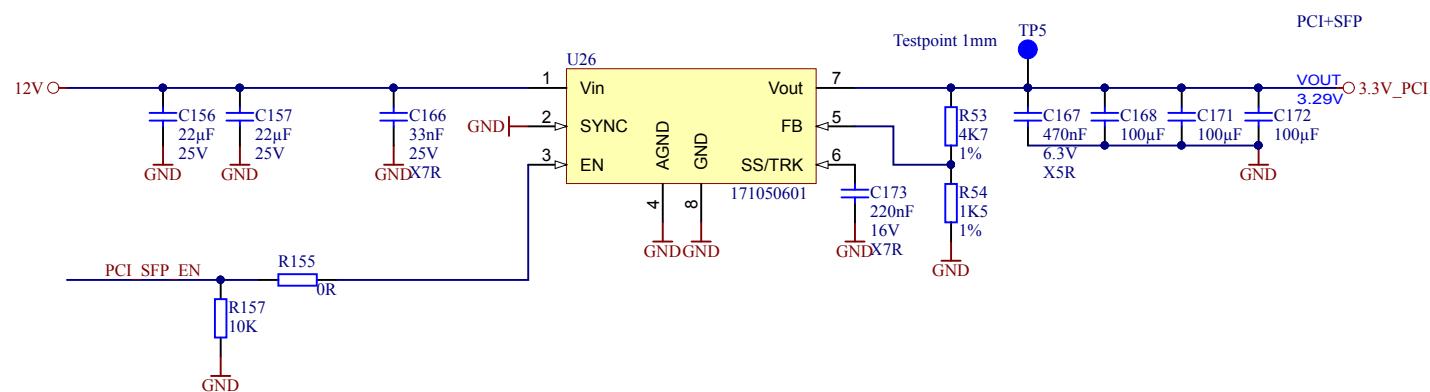
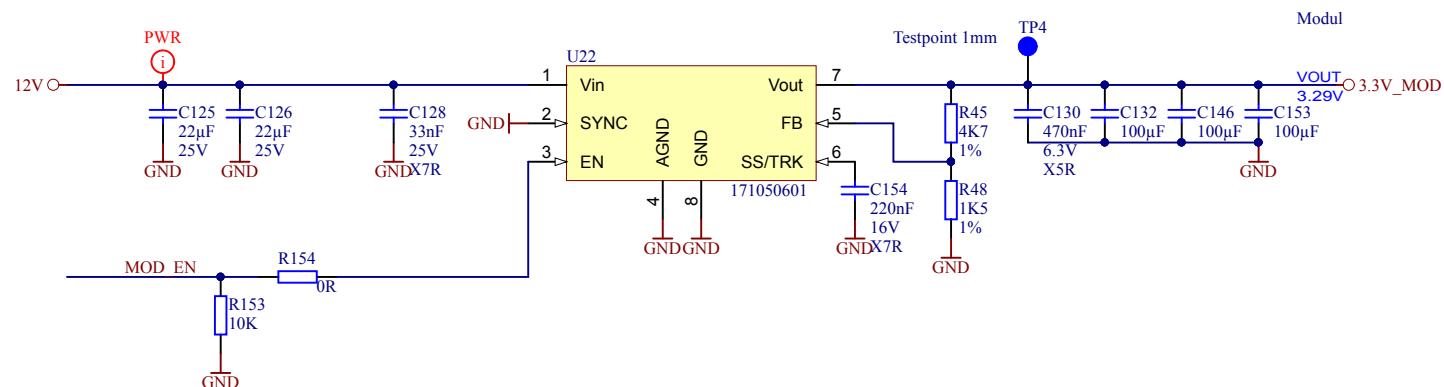
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Rev. 04

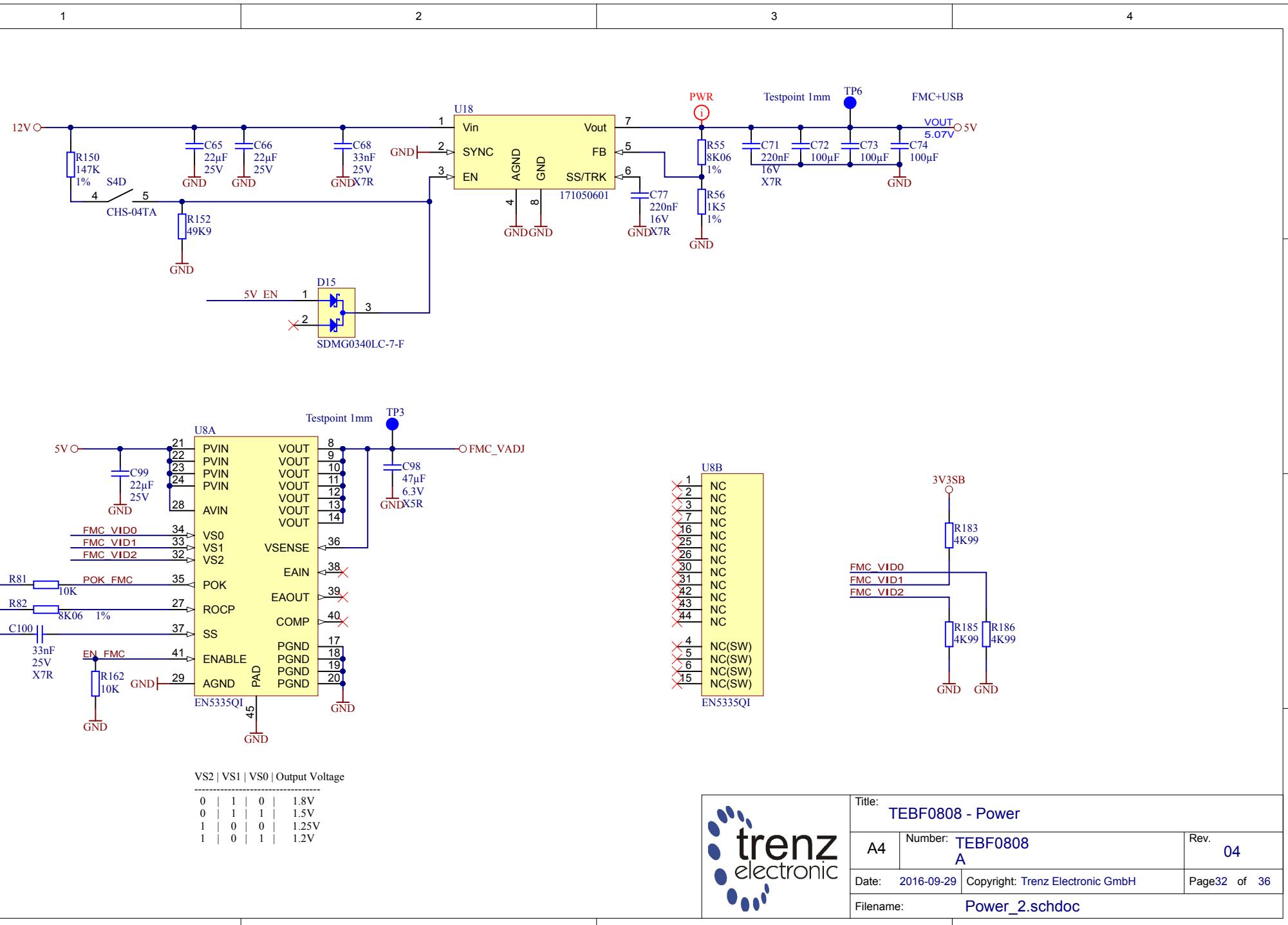
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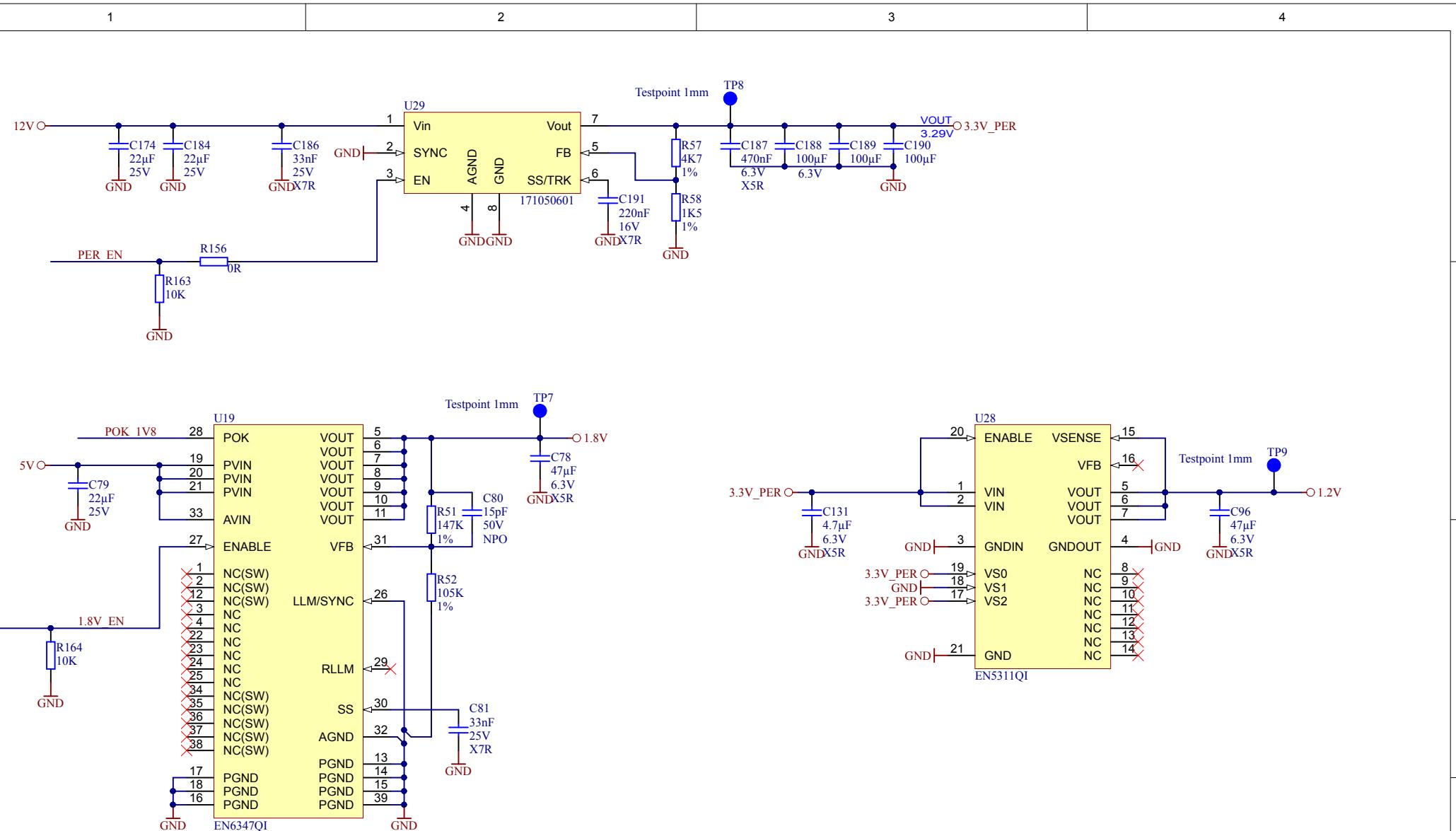
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Filename: PMODE.schdoc



Title: TEBF0808 - Power	
A4	Number: TEBF0808 A
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Filename: Power_1.schdoc	Page 31 of 36





Title: TEBF0808 - Power		Rev. 04
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Filename: Power_3.schdoc		

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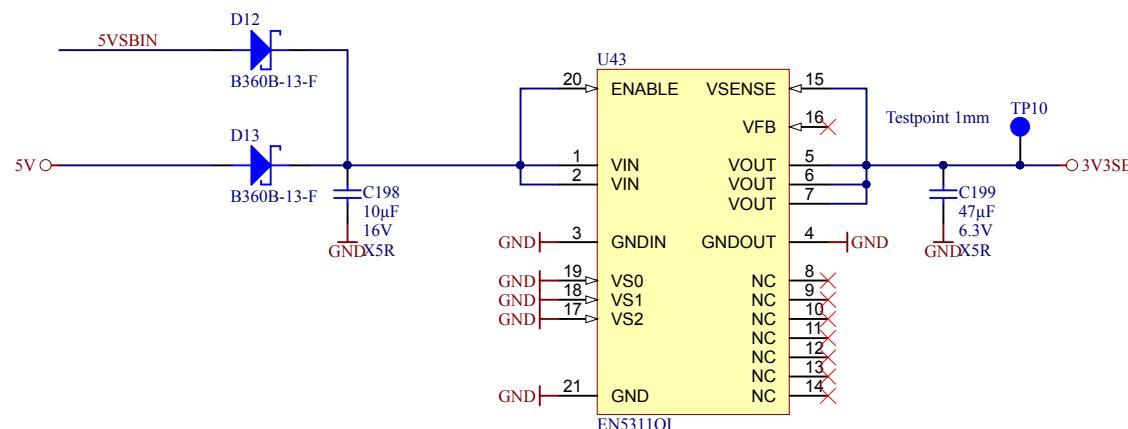
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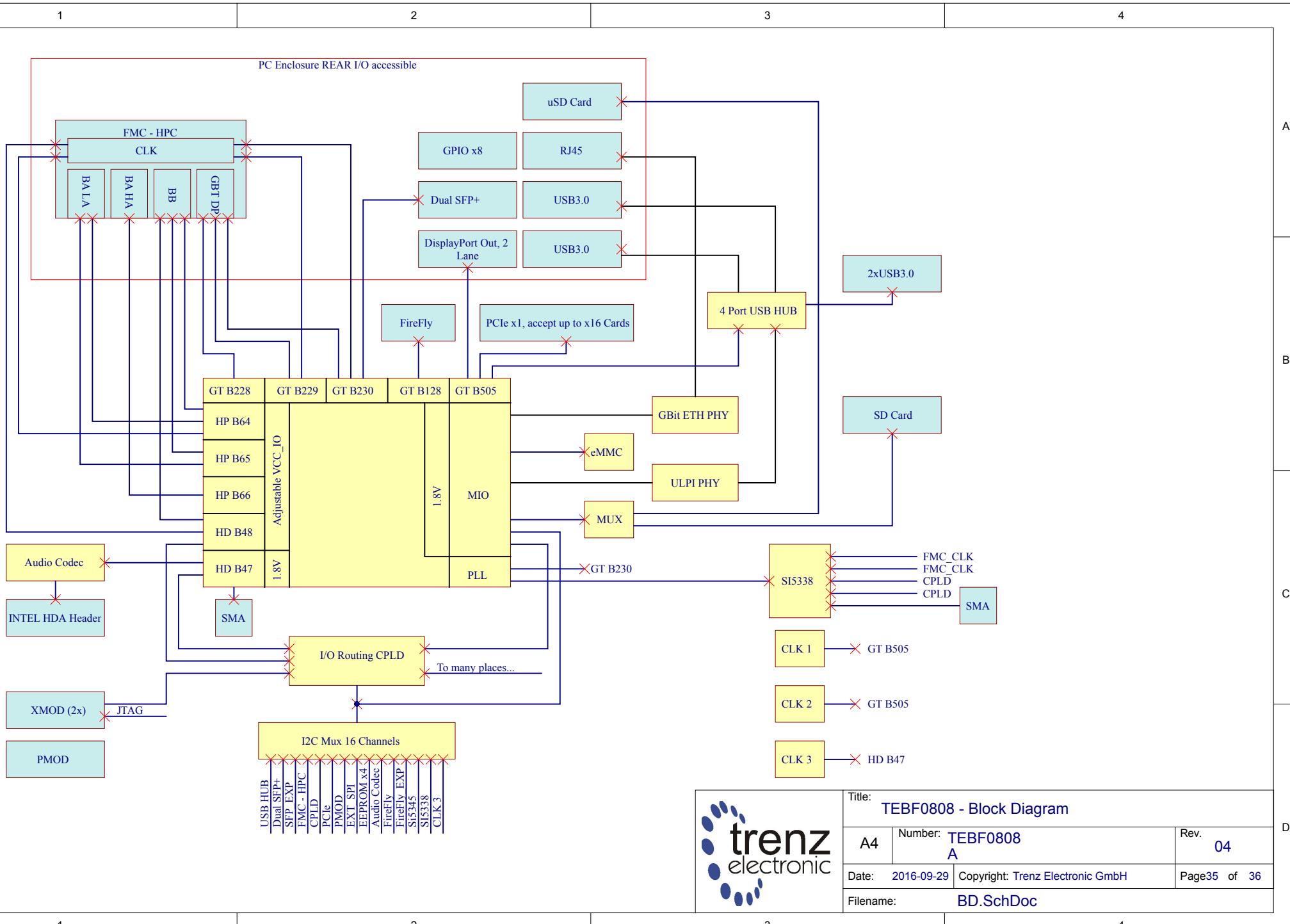
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Title: TEBF0808 - Power		
A4	Number: TEBF0808 A	Rev. 04
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Filename: Power_4.schdoc		



REV03

- 1) Added 2.5V option for clock source U45
- 2) Net B64_TO terminated via 240 Ohm resistor to GND.
- 3) Nets B64_T1..T3 connected to CPLD
- 4) Nets SFP#_LOS and SFP#TX_DIS connected to CPLD (# = 1..2)
- 5) Added 4-pin connector for FAN2 (12V). Added high side switch like option for FAN without control signals.
- 6) Added net SC_IO8 between CPLD1 and CPLD2 (Bank 3V3_SB)
- 7) Deleted USB3.0 port (J18)
- 8) Port USB2.0 replaced to USB3.0 (J8, front panel)
- 9) Replaced USB HUB USB5537B to CYUSB3324 (U4)

REV04:

- 1) P and N lanes swapped in FMC clock signal pairs B228_CLK0 and B229_CLK0
 - 2) Added additional FAN connector (J35)
 - 3) New control circuit for external beeper
 - 4) Layout and routing improvements
- 10.08.2018:
- 5) Resistors R130-131, R173-174 replaced from 4.99k to 2k
 - 26.03.2019
 - 6) VY: Resistor R65 replaced from 8.06k to 1k (assambly variant "A")
 - 7) VY: Page 21: EEPROM I2C address correction
 - 8) VY: Clock source U6 (100Mhz) polulated (assambly variant "A")

A

A

B

B

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Title: TEBF0808 - Revision history		
A4	Number: TEBF0808 A	Rev. 04
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Filename: Revision_Changes.SchDoc		