Carefull, this is a view from TOP !

CONNECTION TABLE

	PIN
sc_spypad_0	V3
shiftreg_spy_0	V2
ccff_head	U2
sc_tail	T3
cout_spypad_0	U1
cc_spypad_2	R3
cc_spypad_0	P2
GPIO6	N3
GPIO7	P1
GPIO8	M3
GPIO9	M4
GPIO10	M1
GPIO11	L1
lut5_out_0	B4
lut5_out_1	B5
Reset	A5
clk	B6
GPIO5	C7
GPIO4	B7
GPIO3	A6
GPIO2	C9
GPIO1	C10
GPIO0	A8
Test_en	D11
cc_spypad_1	A10
perf_spypad_0	A11
lut6_out_0	C12
lut4_out_0	E22
lut4_out_1	F21
lut4_out_2	F22
lut4_out_3	G21
sc_head	H20
ccff_tail	H22
GPIO23	J21
GPIO22	L20
GPIO21	L19
GPIO20	L22
GPIO19	M22
GPIO18	M19
GPIO12	X18
GPIO13	Y19
GPIO14	Y18
GPIO15	Y17
GPIO16	Y16
GPIO17	Z17
prog_clk	Z14
pReset	X14
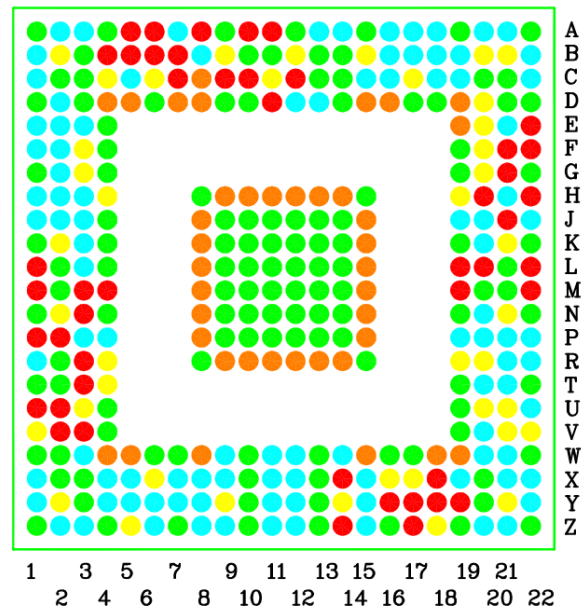
Unconnected Pins

IO's

PVDD pins

VDD pins

Ground pins