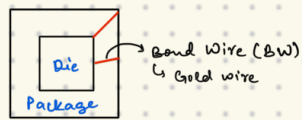


Course Details:

- 1) Intro to VLSI Design
- 2) CMOS Inverter
- 3) Multistage Logic Design & Optimisation
- 4) Other Logic Design
- 5) Intro to System Design using HDL

PCB Design:



- VLSI Design :
- 1) Fundamentals of Design
 - 2) Method
 - 3) Tools

Grading Plan:

- Assignment → 3/4 → 10%
- Course Project → 20%
- Quiz 1, 2 → 20%
- Midsem → 20%
- Endsem → 25%