

- · Need LCO driver board?
- · More Ul devices?
- · Additional supporting circuity for K?

Power Draw

Battery: Is LiPo -> 3.7 V; 500 mAh

Torget battery life: 3-5 hrs

L Acceptable Aug. Current Draw: 100-160 mA

Pegulator Efficiency:

Microcontroller:

MU:

Screen:

G-PS:

Lora Moduk:

RX: 12 mA

+10 dBm: 120 mA +17 dBm: 87 mA +13 dBm: 29 mA

+7dBm: 20 mA

Sleep: 0.2 mA Idle/Standley: 1.5 mA wlo idle mode

Let n: # paired devices

 $I_{avg} : \frac{1}{n} I_{Tx} + \frac{N-1}{n} I_{RX}$

For 3 devices: n=3

Ing = 3(120 mA) + \frac{2}{3}(12mA)
= 48mA @ ADDM

w/ idle mole

Worst Core Time on Air: 400 ms

Max n: 10

Ing, active = 48 mA

Tactive = N x To A = 10 x 400 mg : 45

Tidle = Update Perist - Tootice

= 605-45: 565

I aug: I petive + Tractive + Title > Uplate period

= 48 mA × 4 + 1.5 mA × 60 = 4.6 mA

ncu Selection

	STM32 FYOIRCTGTR	5 FM 32 L476 RETG	E5P32 S3-Wrown-1-N8	ESP32 C3-WROUM-02-44	RP2040
\$	\$5.85	\$6.34	\$3.2	\$\ \\ \.O\U	\$1.00
RAM	641<	128k	512 K	400 K	264 K
FLash	256k	Si2 k	8 M	411	· ·
Freq Light Sleep	84 Mhz 2.4 MA	80 Mhz 120 nA	240 M(+2 8 MA	160 MHz 130 mA	133 MH2
Typ 10 Com SPI 1° C W:F:[B	4	100 MA / MItz 114 3 3	~ 208 MA/Mtz 45(Pagr-h) 2 2	175 m A/MH2 16 3 2	280 nA/Mmz 30
•					A Peg. Ext.

6-Plo / Port Map

IMU:

VCC

SCL / SCLK : 0

SDA / SDI

EDAL

ELL

AD0/500

INT : InHerrypt DIO

MCS: Chipsel

FSYNC:

UI:

Button x2

Screen:

RST

CS

OO

SOA

SCL:0

GND

VCC

Lon:

VIN

GND

ĒΝ

GO: Interrypt

Sck : 0

Miso

MOS/

CS

RST

CPS

VCC

GND

TX UART