

# Sanchit Krishna

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## EDUCATION

### **BITS PILANI, GOA CAMPUS**

B.E. IN ELECTRONICS AND  
COMMUNICATION ENGINEERING  
Aug-2017 - Aug 2021 | Goa  
Cum. GPA: 8.29 / 10.0

### **B.G.K.V (CBSE)**

Grad. May 2017 | Kolkata, India  
XII : 91.6 (2017) | X : 10 CGPA (2015)

## LINKS

LinkedIn: **Sanchit Krishna**  
Codeforces: **NMOS**  
Codechef: **sutta\_time**

## COURSEWORK

### **UNDERGRADUATE**

Computer Programming  
Object Oriented Programming  
Discrete Mathematics  
Machine Learning  
Digital Image Processing  
Microprocessors  
Computer Architecture

## SKILLS

### **PROGRAMMING**

Intermediate:

C++

Familiar:

Java • Python • Typescript • Assembly •  
Verilog • Matlab • C

## POR

First Degree Teaching Assistant  
(FDTA)

Analog Electronics

Academic Assistance Program

Mechanics Oscillations and Waves

## WORK EXPERIENCE

### **EDGEVERVE | MEMBER OF TECHNICAL STAFF**

Aug 2021-Present

Working on the AssistEdge product in the Discover engineering team.

### **HONO.AI | SOFTWARE ENGINEERING INTERN**

July 2020 - Dec 2020

Worked with Angular and Loopback framework to develop a web app which showed consolidated information for all departments.

### **WORLDQUANT | VIRTUAL RESEARCH CONSULTANT**

Jan 2019 – Nov 2019

Developed mathematical expressions called alphas to categorise investments in stocks.

### **ANALOGICS TECH | SUMMER INTERN**

May 2019 – Jul 2019

Worked on LoRaWAN based microcontrollers to enable communication between the gateway and the nodes for a domestic smart gas meter product.

## COURSE PROJECTS

### **MACHINE LEARNING, Apr 2021**

- COVID-19 active cases prediction for London: Used support vector regression and neural networks independently to predict active COVID-19 cases in London.
- Sports Analytics: Used logistic regression for binary classification of the outcome of a volleyball game.

### **COMPUTER ARCHITECTURE, Jan 2021 - May 2021**

- Implemented ripple adder using verilog.
- Implemented IEEE-754 single precision floating point adder using verilog.
- Implemented MIPS single cycle data path using verilog.

### **DIGITAL IMAGE PROCESSING, Oct 2019 – Nov 2019**

LIME: Low-Light Image Enhancement via Illumination Map Estimation: Implemented a research paper on low light image processing and enhancement on MATLAB and further designed a method to get a great output using filters.