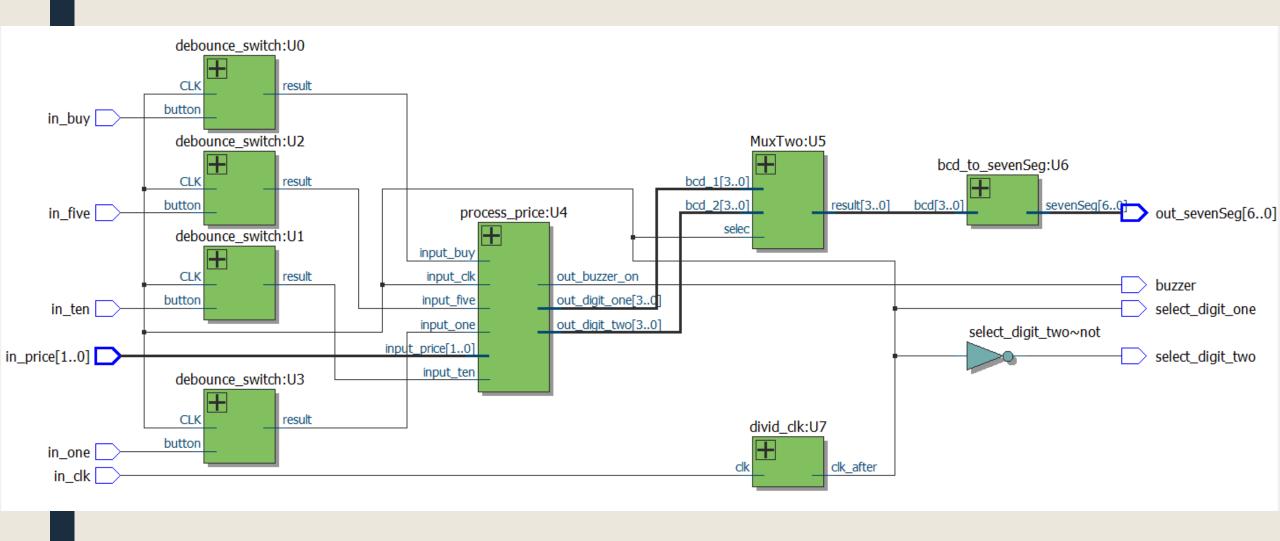
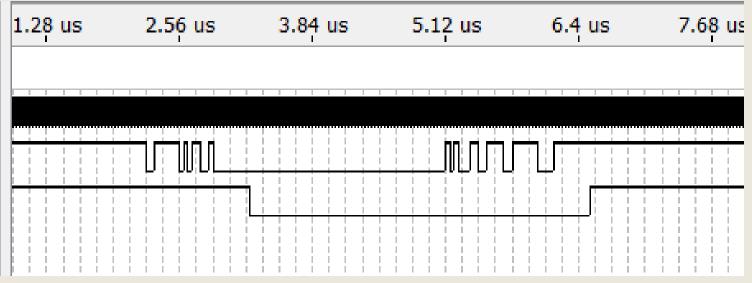
LOGIC DESIGN OF DIGITAL SYSTEMS

Mini Project : เดรื่องขายสินด้าอัตโนมัติ

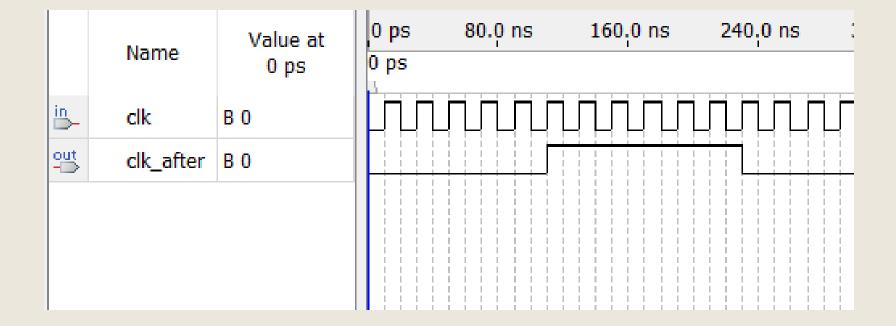


debounce_switch

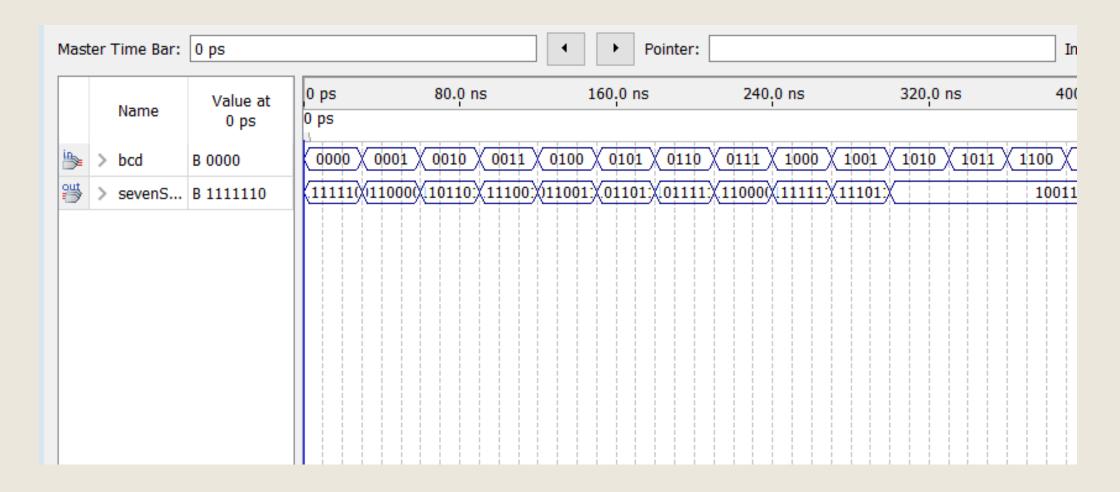
| | Name | Value at 0 ps |
|----------|--------|------------------|
| in_ | CLK | B 0 |
| in_ | button | B 1 |
| out - | result | B 0 |
| | | |



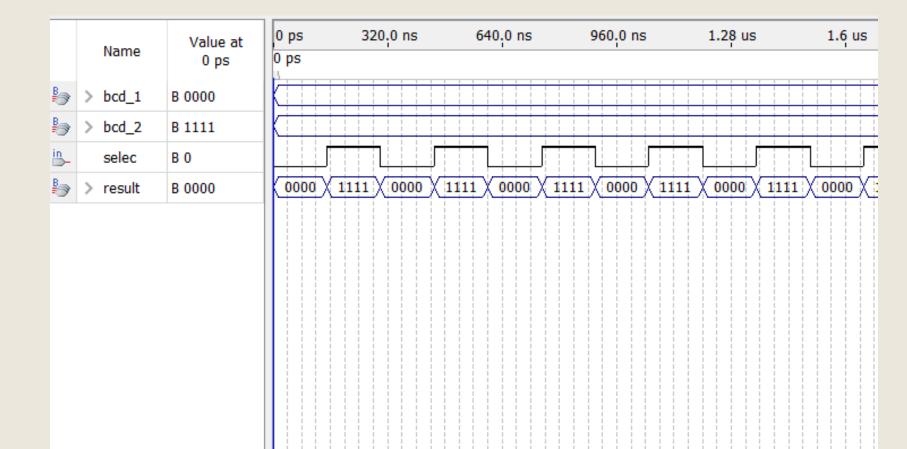
divid_clk:



bcd_to_sevenSeg



MuxTwo



process_price

