

AstraKernel Documentation

By Chris Dedman

SandboxScience @ May 2025

Contents

Table of Contents	1
Preface	2
1 Introduction	3
I. Getting Started	3
A. Prerequisites	4

Preface

AstraKernel is a minimal, experimental operating system kernel written in modern C and ARM assembly. Designed to run on QEMU's VersatilePB (ARM926EJ-S) emulated platform, AstraKernel serves as a practical and approachable foundation for learning and experimenting with core operating system concepts.

This project was developed with a focus on clarity, simplicity, and educational value. Rather than attempting to re-create the complexity of established operating systems, AstraKernel goal is to strip away unnecessary abstractions and present a clean, understandable codebase for anyone interested in the "bare metal" foundations of computing.

Through hands-on implementation of kernel bootstrapping, direct hardware communication, and basic user interaction, AstraKernel demonstrates how fundamental OS components come together. The project showcases how modern C best practices can be utilized in a systems programming context to create code that is maintainable, portable, and robust while still being accessible to those new to kernel development. The design of AstraKernel emphasizes modularity and extensibility, allowing developers to easily add new features or modify existing ones. This makes it ideal for educational purposes, as it provides a clear structure that can be followed and built upon.

It is my hope that AstraKernel will not only serve as a stepping stone for those wishing to understand kernel development, but also inspire curiosity and confidence in exploring lower-level aspects of computer systems.

Chapter 1

Introduction

I. Getting Started

AstraKernel begins its life in a small bootstrap routine, written in ARM assembly, that prepares the processor's state before passing control to the main C kernel. This bootstrap code is responsible for setting up the stack pointer, clearing the uninitialized data section (`.bss`), and ensuring a clean environment for the kernel's entry point.

Below is the initial assembly code that executes at startup:

```
1  .section .text
2  .global _start
3
4  _start:
5      // Set up the stack pointer
6      LDR sp, =_estack
7      BIC sp, sp, #7
8
9      // Zero the .bss section
10     LDR R0, =__bss_start // Start address (symbol from linker script)
11     LDR R1, =__bss_end   // End address (symbol from linker script)
12     MOV R2, #0           // init zero-value for BSS clearing
13
14 zero_bss:
```

```

15    // Check if we are done zeroing the BSS
16    CMP R0, R1          // Compare current address to end
17    BGE bss_done        // If done, skip zeroing
18    STR R2, [R0], #4    // Store zero at [r0], increment r0 by 4
19    B zero_bss
20
21 bss_done:
22    // Call kernel_main function
23    BL kernel_main
24
25 hang:
26    // Halt if kernel_main returns (should not happen)
27    B hang              // Infinite loop

```

Listing 1.1: Initial bootstrap code for AstraKernel.

This startup sequence is the essential first step for any kernel, ensuring the CPU is properly initialized and memory is in a known state before higher-level code takes over. Once these preparations are complete, the `kernel_main` function from `kernel/kernel.c` is called, marking the transition from low-level assembly to the C code that forms the core of AstraKernel.

A. PREREQUISITES

Before you can build and run AstraKernel, please ensure you have the following tools installed on your system:

- **ARM Cross-Compiler:** A cross-compiler targeting ARM is required to build the kernel. It is recommended to use `arm-none-eabi-gcc`, `arm-none-eabi-ld`, and `arm-none-eabi-objcopy` for ARM926EJ-S, which is the target architecture for AstraKernel.
 - Example installation: `arm-none-eabi-xxx` (available via package managers such as `brew`, `apt`, or direct download from ARM’s website).
- **QEMU Emulator:** QEMU is used to emulate the ARM VersatilePB (ARM926EJ-S) platform for kernel development and testing.
 - Ensure your QEMU installation supports the `versatilepb` machine.

- Example installation: `qemu-system-arm` via `qemu` <https://www.qemu.org/download/>.
- **Build Tools:** Standard build tools such as `make` are required to compile the kernel.
 - Example installation: `make` (available via package managers such as `brew`, `apt`, or direct download <https://www.gnu.org/software/make/#download>).

For best results, ensure all tools are up-to-date. Consult the official documentation of each tool for installation instructions on your operating system.

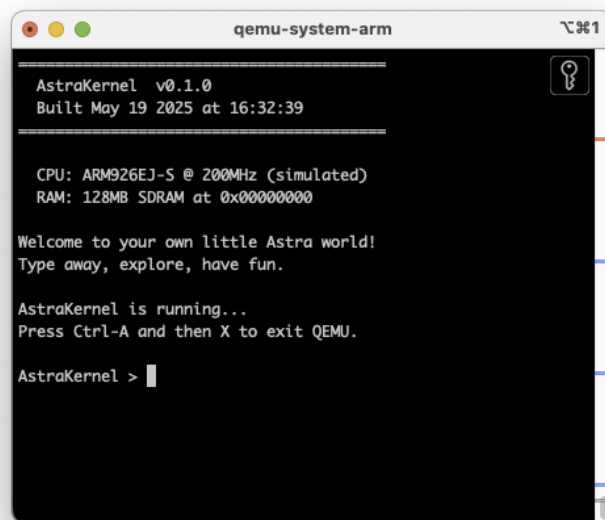


Figure 1.1: AstraKernel booted in QEMU.