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|  |  | SRIMANNARAYANA MIRYALA  Test Engineer |
| Profile Iam a professional post silicon validation engineer, responsible for  documenting, executing and reporting the overall functional test plan and verification strategy for a set of intel chips and Supporting the debug of PCIe PHY IP issues found during post-silicon, bring-up validation, Conduct system performance testing to ensure system reliability, capacity and scalability. Work with testing team to develop performance test plans and cases. Analyze test results and coordinate with development teams for bug fixes   Contact PHONE:  +91-9396251545  ADRESS:  HYDERABAD, INDIA,5000085  EMAIL:  miryalasrimannarayana@outlook.com Hobbies SOCIAL NETWORKING  LISTENING to MUSIC  DIY in ARDUINO and raspberry-pi |  | WORK EXPERIENCE[LOCUZ enterprise solutions](intel corporation) [Test-engineer] [11/2019]–[present]   * Functional and performance validation of discrete and integrated graphic chip sets(ADL,TGL,PVC,ICX) * Performance data analyzing and data reporting to concern team. * Regression testing. * First level debug and bug fixing.  EDUCATIONSRTIST (jntu-h)[B.TECH,ELECTRONICS] [0/2014 – [08/2018]   * Secured 73% in overall academics.   **Gowthami junior college [INTER, state board of intermediate]**  [05/2012]-[07-2014]   * Secured 90% in overall academics.   **BOARD OF SECONDARY EDUCATION [S.S.C]**  [2012]   * Secured 95% in overall academics.   **ACADEMIC PROJECTS**   * ADL (desktop, laptop) PVC(server) * Firefighting robot * Web tracking of parking slots using ESP8266 module * GSM BASED vehicle accident alert system   **PERSONAL TRAITS**   * Hardworking * Responsible to the work involved * Simplicity * Creative * Good communication skills |

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| TECHNICAL SKILLs C, Basic C++, Core java, python  Windows Linux.  Git hub  Microcontrollers.  Arduino, raspberry pi  System bring up  API integration, Hardware integration  Communication protocol[UART i2c spi]  GPIO’s  Debugging skills  RTOS  Shell scripting  IOT  Troubleshooting  Functional validation of chipsets  Performance checks  Effective use of logic analyzers, -oscilloscopes, dediprog, ITP,DCI, lauter batch debuggers.  Basic SQL  Manual tests to automation -conversion using Graphic test automation tools , bash scripts ,python  VMware  System bring up & boot loading  Python for automation TRaININGS Trained in VECTOR INDIA on ADVANCED EMBEDDED SYSTEMS for a period of 6 months. |  | Projects 1 : post silicon validation (intel)  * Worked on power-on for Alder lake with opensource software’s use such as 3dmark,3dmark11,specviewer perf11,maple,onlinegames(Stream,Origin,Epic games etc.) * S-curve executions between ice lake and Tiger lake and tiger lake to alder lake-s.   **Project 2: industrial temperature control system using xigbee (vector)**  **Project 3 : [S.S.C]**  [2012]   * Secured 95% in overall academics.   **Project 4 :**   * Firefighting robot * Web tracking of parking slots using ESP8266 module * GSM BASED vehicle accident alert system   **Project 5:**   * Hardworking |

### DIY projects

GSM BASED NOTICE BOARD (4\*16 display)

Water dispenser using IR.

Home automation using ESP8266

Automatic plant watering system using

Moisture sensor

Water purity testing using turbidity sensor

Remote control-based home automation

HOME UPS using relays

Vehicle tracking using GPS.

Surveillance and motion detection using

Raspberry-pi

Line follower robot using L293D

### personal particulars

Father name : miryala ramesh

Date of birth : 29-08-1997

Address : H.no 7-124/3

ramannapet, yadadri,Telangana

Pin code :508113