

Sandeep Maurice Dsouza

CONTACT INFORMATION	4723 Forbes Avenue Collaborative Innovation Center 2223B Pittsburgh, Pennsylvania 15213	Phone: +1-412-801-3502 E-mail: sandeepd@andrew.cmu.edu Web: users.ece.cmu.edu/~sandeepd
EDUCATION	Carnegie Mellon University Ph.D. Candidate, Electrical and Computer Engineering – <i>Advisor</i> : Prof. Ragunathan (Raj) Rajkumar – <i>Thesis</i> : Designing Predictable Energy-, Thermal- and Time-Aware Cyber-Physical Systems Indian Institute of Technology Kharagpur Bachelor of Technology (Honors), Electronics and Electrical Communication Engineering – <i>Advisor</i> : Prof. Santanu Chattopadhyay – <i>Thesis</i> : System-Design Methodologies for Application-Specific Network-on-Chip Topologies	Pittsburgh, United States August 2015 - present Kharagpur, India July 2011 - April 2015
RESEARCH	Time-based Coordination in Geo-Distributed Cyber-Physical Systems <ul style="list-style-type: none">• Designing a geo-scale cloud-edge framework for time-based distributed coordination in cyber-physical systems, based on the notion of <i>Quality of Time</i> (QoT) and the <i>timeline</i> abstraction.<ul style="list-style-type: none">– Research published at RTSS '16, HotCloud '17 and RTAS '18.• Development of an open-source Quality of Time Stack for Linux.<ul style="list-style-type: none">– Available online: https://bitbucket.org/rose-line/qot-stack Energy and Thermal-Aware Real-Time Scheduling for Many-core Architectures <ul style="list-style-type: none">• Design and theoretical analysis of energy and thermally-efficient real-time scheduling techniques for multi-core processors and hardware accelerators such as GPUs and FPGAs.<ul style="list-style-type: none">– Research published at ECRTS '16, ECRTS '17 and RTSS '18.– Included in Carnegie Mellon University's 18-648 Real-Time Embedded Systems course-work.• Development of the <i>Inferno</i> power and thermal real-time scheduling simulation framework.<ul style="list-style-type: none">– Available online: https://github.com/sandeepsouza93/Inferno	
EXPERIENCE	Nutanix Inc. <i>Member of Technical Staff - Intern</i> <i>Advisors : Akhilesh Joshi and Heiko Koehler</i> <ul style="list-style-type: none">• <i>Time-as-a-Service</i> for Coordinated IoT Applications: Containerized micro-service architecture for distributed IoT applications performing coordinated actions using a shared notion of time.• Proof-of-concept reinforcement-learning-based dynamic vehicular traffic-management. Siemens Corporate Technology USA <i>Manufacturing and Autonomy Intern</i> <i>Advisors : Hasan Sinan Bank and Francesco Bleve</i> <ul style="list-style-type: none">• Task and Motion Planning for an Intelligent Industrial-Robotics Manufacturing System.• Containerization for Industrial Cloud and Edge. Qualcomm India <i>Engineering Intern</i> <i>Advisors : Ajay Sinha and Tushar Singhal</i> <ul style="list-style-type: none">• Development of a Fast Auto-focus Measure for Smart Devices.• JPEG Write-Engine Performance Debug. Tokyo Institute of Technology <i>Research Intern</i> <i>Advisor : Prof. Osamu Hasegawa</i> <ul style="list-style-type: none">• Determining Cause-Effect Relationships between two variables.• Image Classification using <i>Self-Organizing Incremental Neural Networks</i>.• <i>Self-Organizing Incremental Neural Network</i> based model for Time-Series Forecasting.	San Jose, CA May 2018 - August 2018 Princeton, NJ May 2017 - August 2017 Bangalore, India May 2014 - July 2014 Tokyo, Japan May 2013 - July 2013

PUBLICATIONS **Conferences**

- [C.10] S. D'souza, H. Koehler, A. Joshi and R. Rajkumar, *Quartz: Time-as-a-Service for Coordination in Geo-Distributed Systems*, under submission
- [C.9] S. D'souza and R. Rajkumar, *CycleTandem: Energy-Saving Scheduling for Real-Time Systems with Hardware Accelerators*, in IEEE Real-Time Systems Symposium (**RTSS**) 2018
- [C.8] S. D'souza and R. Rajkumar, *QuartzV: Bringing Quality of Time to Virtual Machines*, in IEEE Real-Time and Embedded Technology and Applications Symposium (**RTAS**) 2018
- [C.7] H. Bank, S. D'souza and A. Rasam, *Temporal Logic-based Autonomy for Smart Manufacturing Systems*, in North American Manufacturing Research Conference (**NAMRC**) 2018
- [C.6] S. D'souza and R. Rajkumar, *Time-based Coordination in Geo-Distributed Cyber-Physical Systems*, in USENIX Workshop on Hot Topics in Cloud Computing (**HotCloud**) 2017
- [C.5] S. D'souza and R. Rajkumar, *Thermal Implications of Energy-Saving Schedulers*, in Euromicro Conference on Real-Time Systems (**ECRTS**) 2017
- [C.4] F. Anwar*, S. D'souza*, A. Symington*, A. Dongare*, R. Rajkumar, A. Rowe and M. Srivastava, *Timeline: An Operating-System Abstraction for Time-Aware Applications*, in IEEE Real-Time Systems Symposium (**RTSS**) 2016 (*Equally contributing authors)
- [C.3] S. D'souza, A. Bhat and R. Rajkumar, *Sleep Scheduling for Energy-Savings in Multi-Core Processors*, in Euromicro Conference on Real-Time Systems (**ECRTS**) 2016
- [C.2] S. D'souza, S. Jar, M. Chakraborti, A. Chatterjee and P. Ray, *Heart rate estimation from Photoplethysmogram during Intensive Physical Exercise using Non-Parametric Bayesian Factor Analysis*, in Asilomar Conference on Signals, Systems and Computers (**ACSSC**) 2015
- [C.1] S. D'souza, S. Joshi and S. Chattopadhyay, *A Constructive Heuristic for Application Mapping onto an Express Channel based Network-on-Chip*, in International Symposium on VLSI Design and Test (**V DAT**) 2015

Journals

- [J.2] P. Mukherjee, S. D'souza and S. Chattopadhyay, *Area Constrained Performance Optimized ASNoC Synthesis with Thermal-aware White Space Allocation and Redistribution*, in Integration 2018, 60, p.167
- [J.1] S. D'souza, S. Joshi and S. Chattopadhyay, *Integrated Mapping and Synthesis Techniques for Network-on-Chip Topologies with Express Channels*, in ACM Transactions on Architecture and Code Optimization (TACO) 2016, 12 (4), p.40

RESEARCH DEMOS

- Coordinated City-Scale Traffic Management using Quartz “Time-as-a-Service”**
 - In Demo Session of IEEE Real-Time Systems Symposium (RTSS@Work) 2018
- Time-based Distributed Multi-Robot Coordination**
 - In Demo Session of NSF Cyber-Physical Systems Principal Investigators' Meeting 2017
- Synchronous Task Scheduling for Cyber-Physical Systems**
 - In Demo Session of IEEE Real-Time Systems Symposium (RTSS@Work) 2015

ACADEMIC ACHIEVEMENTS AND AWARDS

Graduate

- Recipient of the **Carnegie Institute of Technology Dean's Fellowship** (2015-2016)
- Recipient of the **Tiara Special Grant** (2015-2016)

Undergraduate

- Awarded the **Aditya Birla Group Scholarship** (2011-2015).
- Awarded the **OP Jindal Engineering and Management Scholarship** (2011-2012).
- Finished Second at **Qualcomm India Intern IdeaQuest 2014**.
- Finished First at **Intel Ideation Contest 2013**, IIT Kharagpur.
- Finished Second in the **Microsoft India Academia Accelerator Programme 2013**.

High School

- Ranked **407** in IIT Joint-Entrance Examination(IITJEE)-2011 among 485,000 examinees.
- Ranked **541** in All India Engineering Entrance Examination 2011 among 1,065,000 examinees.

TEACHING EXPERIENCE	18-648 Real-Time Embedded Systems	Fall 2017
	<i>Guest Lecturer</i>	
	– Taught lectures on rate-monotonic analysis and energy-aware real-time scheduling.	
	18-748 Wireless Sensor Networks	Spring 2017
	<i>Teaching Assistant</i>	
	– Mentored 6 teams for their course projects. – Taught a lecture on Data Storage in Sensor Networks.	
	18-648 Real-Time Embedded Systems	Fall 2016
	<i>Teaching Assistant</i>	
	– Design and grading of Real-Time OS-related programming assignments. – Taught a lecture on Real-Time support in commercial Operating Systems.	
PROFESSIONAL SERVICE	Reviewer	
	– ACM Transactions on Embedded Computing Systems	
	Mentored Students	
	– Sudarshan Mukunda Iyengar, CMU MS ECE '16, Guided Research Project	
SKILLS	<ul style="list-style-type: none"> • Programming Languages : C, C++, Python, Java • Software Packages : Robot Operating System (ROS), OpenCV, Matlab, Xilinx ISE, L^AT_EX • Virtualization : Docker, Kubernetes, QEMU-KVM • Machine Learning: Tensorflow, Keras • Hardware Description : Verilog HDL 	
GRADUATE COURSEWORK	<ul style="list-style-type: none"> • <i>Embedded Systems</i>: Real-Time Embedded Systems, Wireless Sensor Networks • <i>Distributed Systems</i>: Advanced OS and Distributed Systems, Advanced Cloud Computing, Building Reliable Distributed Systems, Analytical Performance Modeling • <i>Algorithms</i>: Machine Learning • <i>Security and Privacy</i>: Information Security, Privacy, and Policy 	
EXTRA CURRICULARS	<ul style="list-style-type: none"> • <i>Best Cadet</i> for the session 2011-2012, of 3 Bengal Technical Air Squadron, Indian Institute of Technology Kharagpur of the National Cadet Corps, India. • <i>Silver Medal</i> in General Quiz at Social and Cultural General Championships 2011-2012, Indian Institute of Technology Kharagpur. • <i>Silver Medal</i> in Foundation Day Debate 2012, Indian Institute of Technology Kharagpur. • <i>Bronze Medal</i> in Business Quiz at Technology General Championships 2012-2013, Indian Institute of Technology Kharagpur. • Participation in CBSE National Chess Championships 2004, 2005, 2006 and 2007. • <i>Board Prize Winner</i> at CBSE National Chess Championships 2007. • Completed five of seven examinations in Hindustani Classical Vocals from Akhil Bhartiya Gandharva University. 	
VOLUNTARY POSITIONS	University Leadership Student Advisory Council	Carnegie Mellon University
	<i>Member</i>	August 2017 - July 2018
	Indian Graduate Student Association	Carnegie Mellon University
	<i>President</i>	November 2016 - November 2017
	<i>Treasurer</i>	November 2015 - November 2016
	Entrepreneurship Cell	IIT Kharagpur
	<i>Associate Manager</i>	July 2012 – January 2013
	<i>Associate Member</i>	April 2012 – July 2012
REFERENCES	Available on Request	