

1. The logic gate circuit shown in the figure realizes the function.

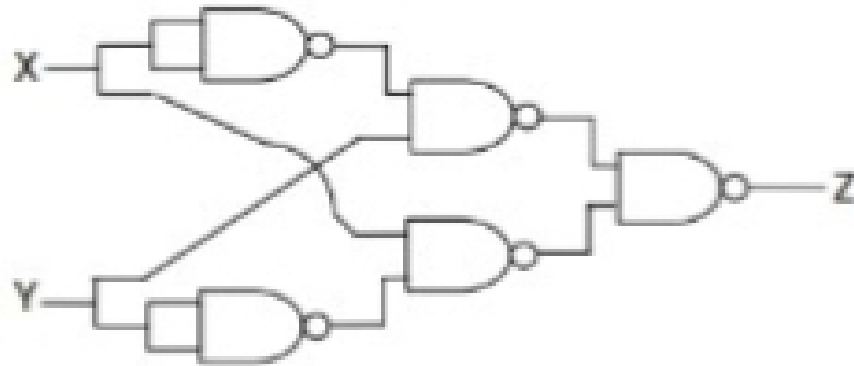


Figure 1

- (a) XOR
- (b) XNOR
- (c) Half adder
- (d) Full adder