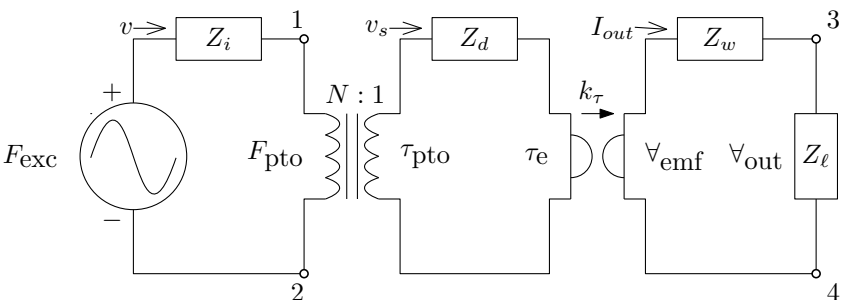
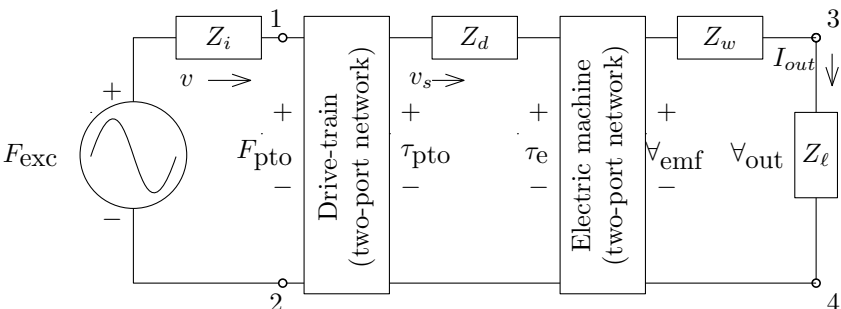
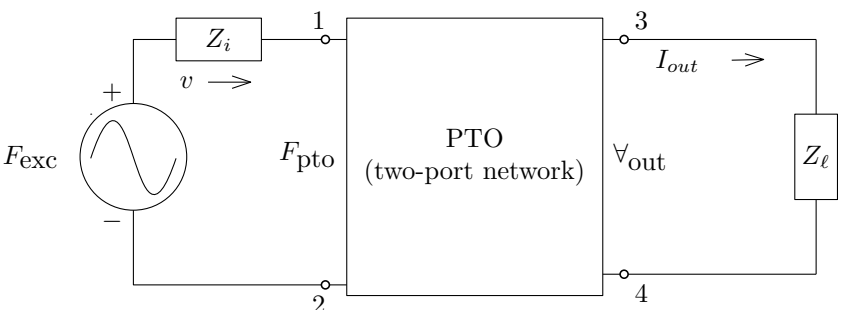
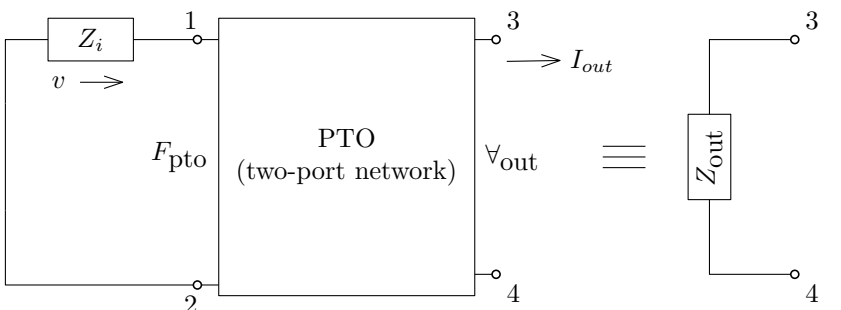
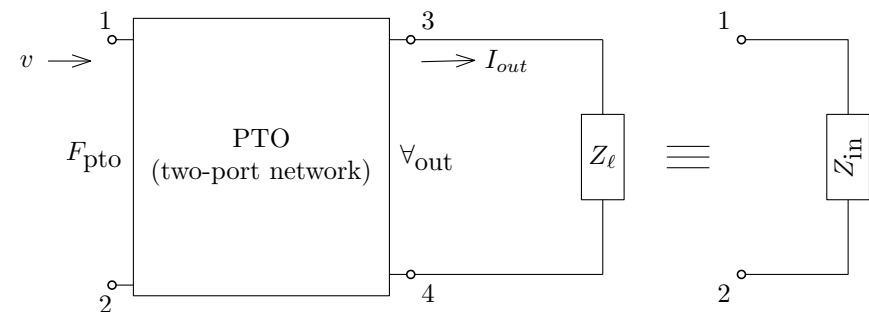


(a) Detailed**(b) Multiple two-port networks****(c) Single two-port network****(d) PTO output impedance, Z_{out}** **(e) PTO input impedance, Z_{in}** **(f) Thévenin equivalent**