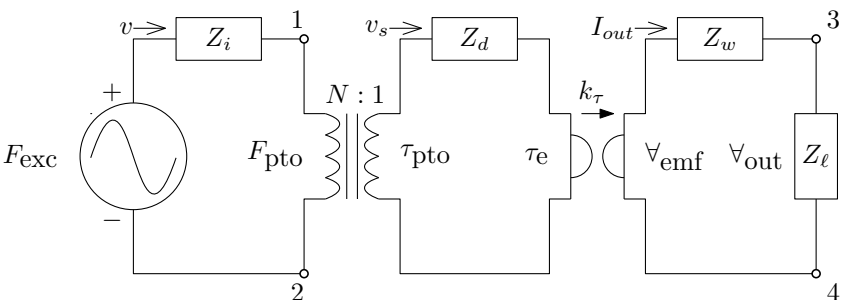
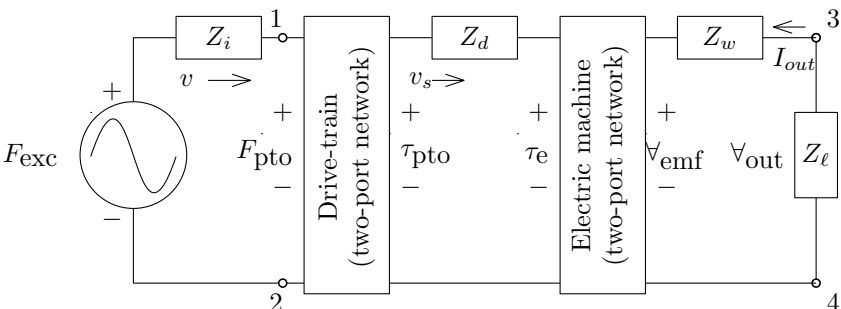
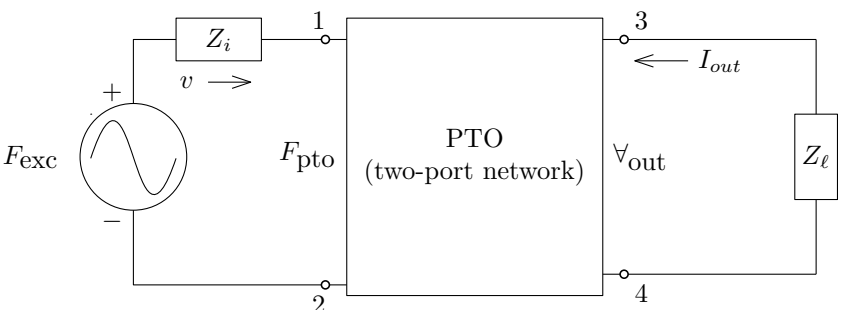
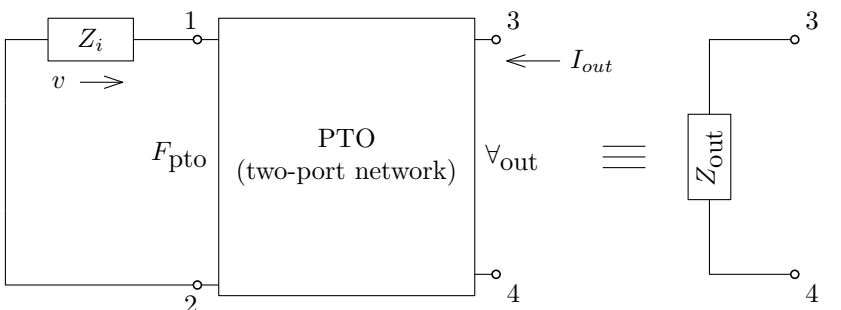
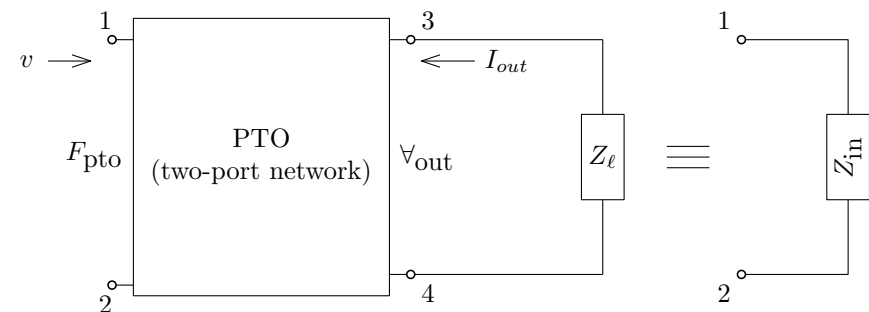


**(a) Detailed****(b) Multiple two-port networks****(c) Single two-port network****(d) PTO output impedance,  $Z_{\text{out}}$** **(e) PTO input impedance,  $Z_{\text{in}}$** **(f) Thévenin equivalent**