

## # Design of Bandgap Reference (BGR) Voltage #

- specifications
  - $V_{DD} = 3.3V$
  - $I_{DC} = 5mA$
  - $N=2$  for PTAT generation.
  - Temp.

# C<sub>TAT</sub> : Negative coefficient

$$I_0 \downarrow \begin{array}{c} \text{---} \\ \text{---} \\ \text{---} \\ \text{---} \\ \text{---} \end{array} \xrightarrow{V_D} I_0 = I_s e^{\frac{V_D}{V_T}}$$

$$\Rightarrow \frac{I_0}{I_s} = e^{\frac{V_D}{V_T}}$$

$$\Rightarrow \ln \frac{I_0}{I_s} = \frac{V_D}{V_T}$$

$$\Rightarrow \boxed{V_D = V_T \ln \left( \frac{I_0}{I_s} \right)} \quad (i)$$

$$\left[ \frac{kT}{q} \right] \leftarrow \text{weak f.n. of temp.} \quad I_s \propto T$$

So, as  $I_s \uparrow \Rightarrow \ln \left( \frac{I_0}{I_s} \right) \downarrow \Rightarrow V_D \downarrow$

# PTAT : Positive temp. coeff.

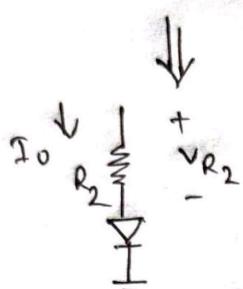
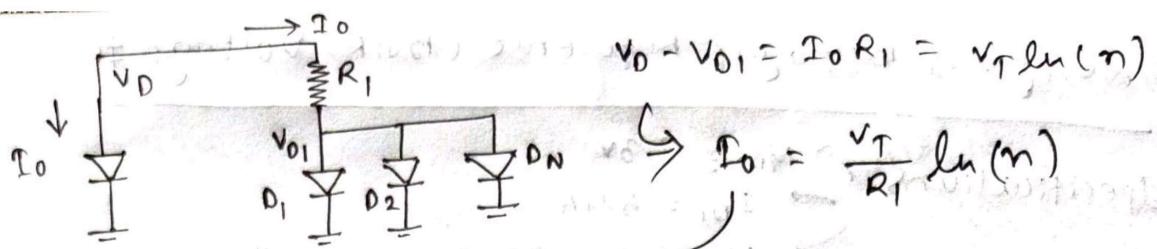
$$I_0 \downarrow \begin{array}{c} \text{---} \\ \text{---} \\ \text{---} \\ \text{---} \\ \text{---} \end{array} \xrightarrow{V_D} I_0 = nI \quad \Rightarrow V_{D1} = V_T \ln \left( \frac{I}{I_s} \right)$$

$$I \downarrow \begin{array}{c} \text{---} \\ \text{---} \\ \text{---} \\ \text{---} \\ \text{---} \end{array} \xrightarrow{I_1} I_1 \downarrow \begin{array}{c} \text{---} \\ \text{---} \\ \text{---} \\ \text{---} \\ \text{---} \end{array} \xrightarrow{I_N} I_N \downarrow \begin{array}{c} \text{---} \\ \text{---} \\ \text{---} \\ \text{---} \\ \text{---} \end{array} \xrightarrow{I} \Rightarrow V_{D1} = V_T \ln \left( \frac{I_0}{nI_s} \right) \quad (ii)$$

$$V_D - V_{D1} = V_T \ln \left( \frac{I_0}{I_s} \right) - V_T \ln \left( \frac{I_0}{nI_s} \right)$$

$$\boxed{V_D - V_{D1} = V_T \ln(n)} \quad (iii)$$

$\uparrow V_T \propto \uparrow \uparrow$  const.



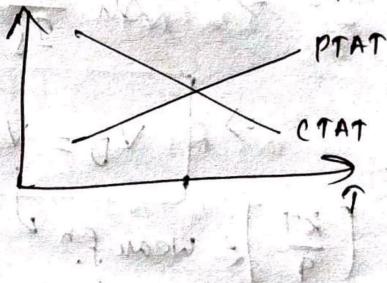
$$V_{R2} = I_0 R_2 = V_T \ln(N) \frac{R_2}{R_1}$$

$$\boxed{V_{R2} = \alpha_1 V_T} \Rightarrow \text{PTAT}$$

Separate PTAT

as  $T \uparrow \Rightarrow V \uparrow$

$$\alpha_1 = \ln(N) \frac{R_2}{R_1}$$



$$V_{ref} = \alpha_1 \text{PTAT} + \alpha_2 \text{CTAT}$$

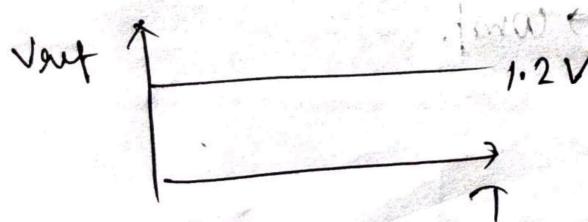
$$= \alpha_1 V_T + \alpha_2 V_D$$

$$\frac{\partial V_{net}}{\partial T} = 0 \Rightarrow \alpha_1 \frac{\partial V_T}{\partial T} + \alpha_2 \frac{\partial V_D}{\partial T} = 0$$

$$\Rightarrow \alpha_1 (85 \mu V/K) + \alpha_2 (-1.6 mV/K) = 0$$

$$\text{Assuming } \alpha_2 \approx 1, \quad \alpha_1 = \frac{1.8 mV/K}{85 \mu V/K} = 18.82$$

$$\boxed{V_{ref} = 18.82 \times 26 mV + 1 \times 0.7 V \approx 1.2 V}$$



# Calculation of register value :-

$$I_0 = 54A, N=2$$

$$V_D - V_{D1} = I_0 R_1 = V_T \ln(N)$$

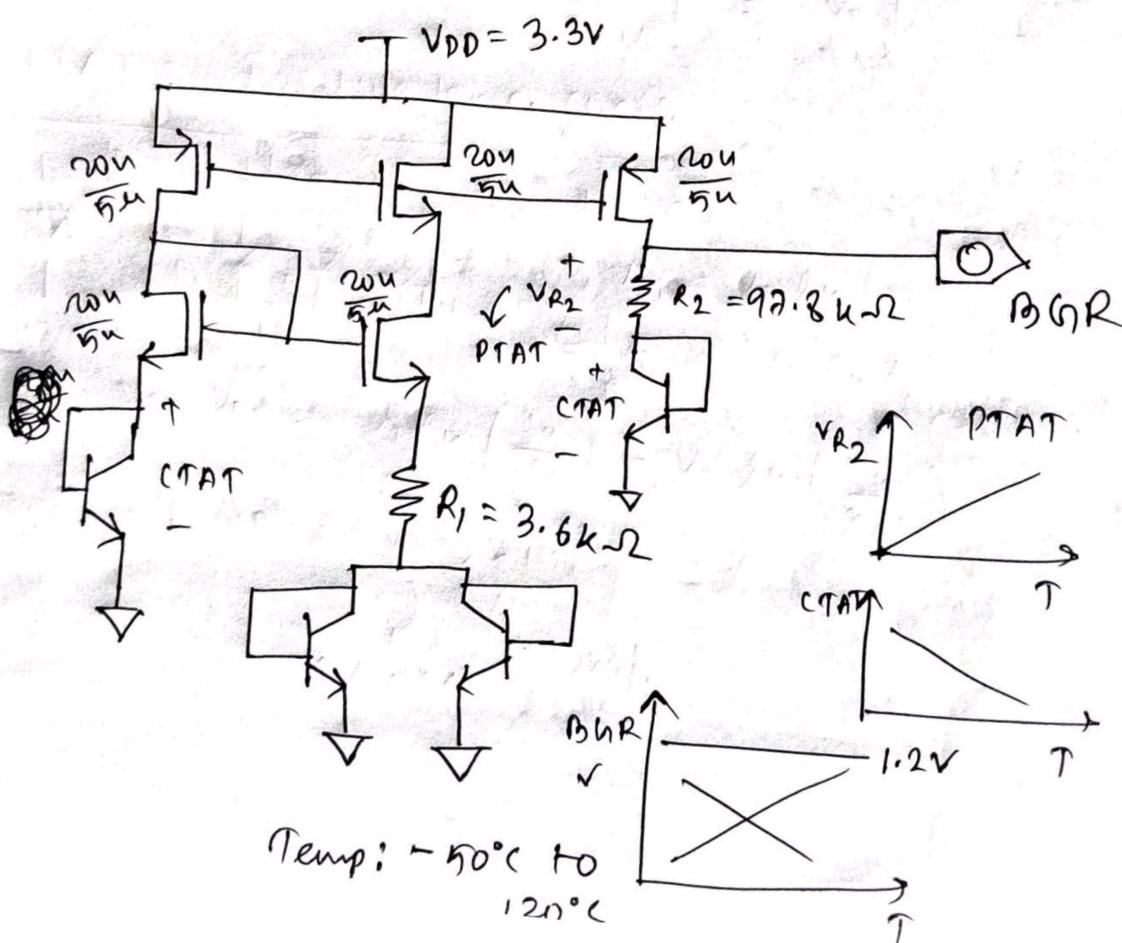
$$R_1 = \frac{26mV}{5mA} \times \ln(2)$$

$$\boxed{R_1 = 3.604k\Omega}$$

$$\alpha_1 = \frac{R_2}{R_1} \ln(N)$$

$$R_2 = \frac{\alpha_1 R_1}{\ln(N)} = \frac{18.82 \times 3.604k}{\ln(2)}$$

$$\boxed{R_2 = 97.8k\Omega}$$



Instead of pstat the label will BGR

