

The diagram shows an ATmega328-AU microcontroller (MCU) with the following connections:

- PC6(RESET/PCINT14)** connected to **RST**.
- PB6(XTAL1/TOSC1/PCINT6)** and **PB7(XTAL2/TOSC2/PCINT7)** connected to a crystal oscillator circuit.
- AREF** and **AVCC** connected to **VCC**.
- ADC7** and **ADC6** connected to **GND**.
- PD7(AIN1/PCINT23)**, **PD6(AIN0/OC0A/PCINT22)**, **PD5(T1/OC0B/PCINT21)**, **PD4(T0/XCK/PCINT20)**, **PD3(INT1/OC2B/PCINT19)**, **PD2(INT0/PCINT18)**, **PD1(TX/PCINT17)**, and **PD0(RXD/PCINT16)** connected to the ICSP header pins 1 through 8 respectively.
- The ICSP header is connected to a MOSI pin and a MUX_ENABLE pin.

[illegible]

ANALOG (I2C pins included, A4: SDA, A5: SDL)

DIGITAL (HW Interrupt pin included, D2: INT0)

DECOUPLED PWM PORT