Ming-Shiuan Tsai

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Professional Summary

A software developer with master's level electrical and computer engineering background, specializing in full-stack software skills. Project experience in both independent and group works. Seeking to leverage my knowledge and skills to bring a prospective design and assistance to the position of Software Engineer.

Skills

- **Programming Languages**: Python, HTML5, CSS, JavaScript
- **Database**: MongoDB
- **Tools, Platforms**: Github, Heroku, jQuery, React, Node.js, Postman, Robo 3T

Experience

Coding Boot Camp   *Aug. 2018 – Feb. 2019*

Georgia Tech, Atlanta, GA, USA

- Amazon Clone Page
    - Front-end code: HTML, CSS, JavaScript, jQuery
    - MongoDB, Node.js, Express implementation
    - Multiple-item purchase feature included with the implementation of Ajax and sessionStorage
- FaceBell
    - Front-end code: HTML, CSS, JavaScript, jQuery
    - MySQL, Node.js, Express implementation
    - Send the message and image of the visitors to owner to decide if the gate should be opened
- Survey App
    - Front-end code: React, HTML, CSS, JavaScript
    - MongoDB, Node.js, React, Express implementation
    - The owner is able to create new surveys, collect survey data from others, modified data and check the analysis of the data

Principal Application Engineer   *Jan. 2018 – present*

- Create data sheets and provide technical explanations to customer
- Collect and identify technical data from customer
- Provide training to new employees or customers
- Successfully solve problems of providing qualified products to customer

Research Project   *Oct. 2015 – Oct. 2017*

Purdue University, West Lafayette, IN, USA

- A Single-Chip Wireless Microelectrode Array for Neural Recording and Stimulation
    - Designed Ultra-compact and energy-efficient Low Noise Amplifier(LNA), Multiplexer(MUX).
    - Designed integrated neural amplifiers using DC clamping topology in 45 nm CMOS SOI
    - Solved input DC offset problem during neuron measurement

Education

Purdue University, Indiana, USA   *Dec. 2017*

Master of science in Electrical and Computer Engineering   **Overall GPA: 3.57/4**

- Specialize in VLSI and Integrated Circuit Design
- Courses: Computational Models and Methods, System-On-Chip Design, High-Speed Mixed-Signal IC, Embedded Systems, MOS VLSI Design, CMOS Analog IC Design,

National Tsing Hua University (NTHU), Hsinchu, Taiwan   *Jun. 2015*

Bachelor of Science in Electrical Engineering   **Overall GPA: 3.95/4.3**

- Specialize in VLSI and Integrated Circuit Design