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In reality, this course is a snapshot of a more detailed, 40 hour course on CMOS analog design found elsewhere. The target audience for this course should have some familiarity with analog circuits and integrated circuit technology. The terminology used is that found in both academia and industry.

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I.1 - INTRODUCTION. GLOBAL OBJECTIVES • Teach the analysis, modeling, simulation, and design of analog circuits implemented in CMOS technology. • Emphasis will be on the design methodology and a hierarchical approach to the subject. SPECIFIC OBJECTIVES. 1. Present an overall, uniform viewpoint of CMOS analog circuit design. 2. Achieve an understanding of analog circuit design.

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