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He has served as technical program cochair of the 1995 IEEE VLSI Signal Processing workshop and the 1996 ASAP conference, and as the general chair of the 2002 IEEE Workshop on Signal Processing Systems. He was a distinguished lecturer for the IEEE Circuits and Systems society during 1996-1998, and is serving a second term currently (2019-20).

Keshab K. Parhi - Electrical and Computer Engineering

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About the Author. Keshab K. Parhi is Edgar F. Johnson Professor of Electrical and Computer Engineering at the University of Minnesota in Minneapolis. Dr. Parhi is widely recognized for his work in the area of VLSI digital signal and image processing. In addition to publishing more than two hundred fifty papers and serving on the editorial boards of a number of professional journals

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